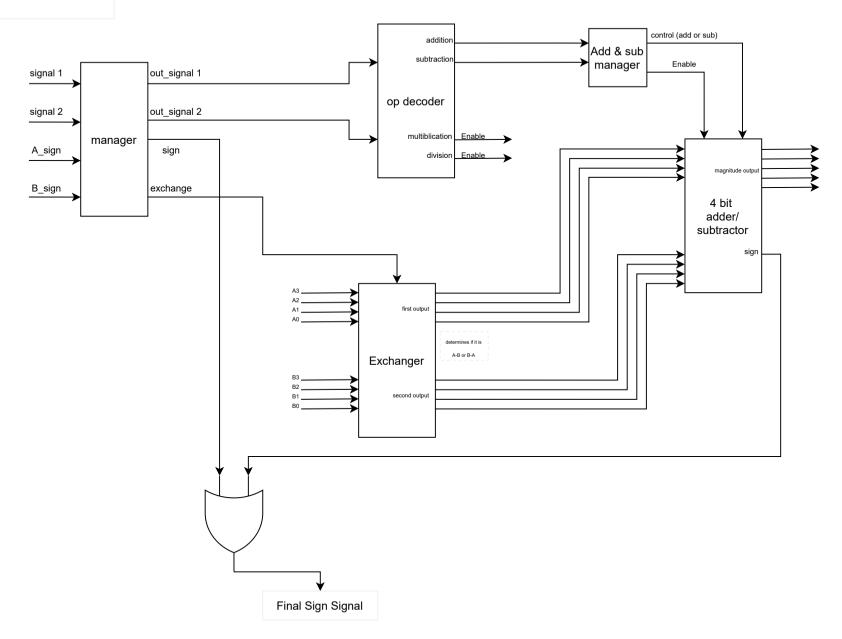
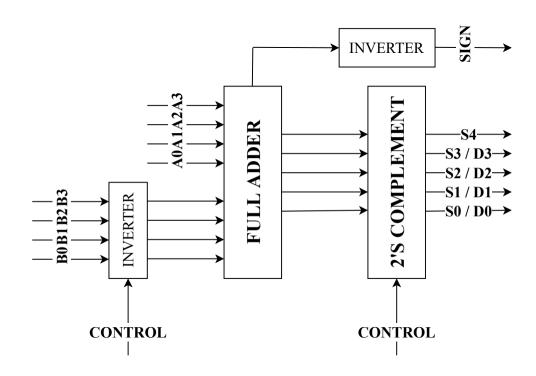
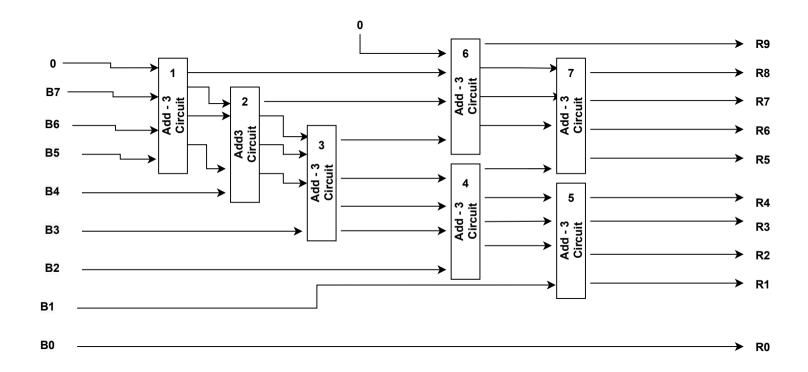
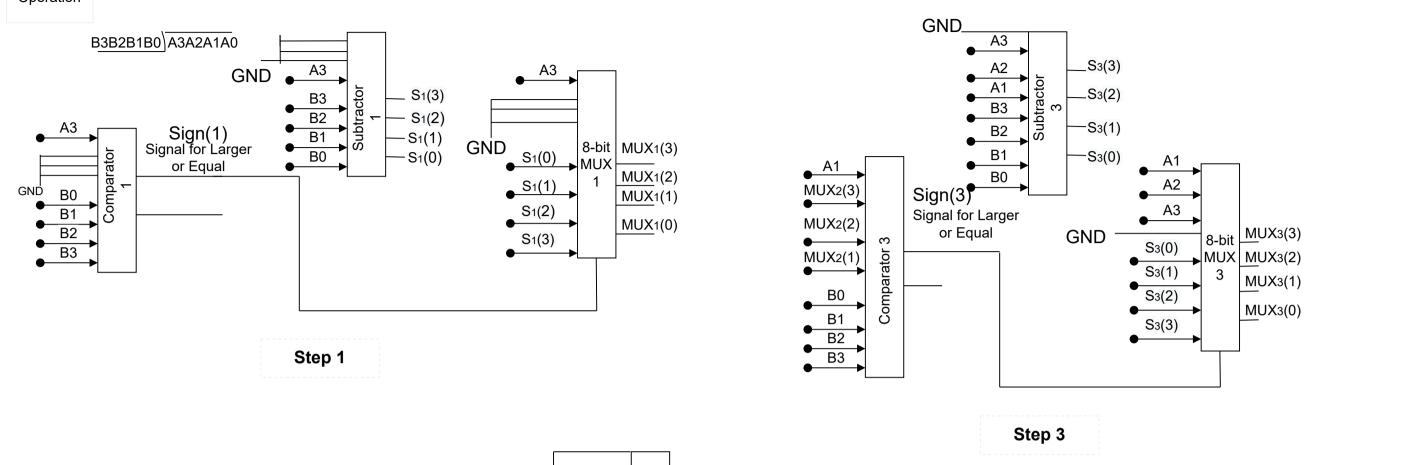
## Sign Management and the adding and subtracting control









\_S<sub>2</sub>(3)

 $-S_2(2)$ 

-S<sub>2</sub>(1)

 $-S_2(0)$ 

**GND** 

A2

S<sub>2</sub>(0)

S<sub>2</sub>(1)

S<sub>2</sub>(2)

S<sub>2</sub>(3)

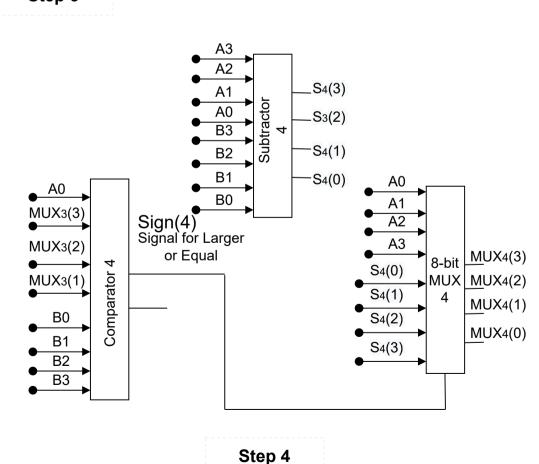
MUX2(3)

MUX2(1)

MUX2(0)

MUX MUX2(2)

8-bit



Sign(4)

Sign(2)

Sign(3) **D1** 

Sign(1) **D3** 

MUX4(3)

MUX4(1) →

MUX4(0)

MUX4(2) **R1** 

R2

R3

→ D2

Step 2

A3

A2

ВЗ

B2

B1

B0

Sign(2) Signal for Larger

or Equal

**GND** 

• A2

MUX1(3)

MUX1(2)

MUX1(1)

B0

В1

B2

В3

Comparator