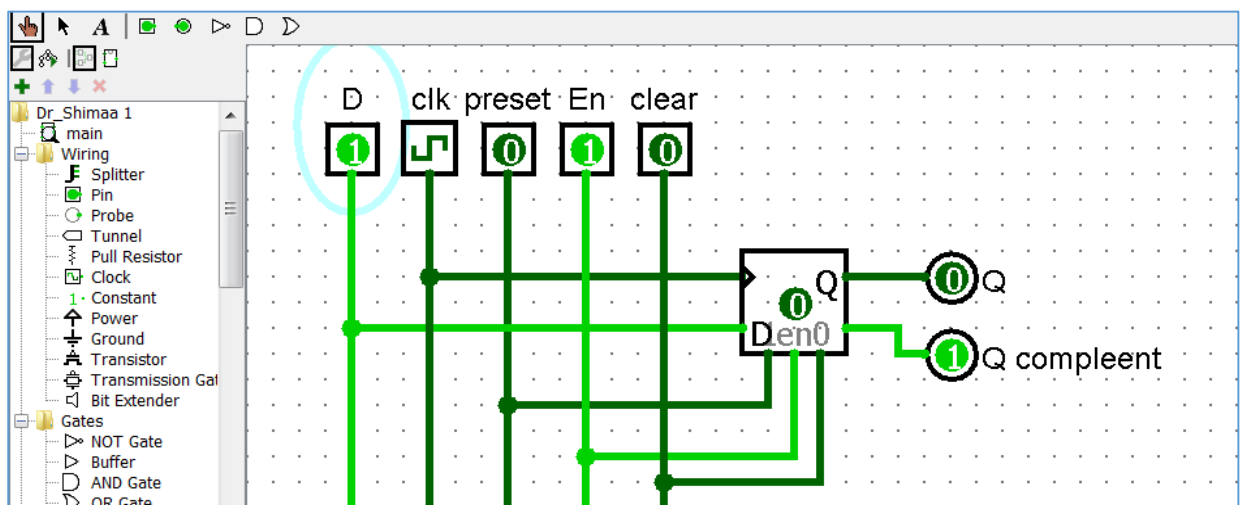
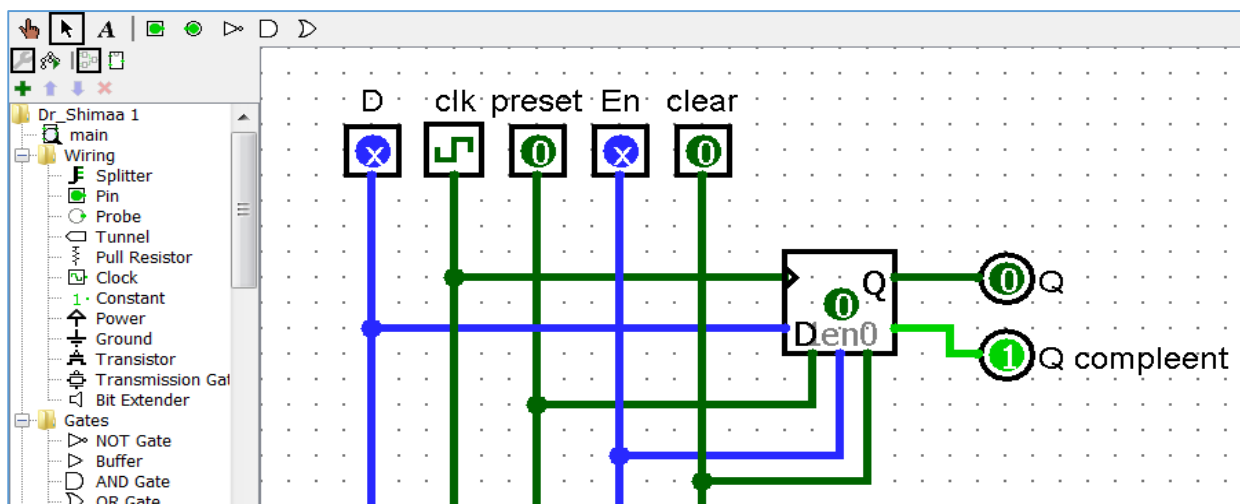
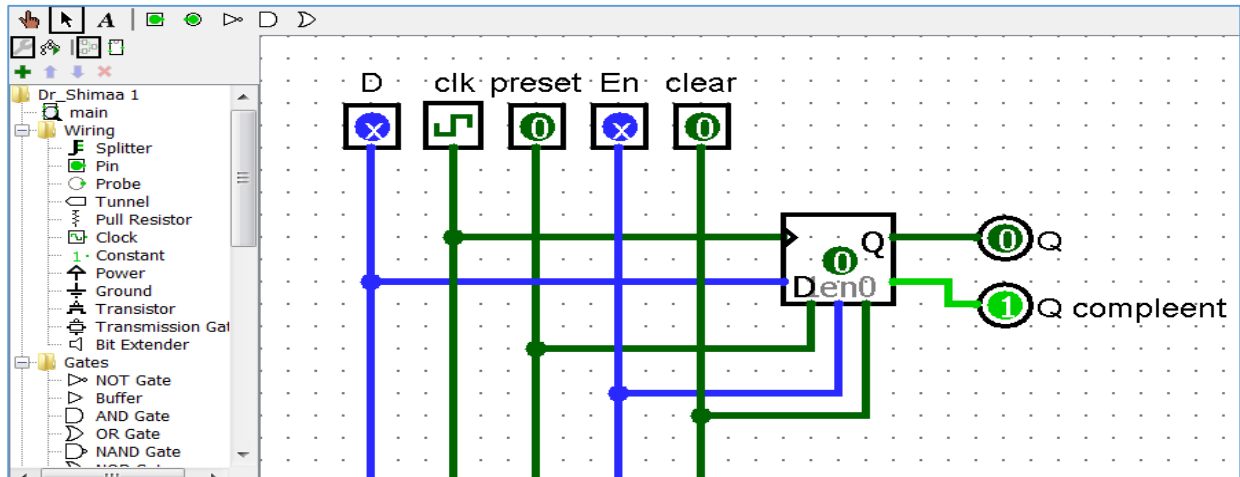
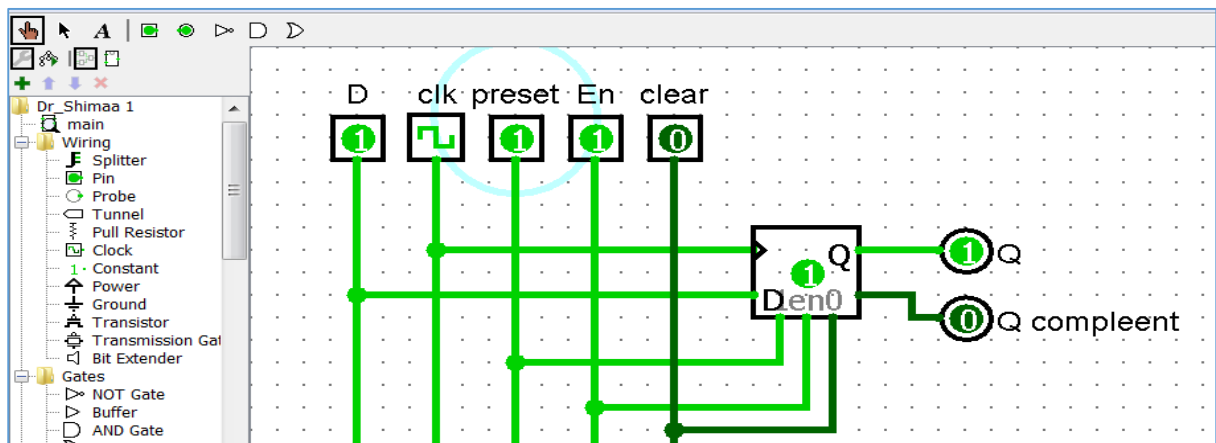
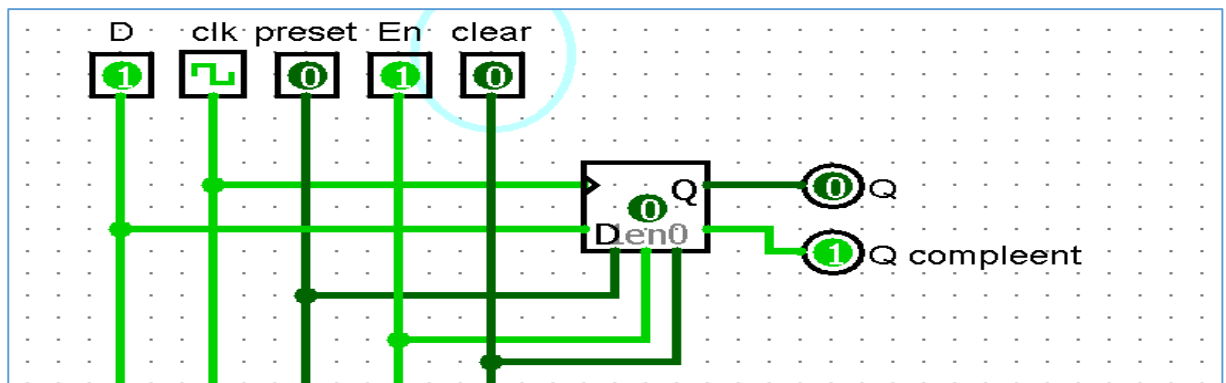
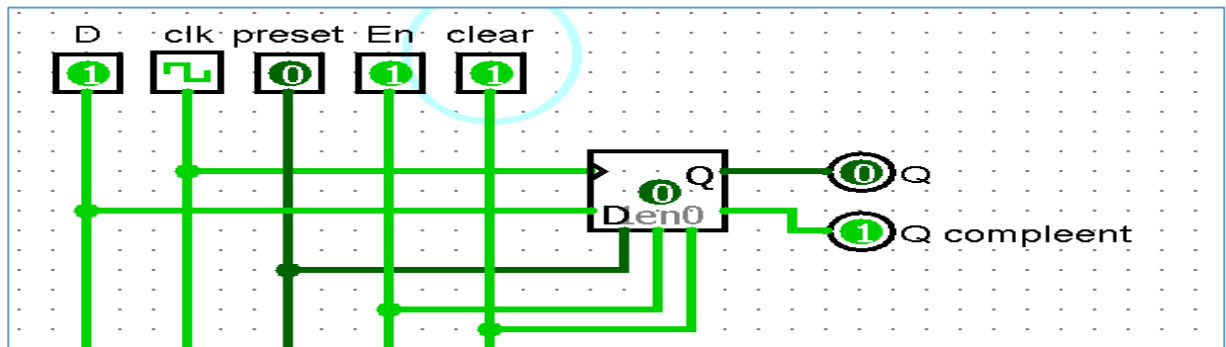
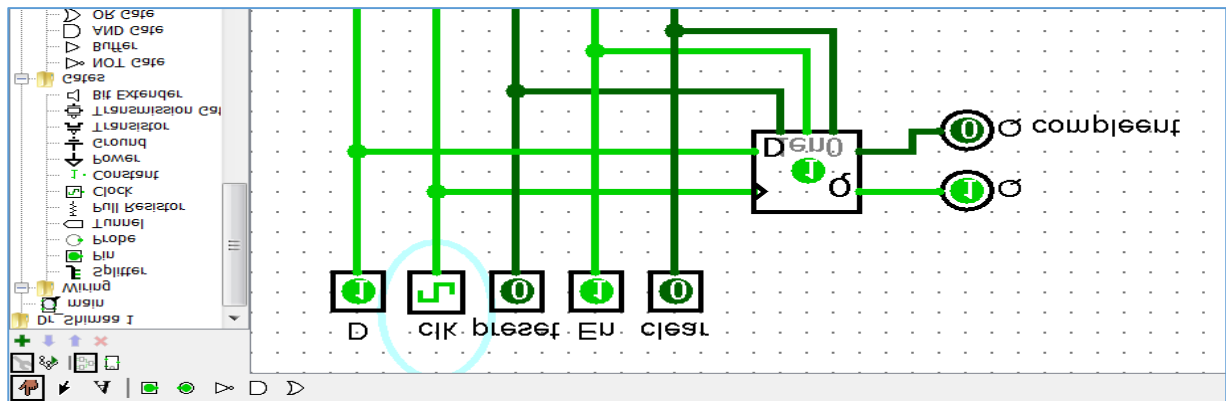


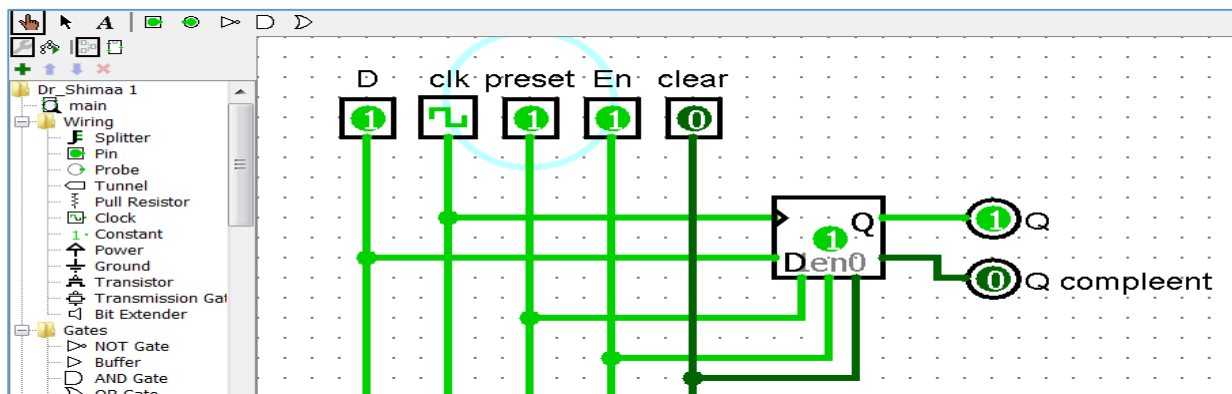
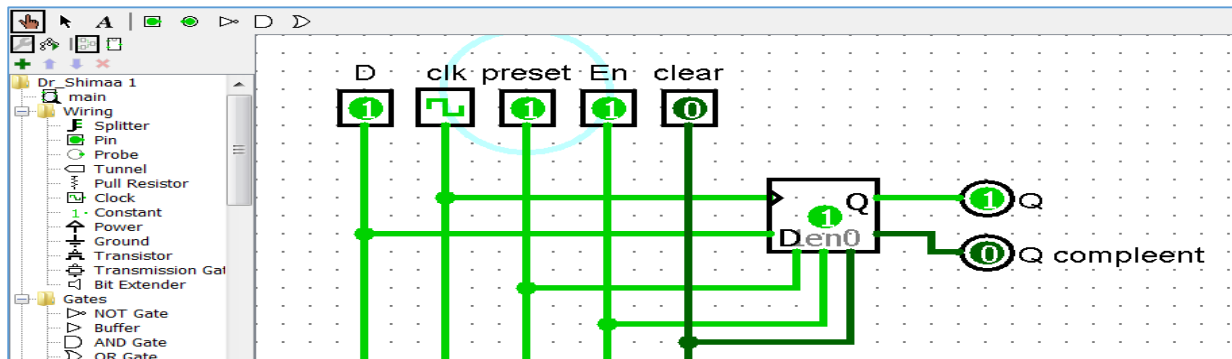
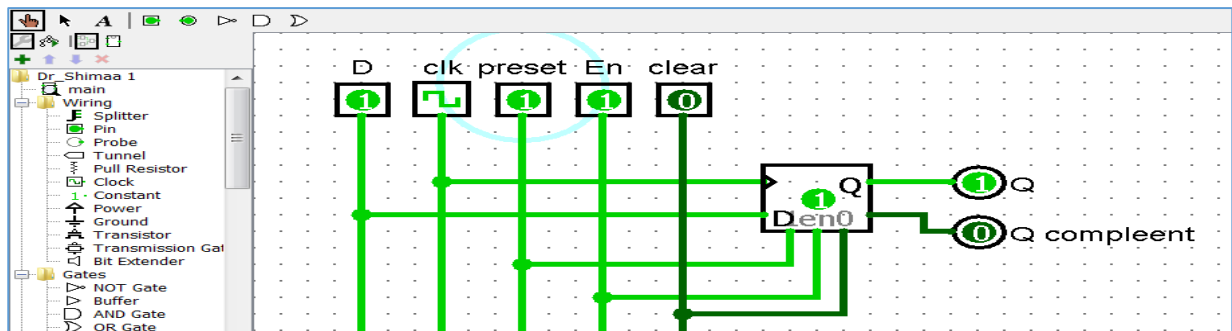
Logisim

Memory Elements and Bus simulation

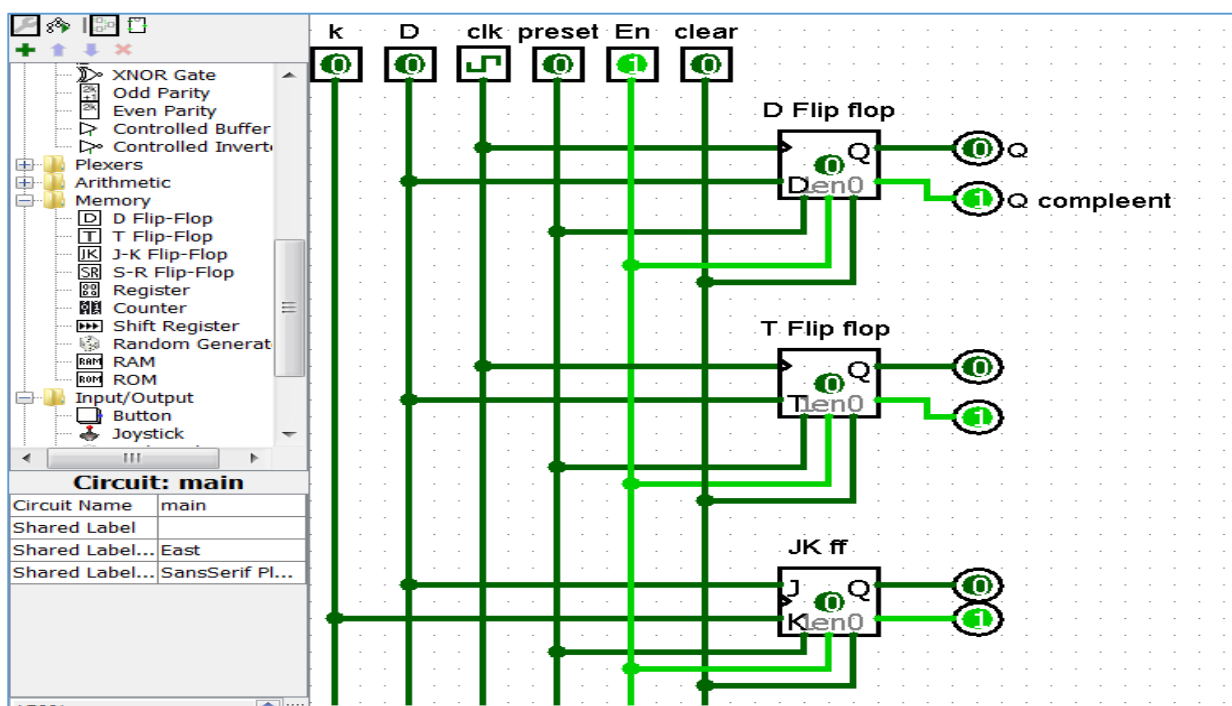
1. D-Flip flop



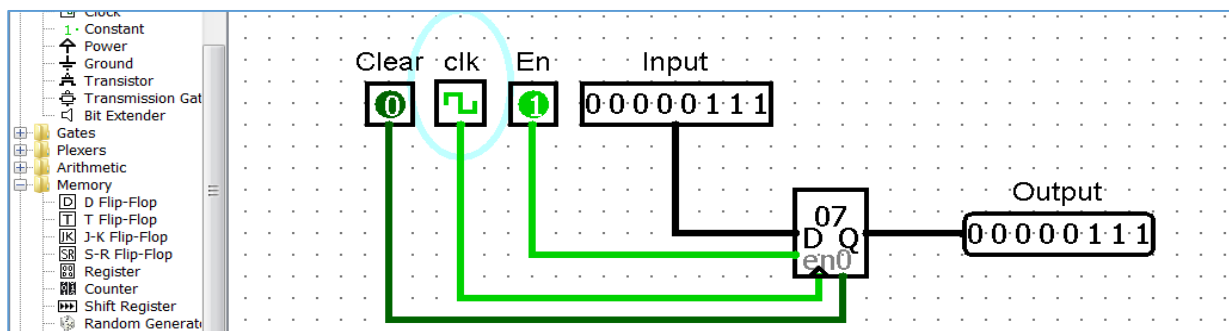
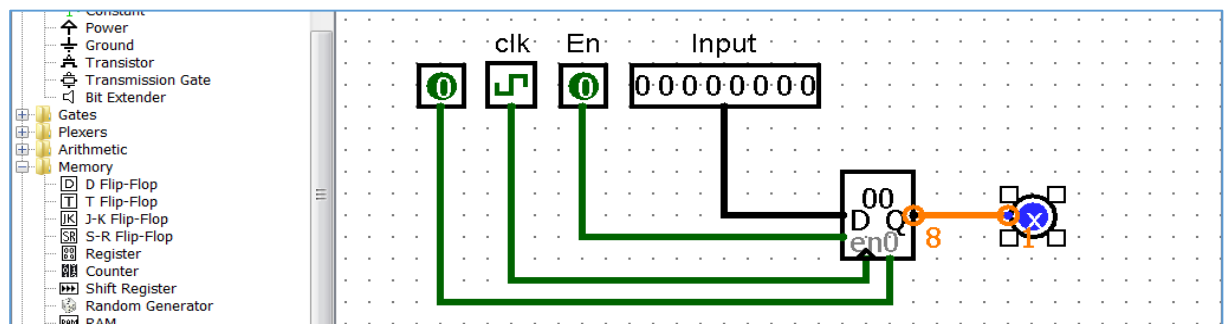
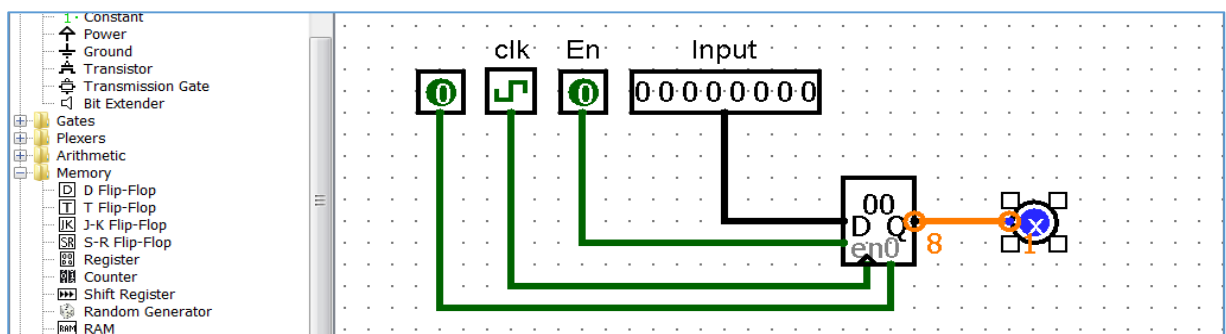
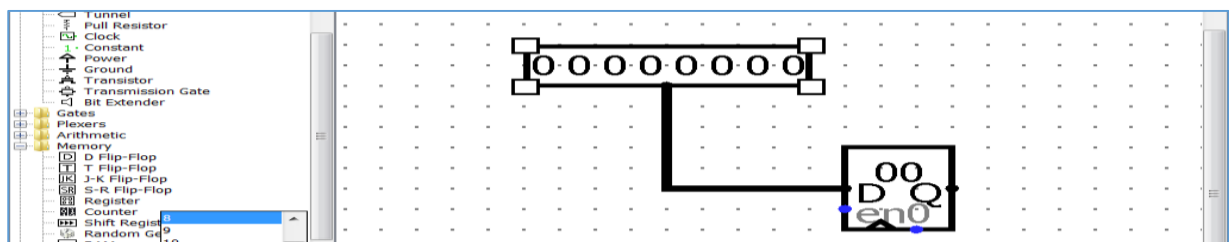
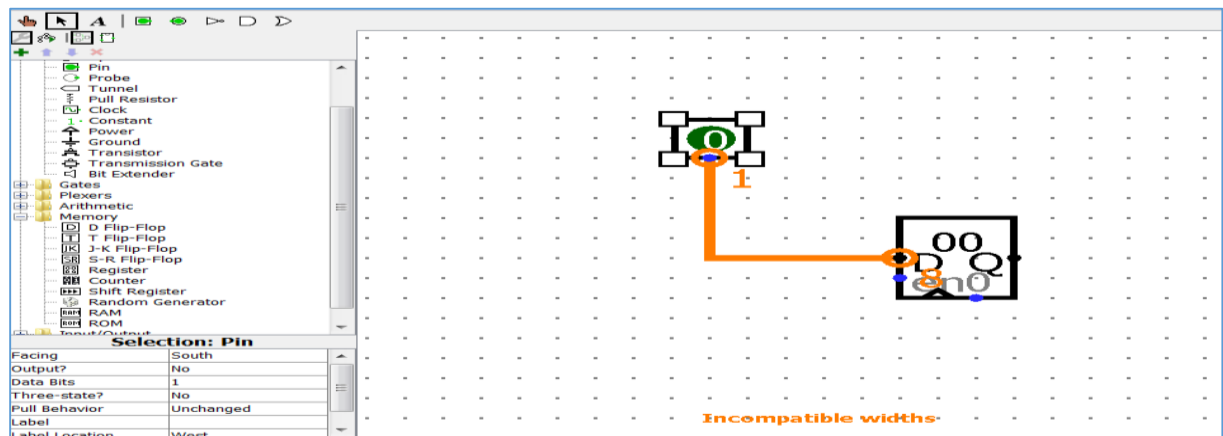


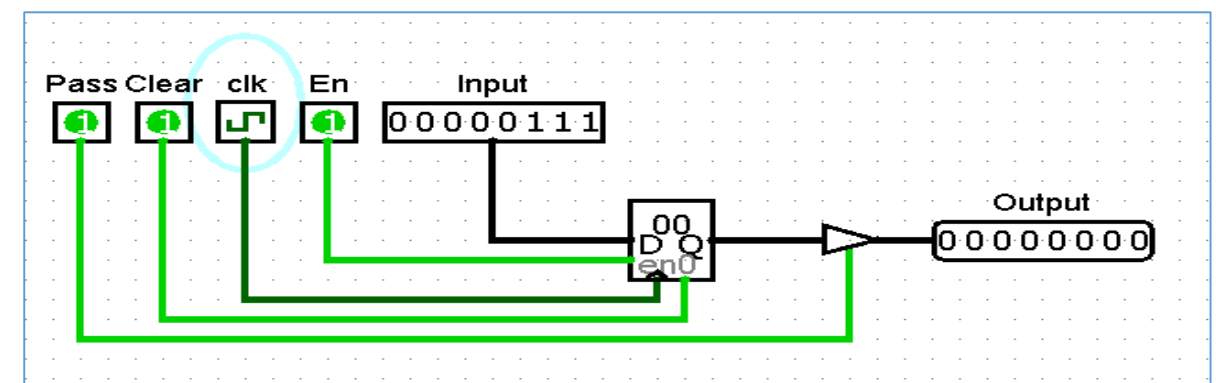
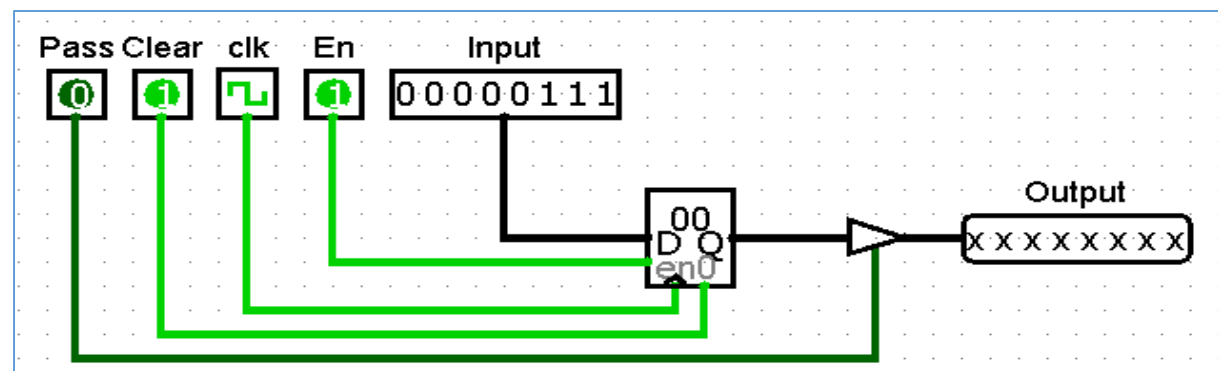
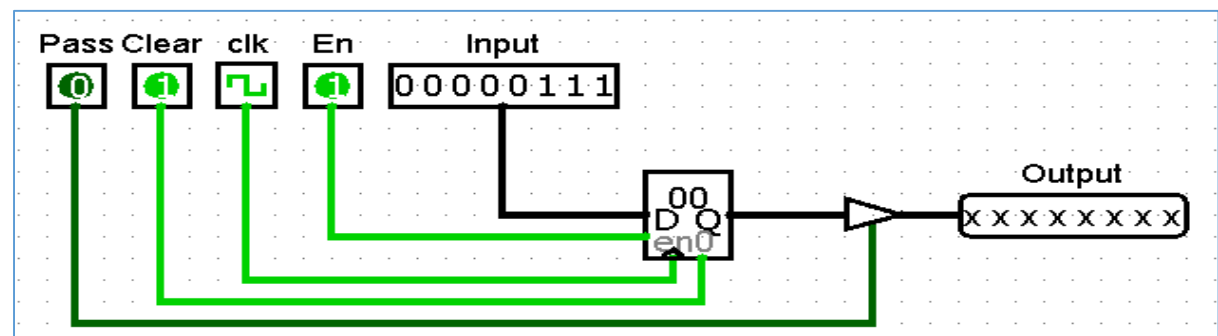
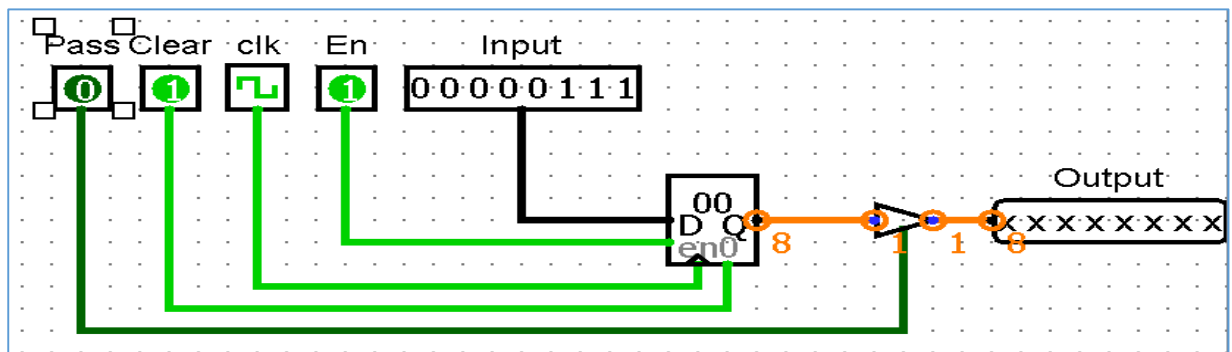
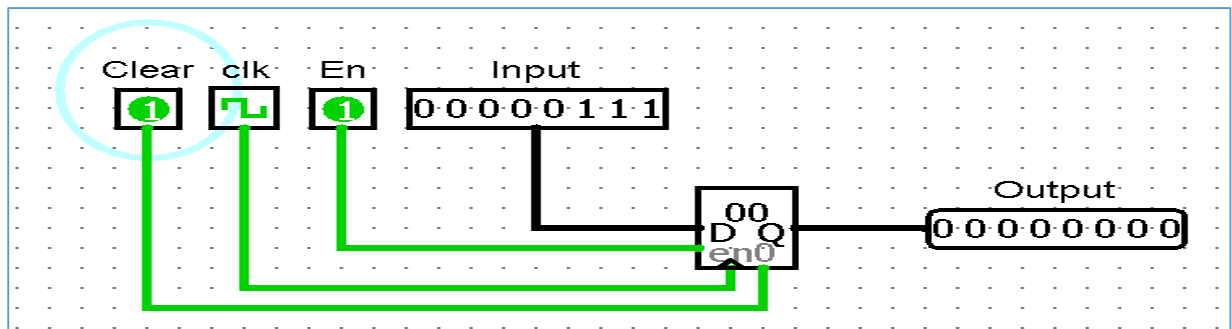


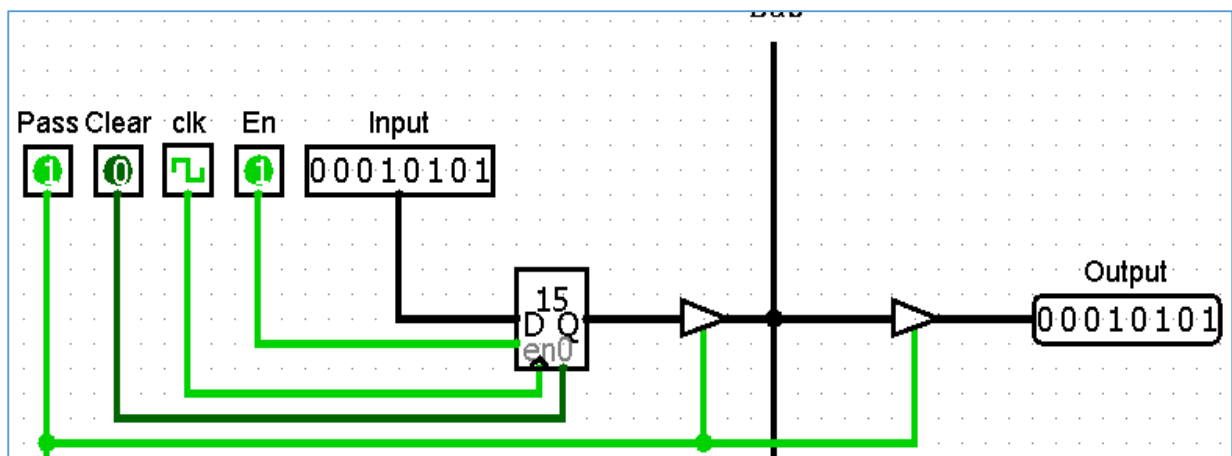
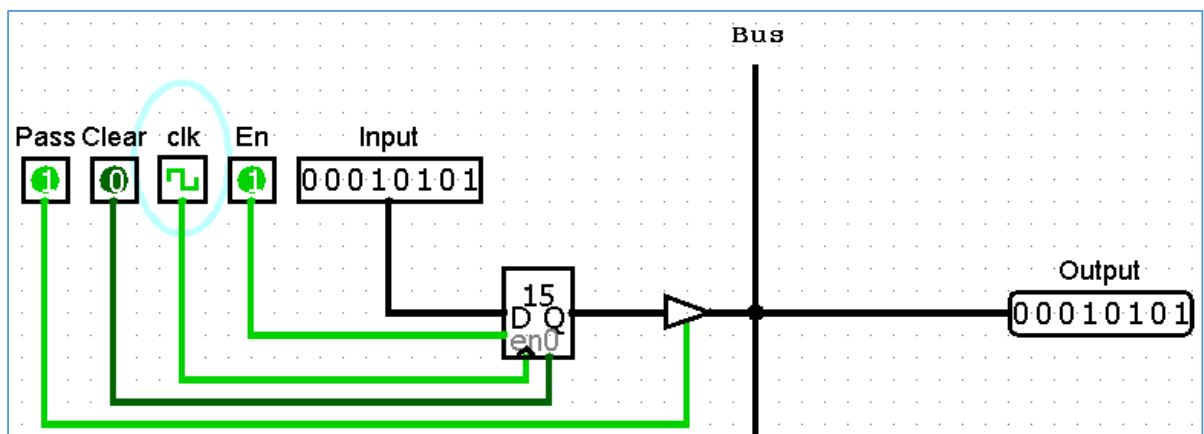
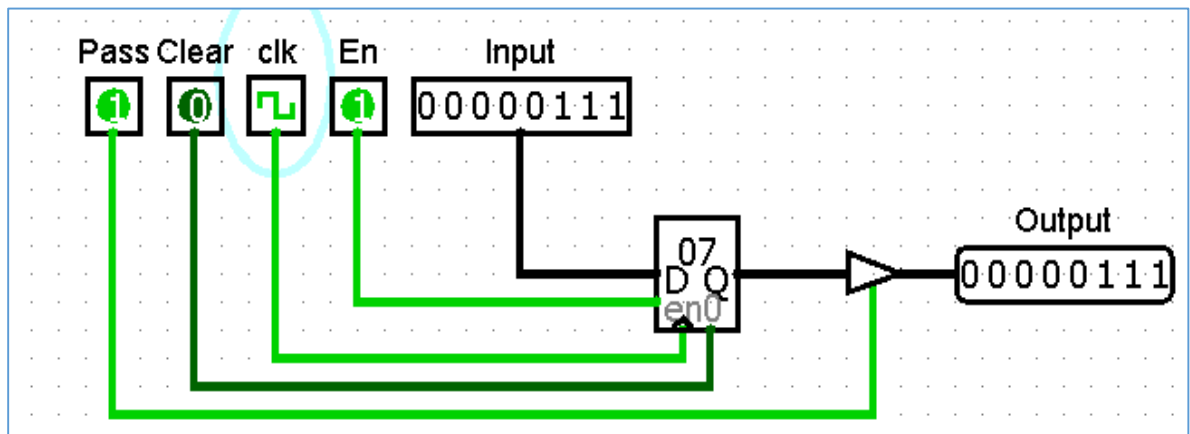
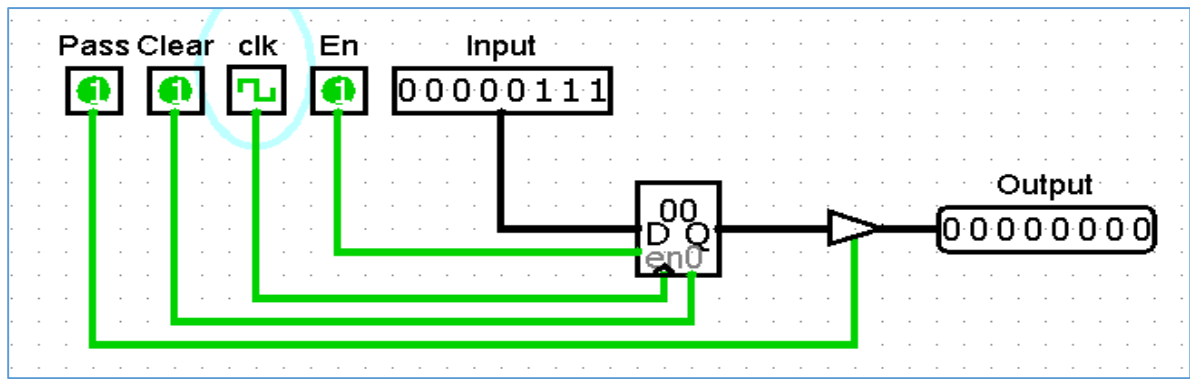
2. D, T, JK Flip flops

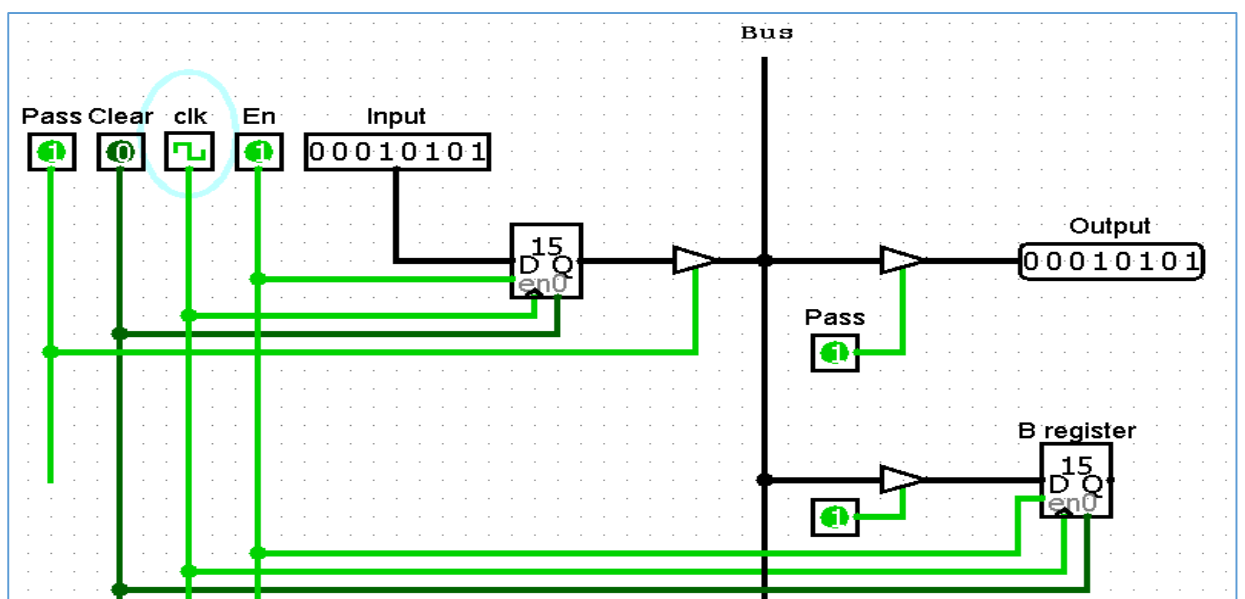
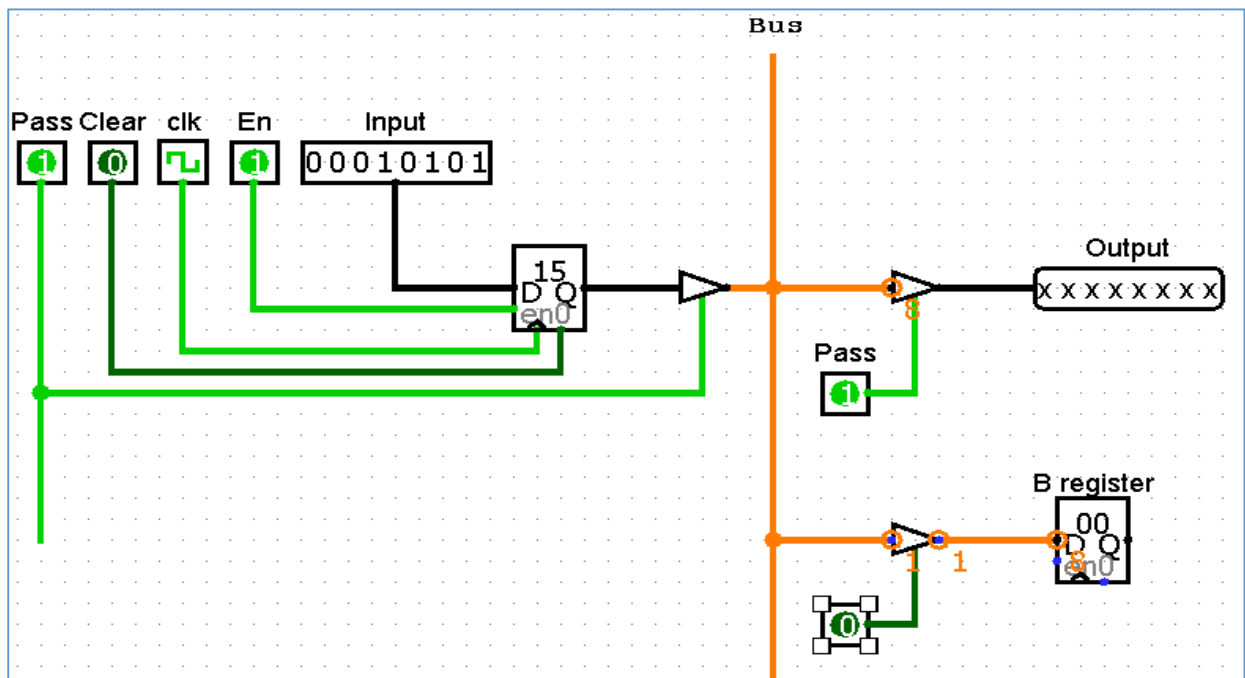
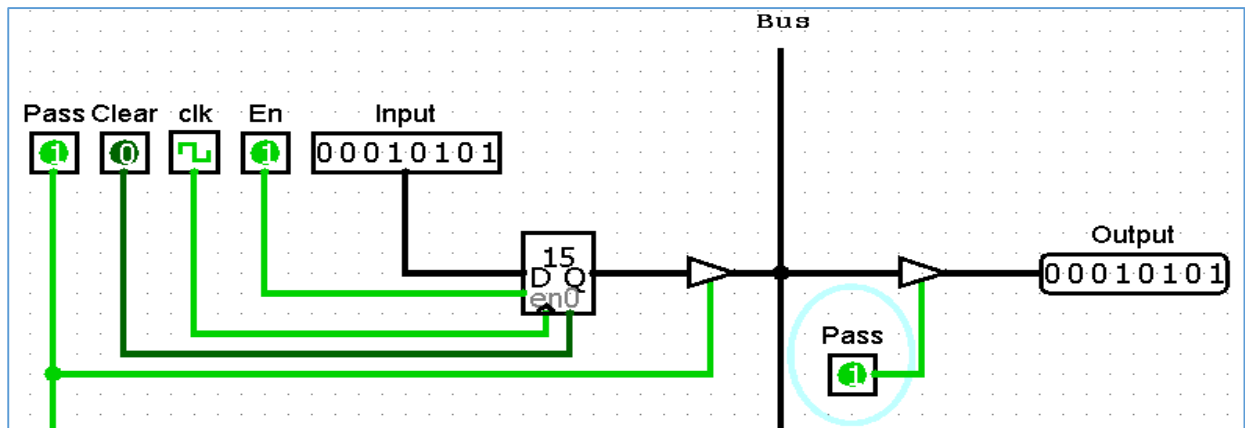


Registers









Short Circuit Error

