

The relations:
- faster accesstine, greater Gst Per unit
-Greater Cafacity, Smaller Ost Perunit
-Greater Capacity, slower access time
-> The designer needs to use expensive, relatively
Lower-Cefacity with short accesstine
Memory Hierachy - Diagram
- Inboard momory (register, cache, main memory)
-out pat board storge (Magnatic disk, CD-RAM, CD-RW
DUD-RW, DUD-RAM, Bly-Ray)
-off line storage (Magnatic take)