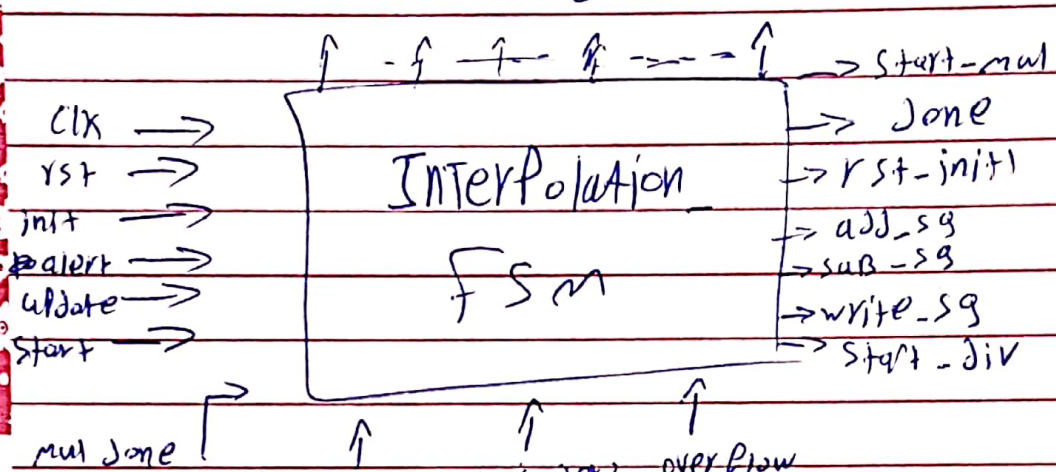
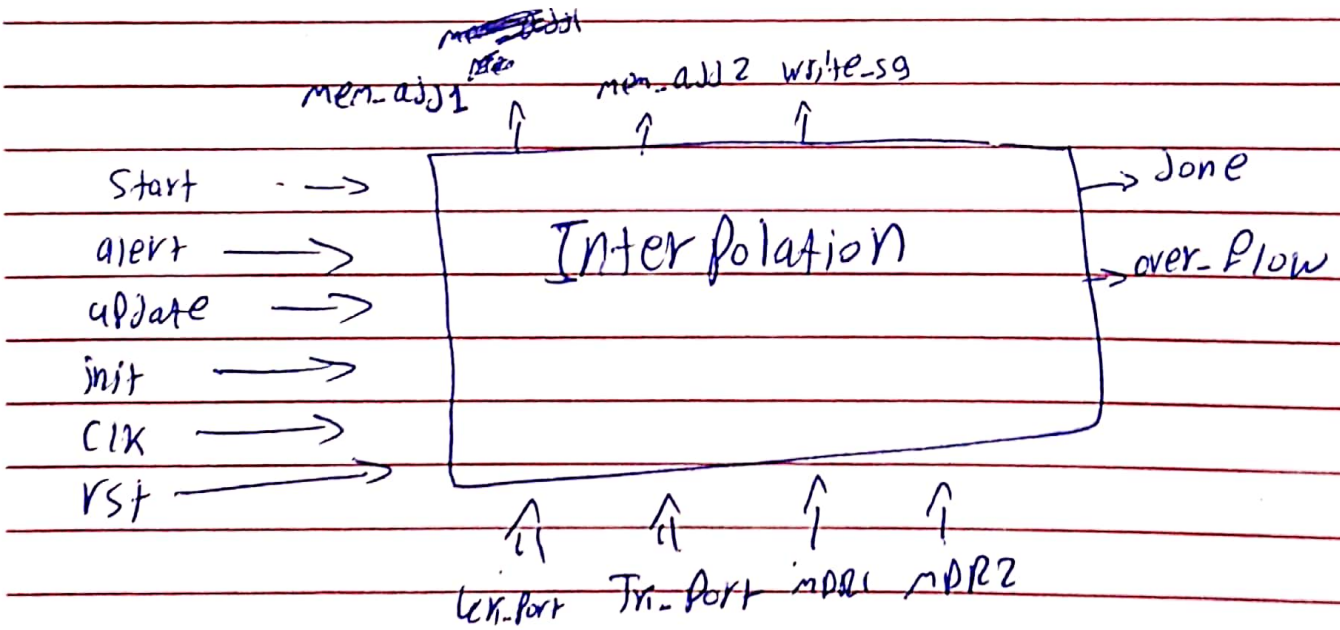
التاريخ : ..... الموضوع : .....

all registers enables and all mux selectors





note MDR2 is actually In/out

if write = 1 then Temp2 ⇒ MDR2