Static Analysis for UART Driver

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Agenda:

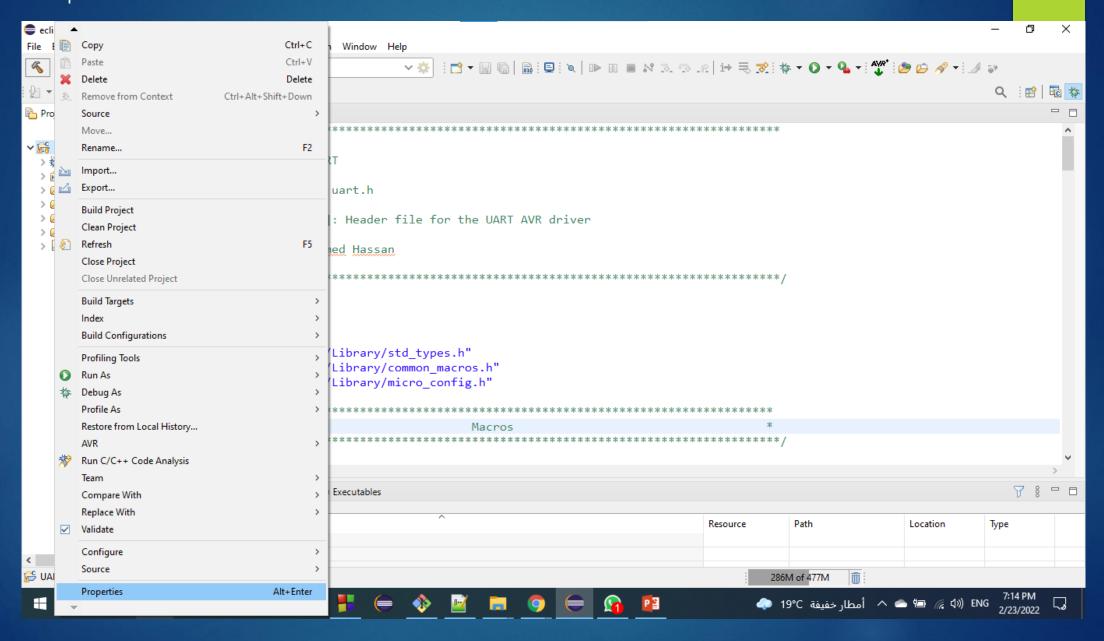
- IDE and Install Static Analysis Tool.
- Warnings and Issues.
- Solution.

IDE and Install Static Analysis Tool:

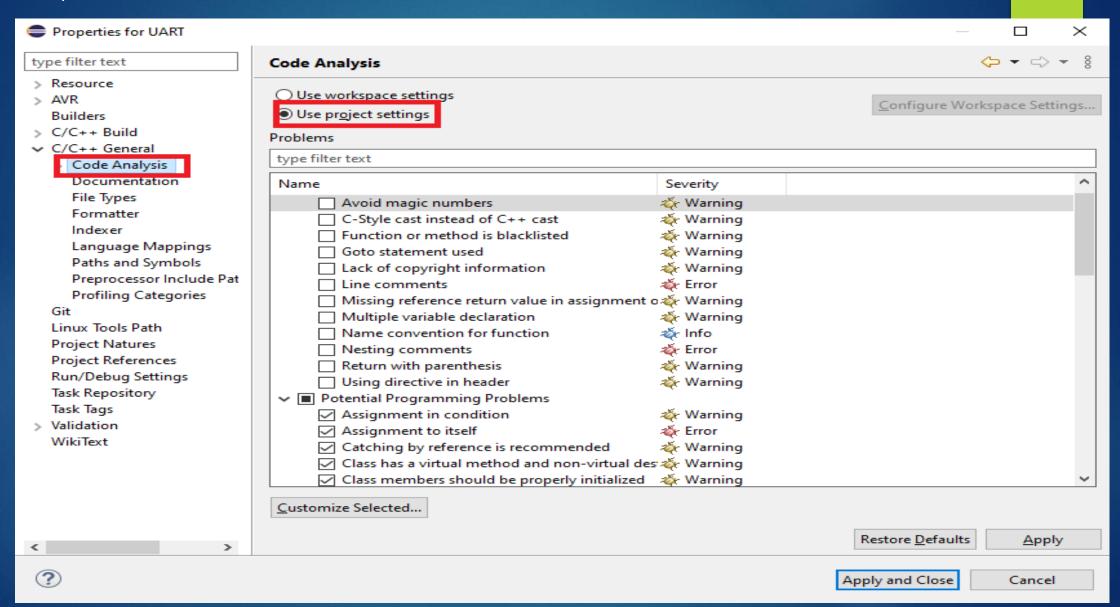
Steps of adding static Analysis to IDE:

- ▶ IDE: Eclipse
- Add the Static Analysis tool to the project:
 - Step 1: Go to the project property.
 - Step 2: Select code analysis, then select use project setting.
 - ▶ Step 3: Select the check box that compatible with Misra C Rules.

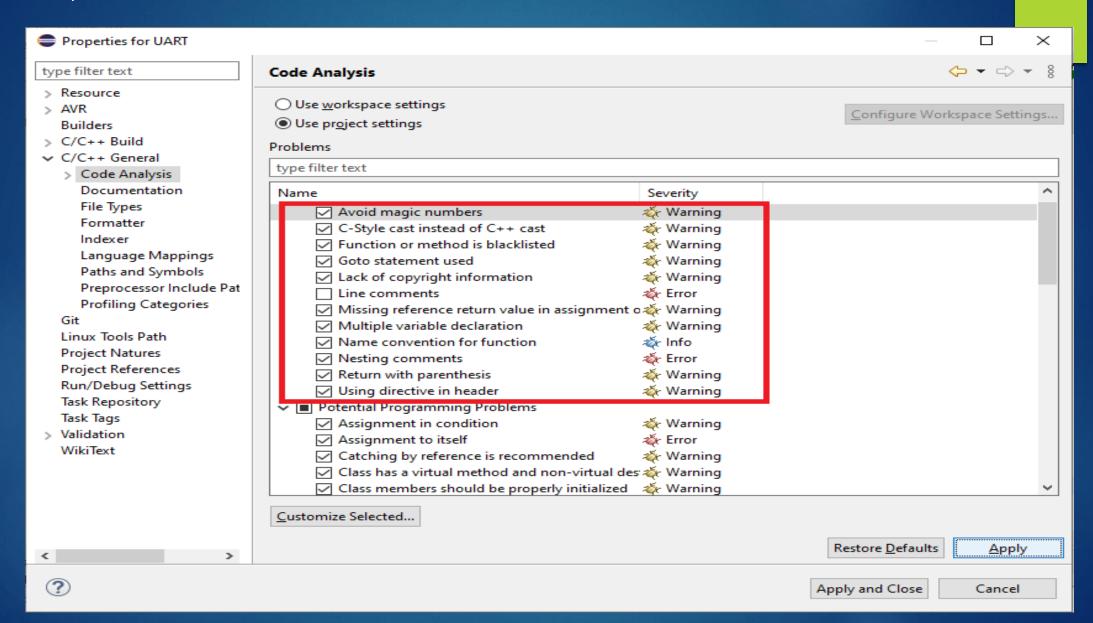
Step 1:



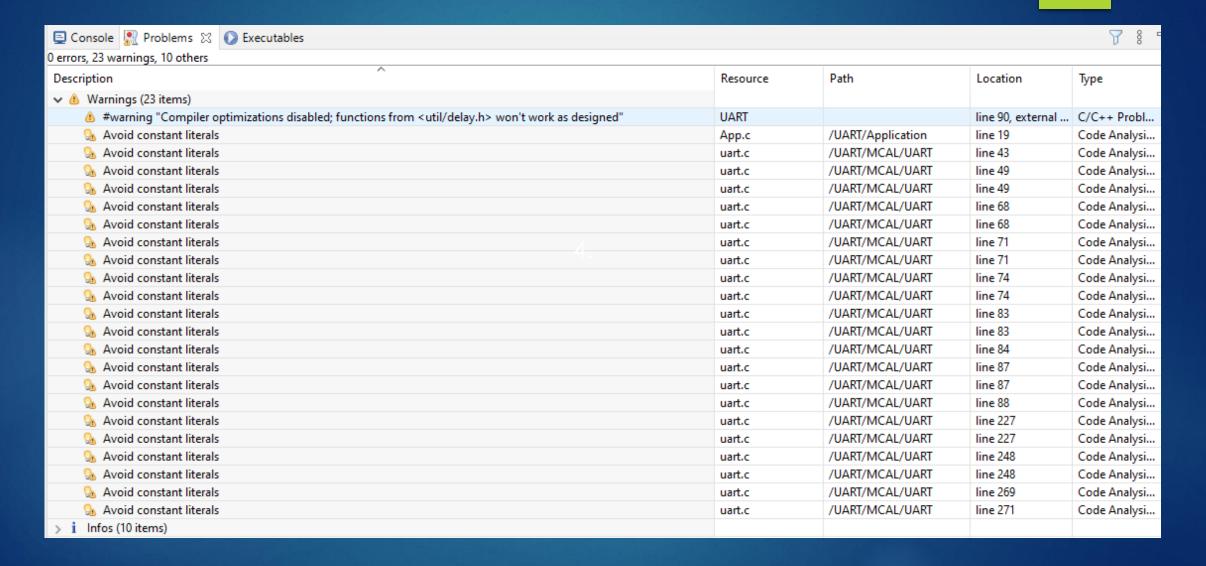
Step 2:



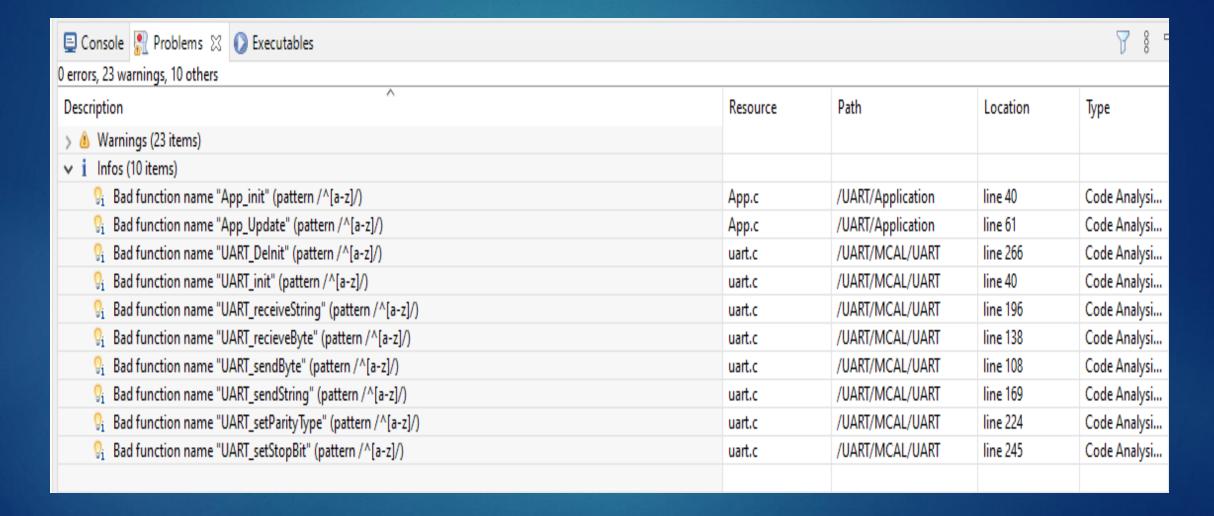
Step 3:



Warnings and Issues:



Warnings and Issues:



```
void UART init(const UART ConfigType * Config Ptr)
Issue:
Solution:
          void uart_init(const UART ConfigType * Config Ptr)
Issue:
          void UART sendByte(const uint8 t au8 data)
Solution:
         void uart_sendByte(const uint8_t au8_data)
          uint8 t UART_recieveByte(void)
Issue:
Solution:
         uint8 t uart recieveByte(void)
Issue:
          void UART sendString(const uint8 t * aStr message)
Solution:
          void uart_sendString(const uint8_t * aStr_message)
Issue:
          void UART receiveString(uint8 t * aStr message)
Solution:
          void uart receiveString(uint8 t * aStr message)
```

```
void UART_setParityType(const enuUART_ParityType_t enu_parityType)
Issue:
Solution: void uart_setParityType(const enuUART_ParityType_t enu_parityType)
Issue:
            void UART_setStopBit(const enuUART_StopBit_t enu_stopBit)
            void uart_setStopBit(const enuUART StopBit t enu stopBit)
Solution:
Issue:
            void UART DeInit(void)
            void uart_DeInit(void)
Solution:
            void uart init(const UART ConfigType * Config Ptr)
Issue:
               /* Insert the required Mode in U2X bit in UCSRA Register */
               UCSRA = (UCSRA & 0xFD) | ( (Config Ptr->uart Mode) << 1);</pre>
            void uart_init(const UART ConfigType * Config Ptr)
Solution:
                /* Insert the required Mode in U2X bit in UCSRA Register */
                UCSRA = (UCSRA & MODE MASK) | ( (Config Ptr->uart Mode) << 1);</pre>
```

```
/* Insert the last bit of required data bits in bit UCSZ2 in UCSRB Register */
Issue:
                       UCSRB = (UCSRB & 0xFB) | ( (Config Ptr->uart DataBits) & 0x04);
                     /* Insert the last bit of required data bits in bit UCSZ2 in UCSRB Register */
Solution:
                     UCSRB = (UCSRB & NINE DATA BITS MASK) | ( (Config Ptr->uart DataBits) & NINE DATA BITS SHIFT);
                    * Insert the First two bits of required data bits in bits (UCSZ0, UCSZ1)
                    * in UCSRC Register in case of (5,6,7 or 8) Data bits
                   UCSRC = (UCSRC & 0xF9) | ( (Config Ptr->uart DataBits & 0x03) << 1);</pre>
Issue:
                   /* Insert the required parity type in bits (UPM0, UPM1) in UCSRC Register */
                   UCSRC = (UCSRC & 0xCF) | ( (Config Ptr->uart Parity) << 4UL);</pre>
                   /* Insert the required number of stop bit(s) in bit USBS in UCSRC Register */
                   UCSRC = (UCSRC & 0xF7) | ( (Config Ptr->uart StopBit) << 3UL);</pre>
                   * Insert the First two bits of required data bits in bits (UCSZ0, UCSZ1)
```

```
* Insert the First two bits of required data bits in bits (UCSZ0, UCSZ1)

* in UCSRC Register in case of (5,6,7 or 8) Data bits

*/

UCSRC = (UCSRC & DATA_BITS_MASK) | ( (Config_Ptr->uart_DataBits & DATA_BITS_SHIFT) << 1);

/* Insert the required parity type in bits (UPM0, UPM1) in UCSRC Register */

UCSRC = (UCSRC & PARITY_MASK) | ( (Config_Ptr->uart_Parity) << PARITY_SHIFT);

/* Insert the required number of stop bit(s) in bit USBS in UCSRC Register */

UCSRC = (UCSRC & STOP_MASK) | ( (Config_Ptr->uart_StopBit) << STOP_SHIFT);
```

* This switch case is to choose between two Modes (Normal, Double Speed),

* then calculate and insert the required Baud rate in First 8 bits from the

Solution:

Issue:

```
void uart_setParityType(const enuUART ParityType t enu parityType)
 Issue:
                    /* Insert the required parity type in bits (UPM0, UPM1) in UCSRC Register */
                    UCSRC = (UCSRC & PARITY_MASK) | (enu_parityType << PARITY_SHIFT);</pre>
                void uart_setParityType(const enuUART_ParityType_t enu_parityType)
Solution:
                    /* Insert the required parity type in bits (UPMO, UPM1) in UCSRC Register */
                    UCSRC = (UCSRC & PARITY_MASK) | (enu_parityType << PARITY_SHIFT);</pre>
                void uart_setStopBit(const enuUART StopBit t enu stopBit)
Issue:
                    /* Insert the required number of stop bit(s) in bit USBS in UCSRC Register */
                    UCSRC = (UCSRC & 0xF7) | (enu stopBit << 3UL);</pre>
                void uart_setStopBit(const enuUART StopBit t enu stopBit)
Solution:
                     /* Insert the required number of stop bit(s) in bit USBS in UCSRC Register */
                     UCSRC = (UCSRC & STOP MASK) | (enu stopBit << STOP SHIFT);</pre>
```

Issue:

```
void uart_DeInit(void)
{
    /* Reset All UART Registers to its initial value */
    UCSRA = 0x20;
    UCSRB = 0;
    UCSRC = 0x86;

    /* Clear UART Baud Rate Registers */
    UBRRH = 0;
    UBRRL = 0;
}
```

```
Solution:
```

```
void uart_DeInit(void)
{
    /* Reset All UART Registers to its initial value */
    UCSRA = USCRA_INITIAL_VALUE;
    UCSRB = 0;
    UCSRC = USCRC_INITIAL_VALUE;

    /* Clear UART Baud Rate Registers */
    UBRRH = 0;
    UBRRL = 0;
}
```

Thank you