**Computer Architecture Project**

**1st milestone report**

* **Names:** 
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* **What is done:** 
  + We have edited the original design of PDP11 microprocessor and implemented it to support the following:
    1. The 8 addressing modes.
    2. Two words operand instruction set.
    3. No operand instruction set.
  + Our design include the following components:
    1. Register array.
    2. Special register array.
    3. ALSU.
    4. Decoder set.
    5. Wide branch.
    6. RAM.
    7. CPU.
* **What is remaining:**

1. One operand instruction set.
2. BRANCH instruction set.

* **Notes about the design:**

1. We have added a part in BitOring that allow us to handle two operand operations “OR2operand”.
2. We edited F9 in IR as we replaced the WMFC by Flag-e that enable the flags.