11 Direct mapping -				
M. Memory SiZe =			- Address bits = 20 bit	
Cache blocks = 3			> Block bits = 5 bit	
block size 2	16 = 24		offset bits 2 4 bit	
	Tag	Block	offset	
		5	the state of the s	
Main memory blocks				_
_ 0X0DB63 ⇒	000011	0110110	119,0011	
La Mapped to B'				
101 Direct married	1			
Direct mapping Main memory size	z 2 <sup>32</sup>		-> Address bits = 32	
Cache blocks			Block bits = 10	
block Size	_		- offset bits = 5	
	-			
	Tag	Block	offset	
	17	lo	5	
Main memory blo	ds = 282	= 227	block	_
	25			_
_Ox000063FA	=) 0000	000000	00 0000 0/10 0011 1111 10/0,	
1.1. 01	V 700			
Mapped to Block	7 77			
	- Contract			
				-
				-

[3] Direct mapping.		>A	loddress bi	ts 32
Main memory size = 232	81			15.3
Cache blocks = 29 - 23		> affect bits 6		
Block Size = 26				
		Slock	offset	
1.1	23	3	6	
Main memory blocks =	232 - 226	block	t - le	
Mapped to Block &		, <sup>©</sup>	01/2 <sup>0</sup> .c	A le region de
[4] Fully Associative:			1. 1. 1. 1. 1. 1. 1.	Jam Wall J.
Main memory size = 216			Address	bits= 16
Cache blocks = 26				soles see a
Block size = 25				bits= 5.
			100	
	Tagaild			
	Tagnold 1101			
Main memory blocks 2	7ag 11 1 216 - 9" }	6.0	Pfset 5	
Main memory blocks 2 OXF8C9, will be ma	216 - 211 } 25 upped to any	olock -	Pfset 5 4. A.	when Cache is
Main memory blocks 2 OXF8C9, will be ma	216 - 211 } 25 upped to any	olock -	Pfset 5 4. A.	when Cache is
Main memory blocks 2  OXF8C9, will be ma	216 - 211 } 25 upped to any	lock - free block to speci	Pfset  5  K, and  Lic block	

The state of the s			
15) Pully associative:	24	٨١١،	14
Main memory six	2-2	$\longrightarrow$ Hololyes	s bits = 24
Cache blocks	227		
Block size	2 26	-> offset	bits 26
	Tag	offset	
	18	6	
Main memory blocks	= 224 = 218 blo	Ks	
6) Fully associative.	0.4		
Main memory siz	e = 2"	same as (5)	
Cache blocks	227	same as	
Block size	. 26		
	>		
El 0 a + asses)	4:00		
[7] 2 way set associa	Size = 27 + 20 =	27	Vess bits= 27
Main memory	05.00	Z -> FIVE	
n. Sets z	25.210 - 214	→ 5eA	bits = 14
			111
Block size	2 2	-> offs	et bits = 6
	7	14 6	
	Tag.	Set Off.	set

Subject:			Date: /	,
13) 2-way set ass	ociative.			
a) thain mame	size= 21 = 21	23 = 214	- Address b	
n. sots	= 2 <sup>2</sup>		> Sets b	its = 2
Block Si	2 2 3	-	-> offset b	sits 3
	Teir	601	,	
	Tag.	set	offset	
	9	2	3	
b) First loo	0):-			
	•			
5.51	Miss	> 6000000	00001000	3
	-, Block o = 0x8	exo;	, 0 × 15	1.10
2				
OXY	, ox lo,,	0x15 -> hi	in Set 1,	Blocko
	1			
OXI	Miss >	000	100000010	2119
= 5et 2, Block 0 - 0x16, 0x17, 0x23				
entransient in the second of t				
0x17,0x18, ,0x23 > hit in set 2, Blocko				
			,	
OXO	4 Miss	<b>S</b> 07	00,0000001	مامام
. set	3 Block 0 =	2×24 0×25		24
	3	1,0,75	, o x	31
a V	25,0X26,	0×21	1.+	3 01 1
υ. Δ	UN 10,	,0131-5	hil in Set:	3,1310CKO
-Y	30 Miss	3	0.000000	

0X33 - hit in set o, Blocko

: Set o, Block o - 0x32, 0x33, , , 0x39

First Second loop has	4 misses and (7003) +1 = 22 Lits

Second loop:

0x8, 0x9, 0x10, ---, 0x 31, 0x 32,0x33 -> hit

Secondloop has o Misses and 26 hits

Third loop,

0x8,0x9, \_\_\_\_,0x31,0x32,0x33 > hit

Third loop has a Misses and 26 hits.

Intotal, we get 74 hits and 4 misses

: hit rat 10 = hits = 74 \$100 - 94.87 %

Block size = 23 Address bits = 16 bit

Block size = 23 > offset bits = 3 bit

2. Way set associative Cache:

n.of sets: 25 = 24 \_\_\_\_\_ set bits = 4 bit

Tag	Set	offset
9	4	3

4 way set associatives

n. of sets = 25 - 23

> Set bits = 3 bits

offset Set 3

To Main memory size = 221

, Address bits 21

Block size . 22

- Offset bits 2

2 way set associative.

n. of set 5 = 64 = 32 = 25 > set bits = 5

14

Set

offset

4 way set associative.

no. of sets = 64 = 16 = 24

Tag.

offset 2