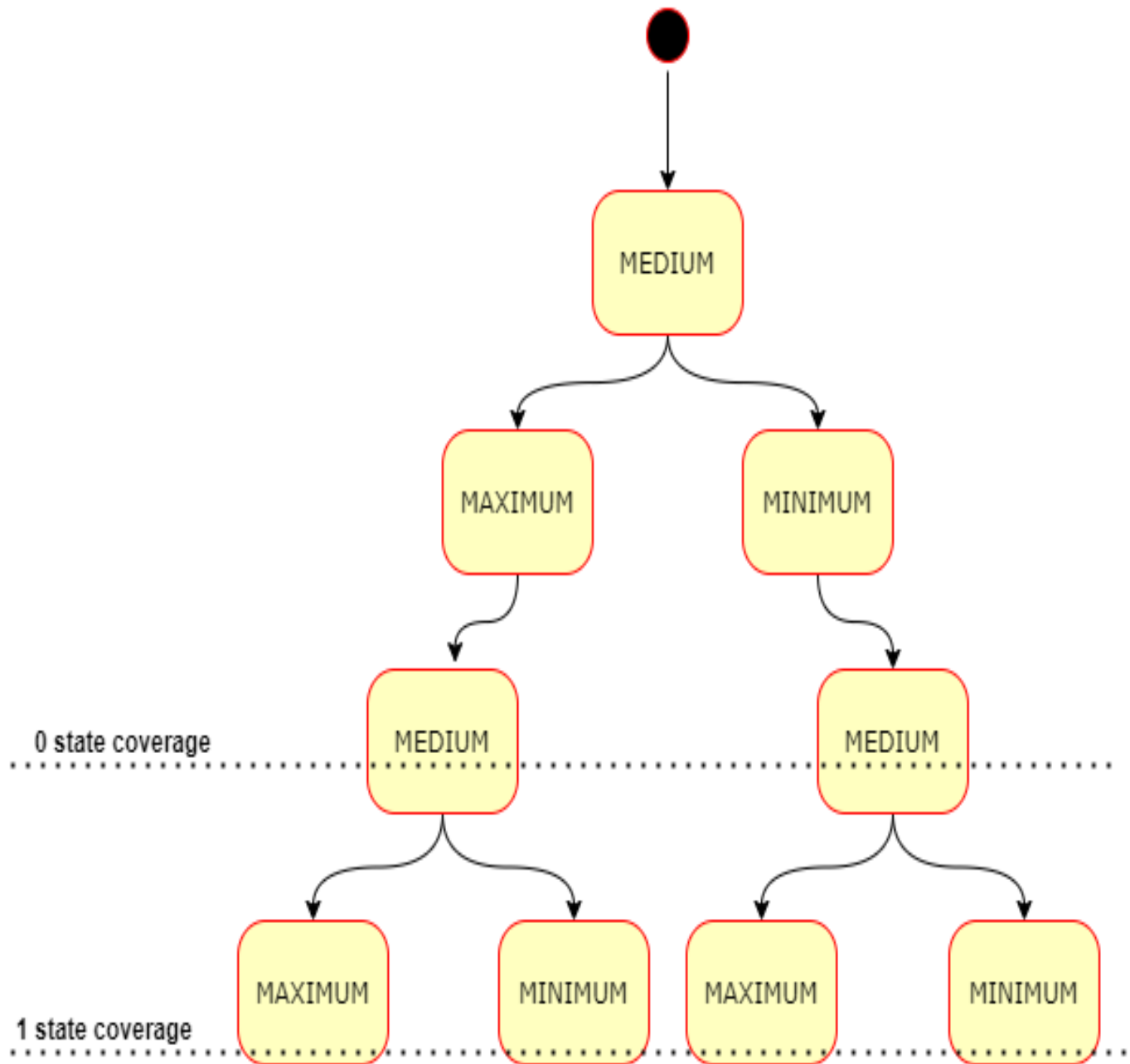
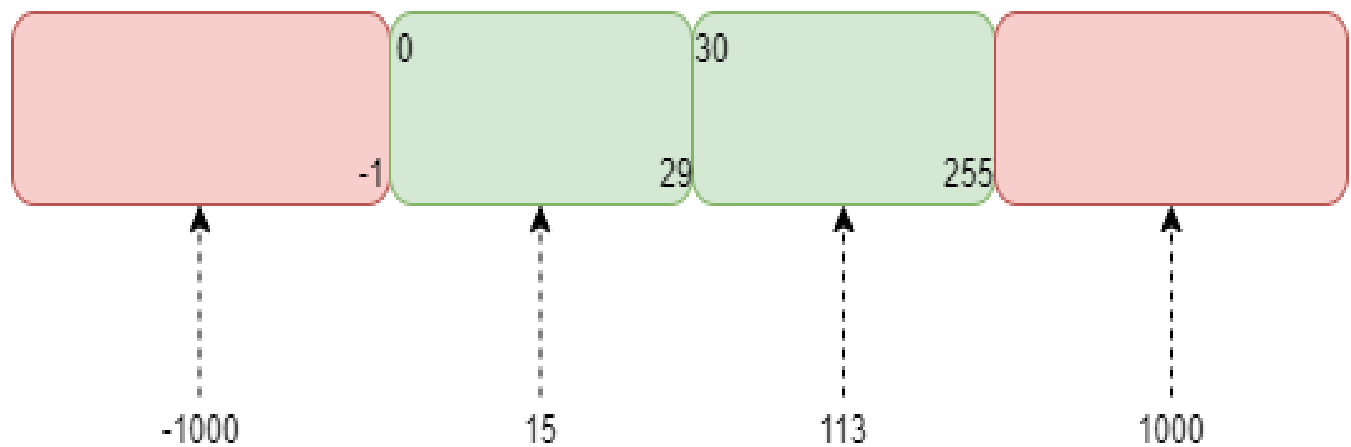


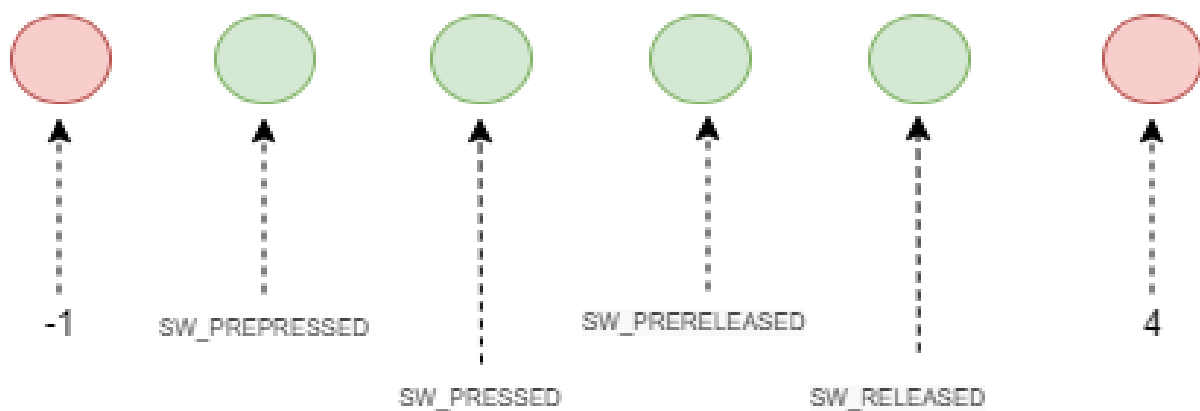
1State Coverage



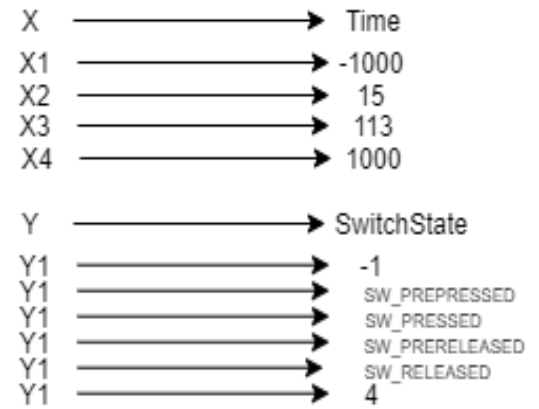
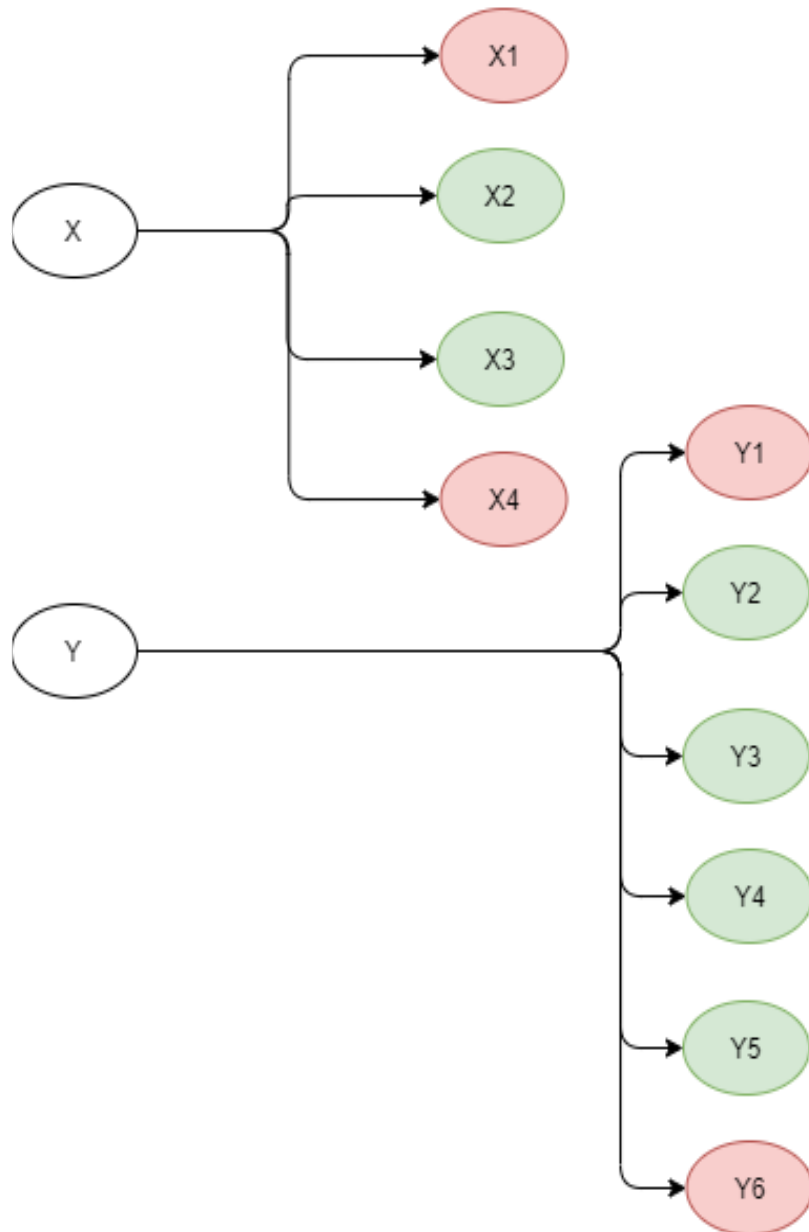
EP for time



EP for switches state

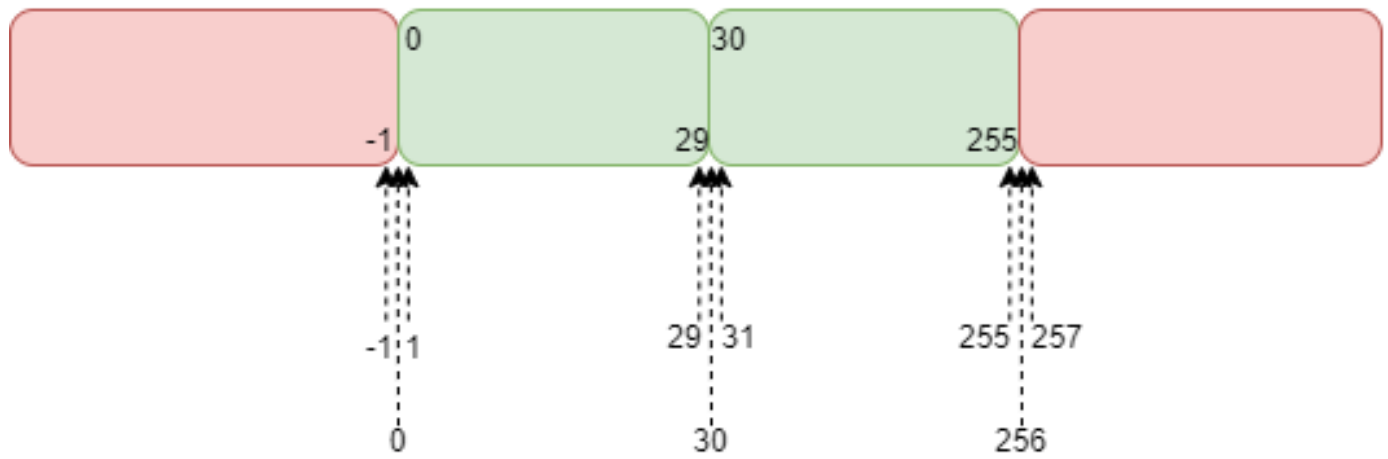


EP composing with invalid



	X	Y
TC1	X2	Y2
TC2	X3	Y3
TC3	X2	Y4
TC4	X3	Y5
TC5	X1	Y2
TC6	X4	Y3
TC5	X2	Y1
TC6	X3	Y6

BVA for time



EP for switch names

(input to SpeedControl)

