

## Task 2

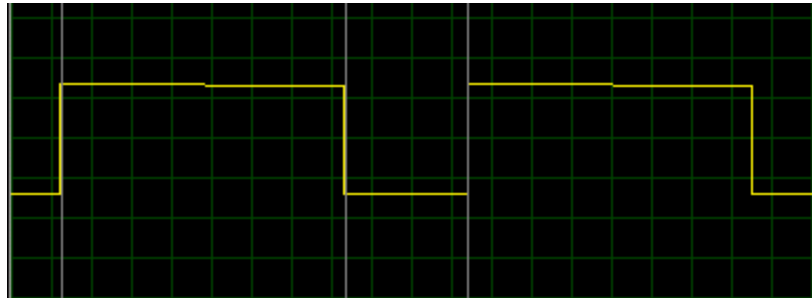
MBSD

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Deadline: 10pm on 25/4/2021

- A. Generate a signal on pin P1.1 having frequency equal to 100 Hz with a duty cycle of 70%.  
$$\text{Duty cycle} = [(up\ time)/(total\ time)] * 100$$
- B. Show it on oscilloscope.
- C. Code it,
  - a. Assembly
  - b. C



- D. Is there any relationship between duty cycle and frequency, if yes then what it is?