**Seven Segment Digital Clock**

In this ATMega32 AVR project we will be designing and implementing a digital clock with the aid of a Atmel AVR ATMega32 microcontroller and Seven Segment Displays

**Digital Clock Hardware Description**

The figure below gives the circuit diagram for the ATmega16 Seven Segment Display Digital Clock. One feature to note in that all seven segment displays are driven by the same port **(PortB)**. The microcontroller through controls from **PortC** indicate which seven segment display each digit is displayed on. Note that these Seven Segment Displays are **common cathode** display.

