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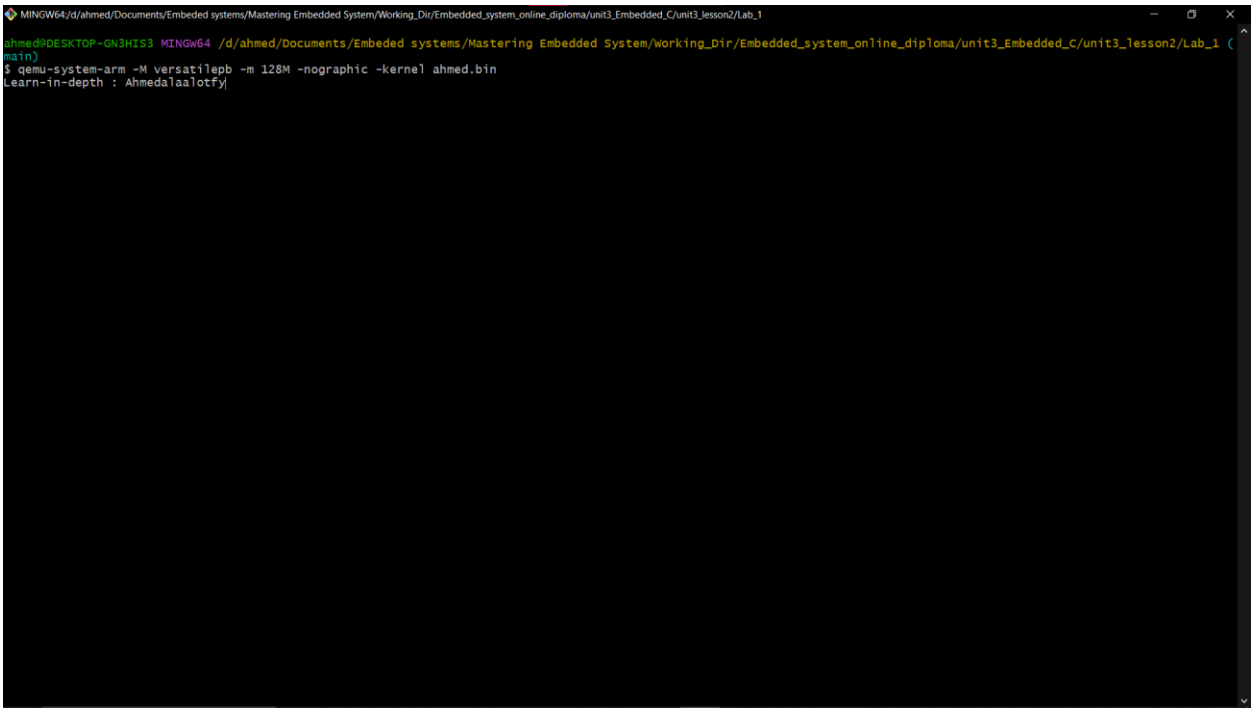
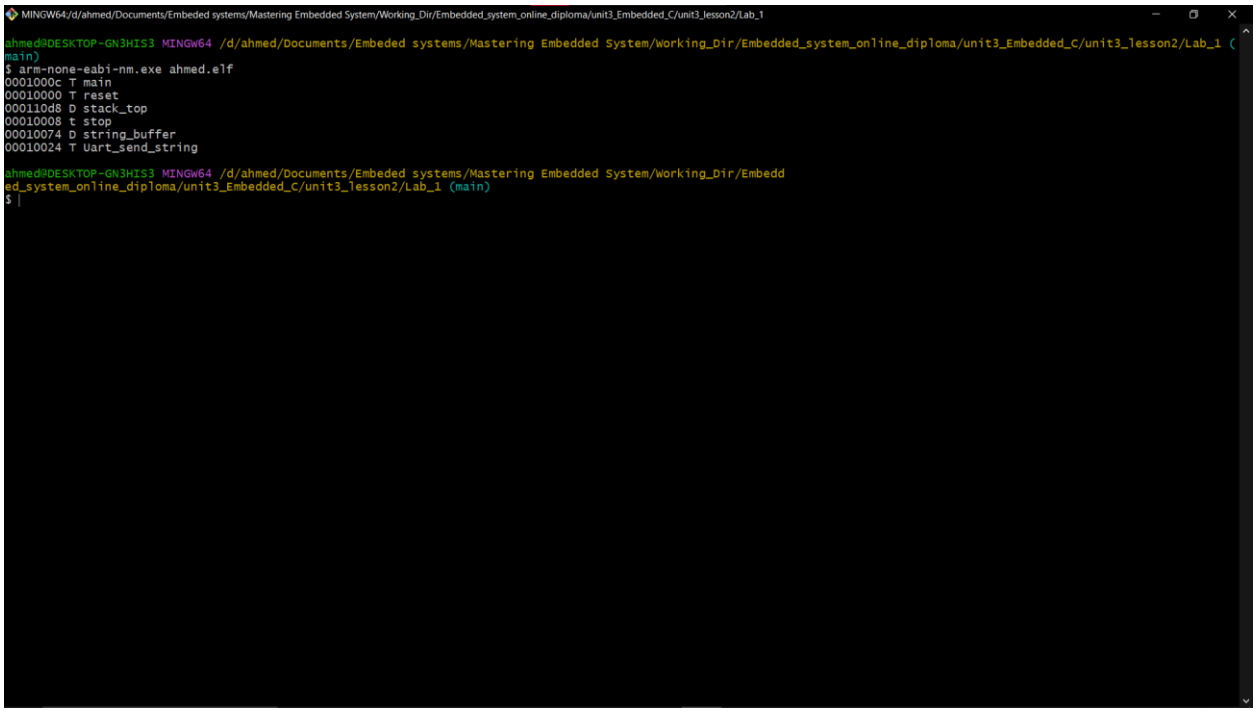
QEMU run the SW



Symbol table



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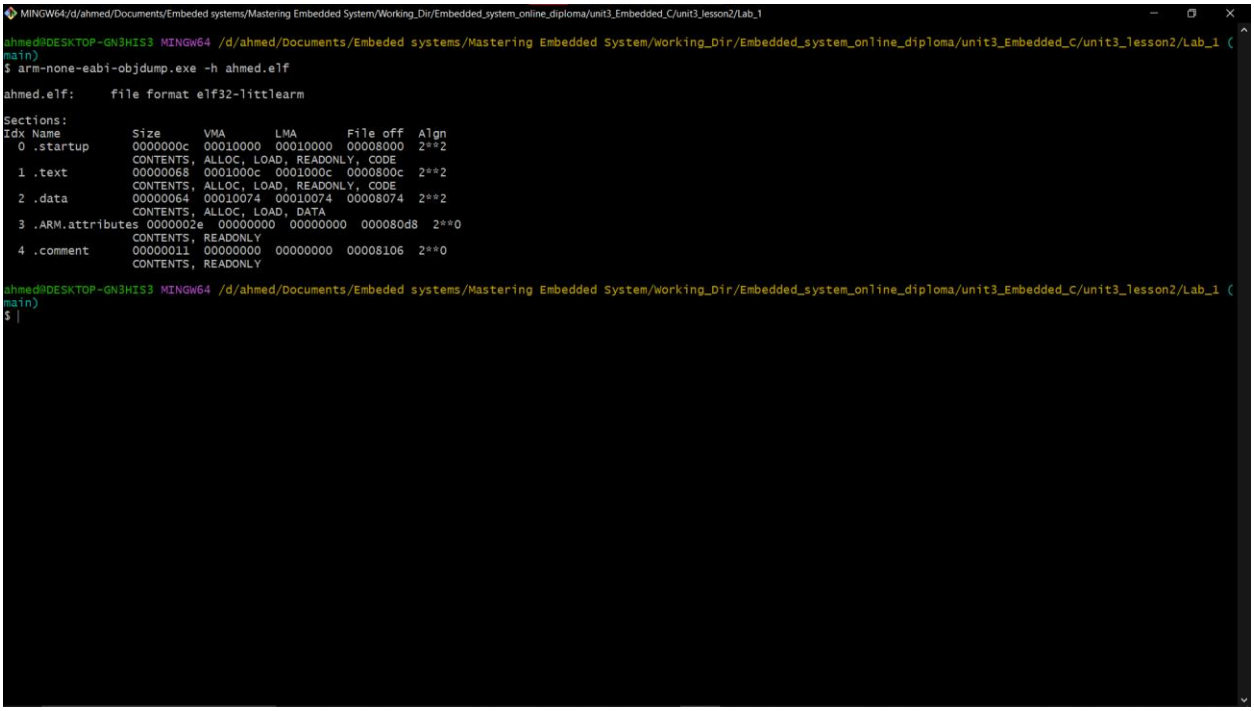
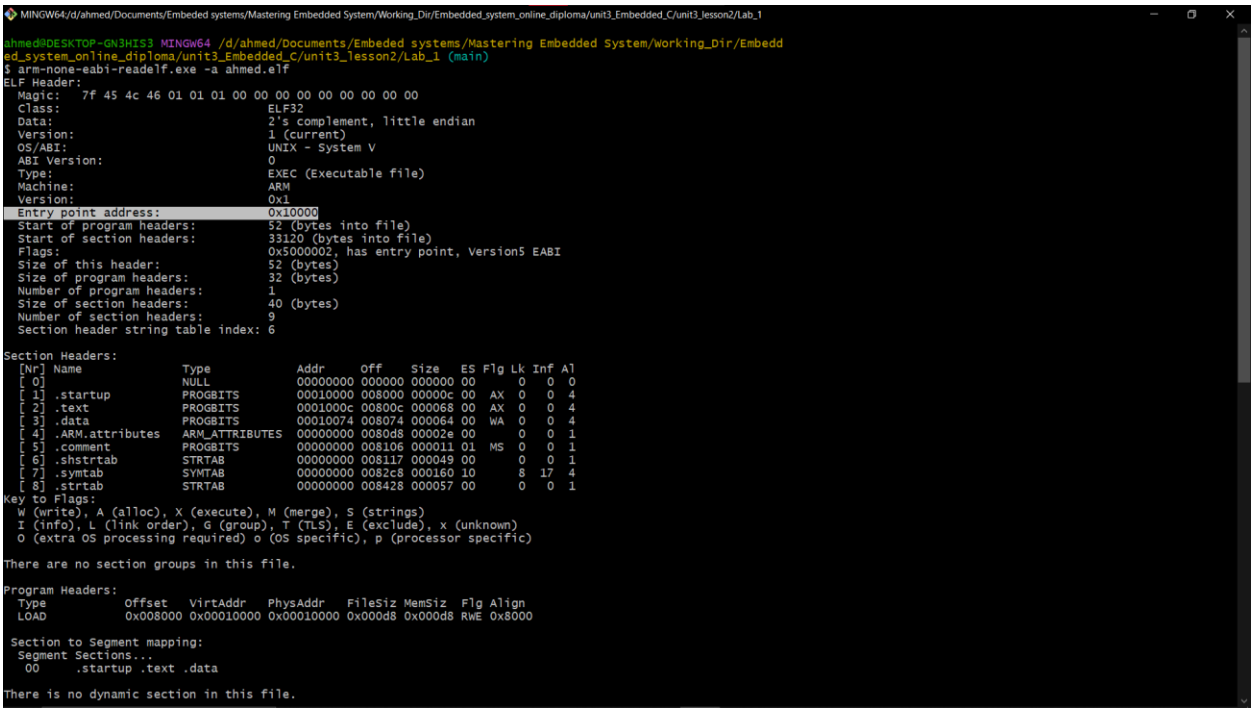
Sections



Readelf for ahmed.elf and check entry point



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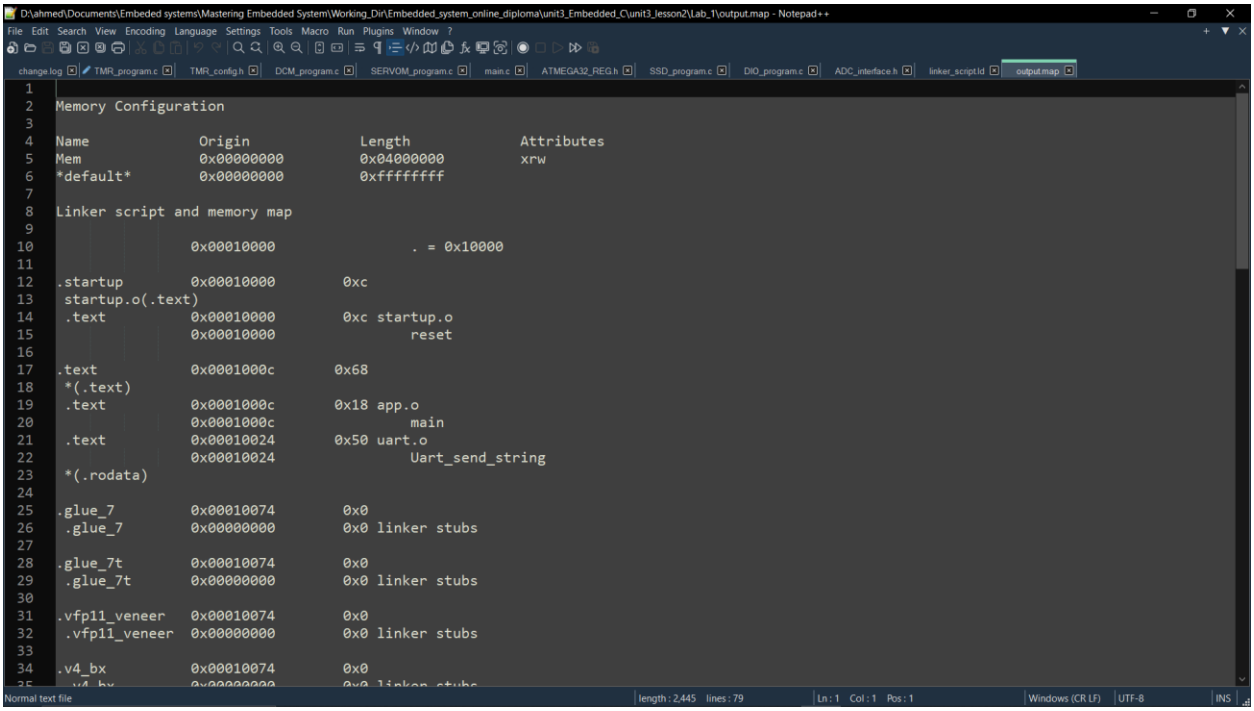
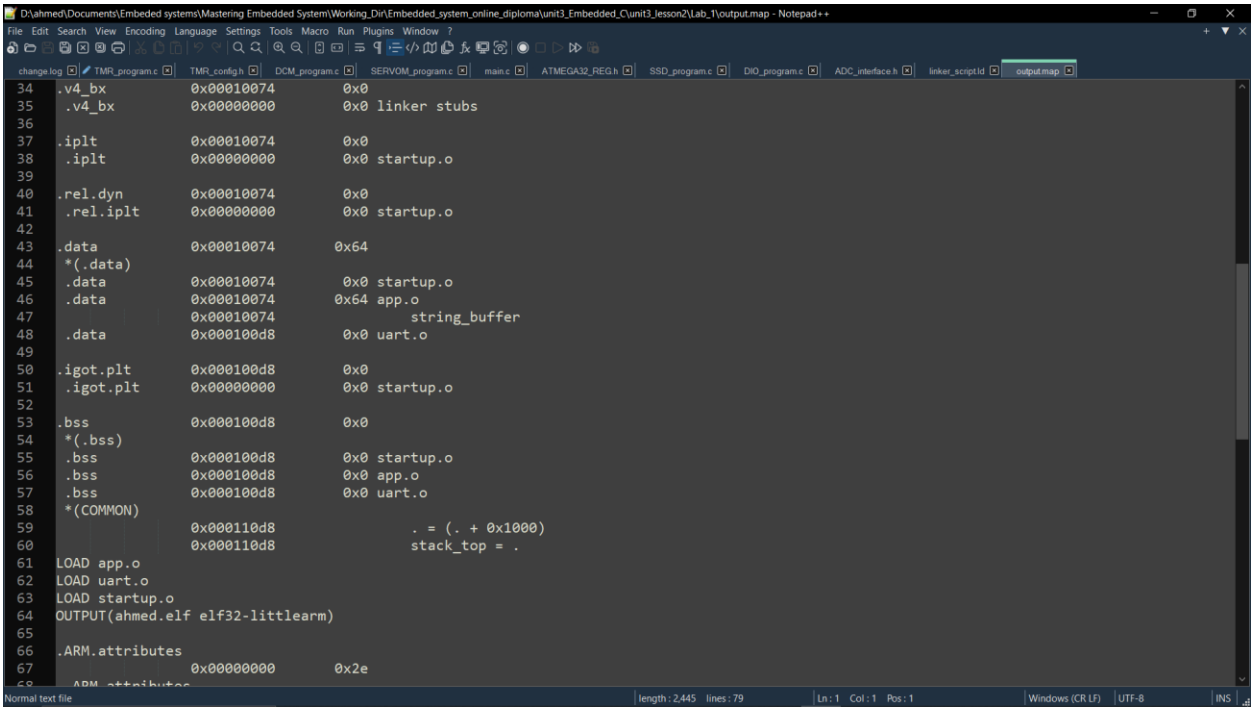
Map file



```
Memory Configuration

Name             Origin             Length             Attributes
Mem              0x00000000         0x04000000         xrw
*default*        0x00000000         0xffffffff

Linker script and memory map

                 0x00010000                 . = 0x10000

.startup         0x00010000         0xc
 startup.o(.text)
 .text           0x00010000         0xc startup.o
                 0x00010000                 reset

.text            0x0001000c        0x68
 *(.text)
 .text           0x0001000c        0x18 app.o
                 0x0001000c                 main
 .text           0x00010024        0x50 uart.o
                 0x00010024                 Uart_send_string
 *(.rodata)

.glue_7          0x00010074         0x0
 .glue_7         0x00000000         0x0 linker stubs

.glue_7t         0x00010074         0x0
 .glue_7t        0x00000000         0x0 linker stubs

.vfp11_veneer    0x00010074         0x0
 .vfp11_veneer   0x00000000         0x0 linker stubs

.v4_bx           0x00010074         0x0
```



```
.v4_bx           0x00010074         0x0
 .v4_bx          0x00000000         0x0 linker stubs

.iplt            0x00010074         0x0
 .iplt           0x00000000         0x0 startup.o

.rel.dyn         0x00010074         0x0
 .rel.iplt       0x00000000         0x0 startup.o

.data            0x00010074        0x64
 *(.data)
 .data           0x00010074         0x0 startup.o
 .data           0x00010074        0x64 app.o
                 0x00010074                 string_buffer
 .data           0x000100d8         0x0 uart.o

.igot.plt        0x000100d8         0x0
 .igot.plt       0x00000000         0x0 startup.o

.bss             0x000100d8         0x0
 *(.bss)
 .bss            0x000100d8         0x0 startup.o
 .bss            0x000100d8         0x0 app.o
 .bss            0x000100d8         0x0 uart.o
 *(COMMON)
                 0x000110d8                 . = (. + 0x1000)
                 0x000110d8                 stack_top = .
LOAD app.o
LOAD uart.o
LOAD startup.o
OUTPUT(ahmed.elf elf32-littlearm)

.ARM.attributes
                 0x00000000        0x2e
```