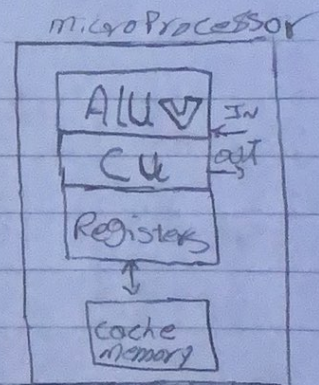
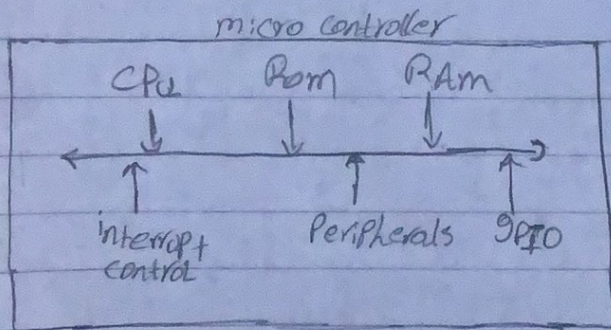


أهم ما في الـ (لغتي) ① learn in depth

- Micro Processor (MP): it's a IC (integrated circuit) used for general purpose functions and consist of mostly for ALU + Control Unit + Register + small memory

- micro controller: MP + Peripherals + Memory



- Embedded system: it's a system like computer But smaller. Used for specific purpose and with more constraints in time, speed and storage

- mechatronics system: it's a system that contain a mechanical part driven with controller and contain also Electrical part.

- n-bit Processor: it's a Processor that understand a word with n-bits only, and any larger word must be broken into pieces

learn in depth

(2)

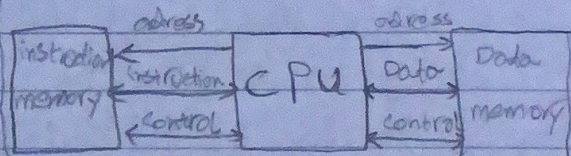
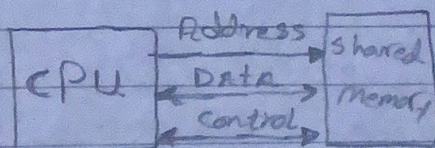
Microprocessor vs Microcontroller

- Microprocessor vs Microcontroller:

General Purpose	Specific Purpose
Part of microcontroller	Contain microprocessor
Can add to it more RAM and memory	Can't add any peripherals to it

- Von-Neuman Vs Harvard Architecture

instruction and data share the same memory device and busses	instruction and data has separate busses and memories
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- Rom Types:

- 1- PROM: Read only memory, it can be programmed only once by the user
- 2- MaskROM: same as PROM but it can be programmed once by manufacturer not user
- 3- EPROM: it can be programmed many times but it's not electrically erasing, it can be fully erased by UV-EPROM

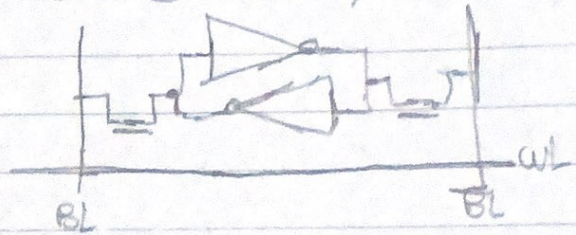
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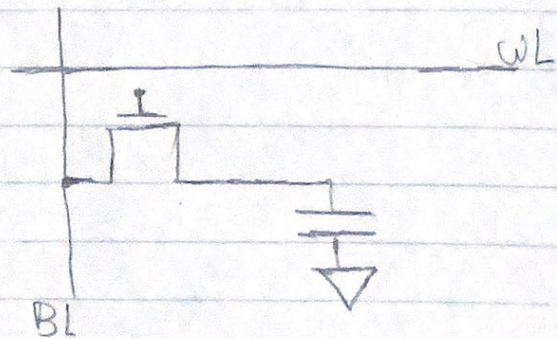
عزیز کاروانین لطفاً

RAM Types:

1- SRAM: static RAM because it doesn't need to be refreshed, it consists of 6 transistors so it's high cost but faster and better than DRAM, used for cache memory



2- DRAM: Dynamic RAM because it needs to be refreshed because of leakage of the voltage, consists of 1 transistor and 1 capacitor so it's cheaper than SRAM but slower and more power consumption because of refreshing process, and used in main memory



Why ROM is Read only Memory Although we can write on it?

It can be written on it by itself, but by another devices.

learn in depth

So go in side of ?

Type	Volatility	Writable	Erase size	Wipe cycle	cost (Per byte)	Speed
SRAM	Yes	Yes	Byte	unlimited	Expensive	Fast
DRAM	Yes	Yes	Byte	unlimited	Moderate	Moderate
Masked Rom	No	No	N/A	N/A	Inexpensive	Fast
PROM	No	Once	N/A	N/A	Moderate	Fast
EEPROM	No	Once	Entire chip	Limited	Moderate	Fast
Flash	No	Yes	Byte	Limited	Expensive	Fast to Read slow to write/erase
NVRAM	No	Yes	Byte	unlimited	Expensive (SRAM + Battery)	Fast

⑦