AN4488 Contents

List of tables AN4488

AN4488 Rev 7 5/50

Reference documents AN4488

Power supplies AN4488

AN4488 Power supplies

Power supplies AN4488

AN4488 Power supplies

Power supplies

AN4488 Rev 7 13/50

Note: Please contact your local STMicroelectronics representative or visit www.st.com



AN4488 Rev 7 17/50

AN4488 Rev 7 19/50

Package AN4488

4 Package

4.1 Package Selection

Package should be selected by taking into



AN4488 Package

AN4488 Rev 7 23/50

Package AN4488 AN4488 Package

Package AN4488

AN4488 Package

Package AN4488

AN4488 Debug management

5 Debug management

The Host/Target interface is the hardware equipment that connects the host to the application board. This interface is made of



AN4488 Rev 7 29/50

Debug management AN4488



Clocks AN4488

AN4488 Clocks

6.2 LSE OSC clock

The low-speed external clock signal (LSE) can be generated from two possible clock sources:

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33/50

38/50 AN4488 Rev 7

AN4488 Rev 7 39/50

AN4488 Rev 7 41/50

42/50 AN4488 Rev 7

AN4488 Rev 7 43/50

AN4488 FAQ



AN4488 Conclusion

AN4488

50/50 AN4488 Rev 7