
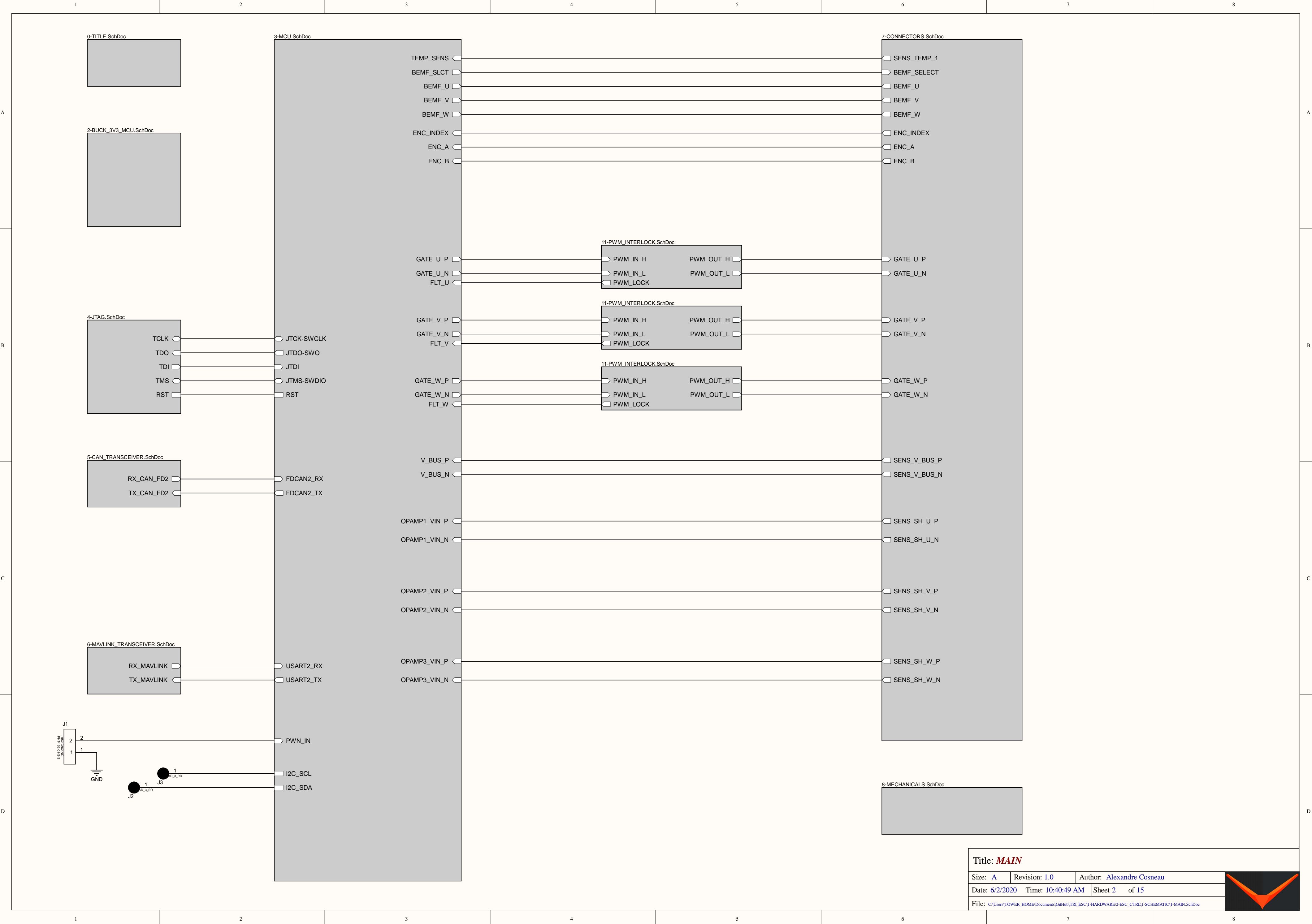
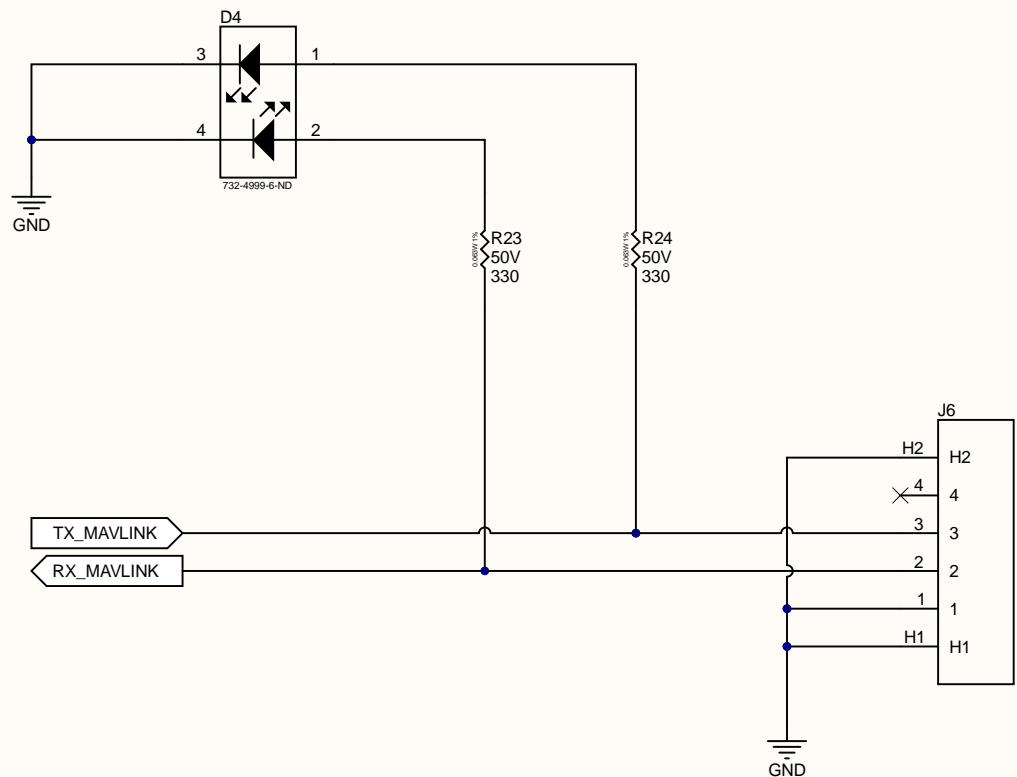


1		2		3		4		5		6													
A		ESC GaN V1										REVISION HISTORY											
												REV	COMMENT		DATE								
B																							
C																							
D												Title: ESC GaN V1											
												Size: A		Revision: 1.0		Author: Alexandre Cosneau							
												Date: 6/2/2020		Time: 10:40:49 AM		Sheet 1 of 15							
												File: 0-TITLE.SchDoc											
												1		2		3		4		5		6	





Title: **MAVLINK TRANSCEIVER**



UNIVERSITÉ DE
SHERBROOKE

Size: E

Revision: 1.0

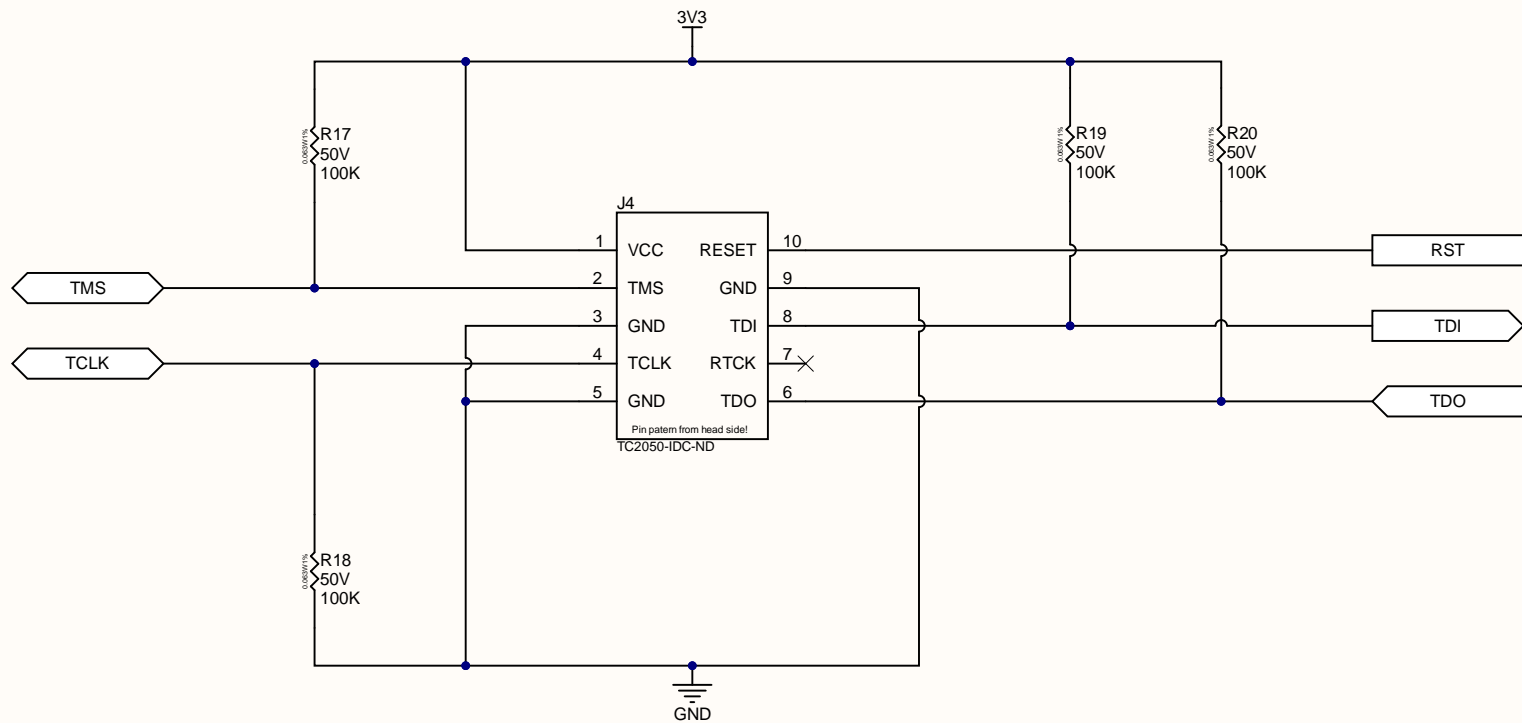
Author: Alexandre Cosneau

Date: 6/2/2020 Time: 10:40:49 AM

Sheet 12 of 15

File: C:\Users\TOWER_HOME\Documents\GitHub\TRI_ESC\1-HARDWARE\2-ESC_CTRL\1-SCHEMATIC\6-M





Title: **JTAG**

Size: A

Revision: 1.0

Author: Alexandre Cosneau

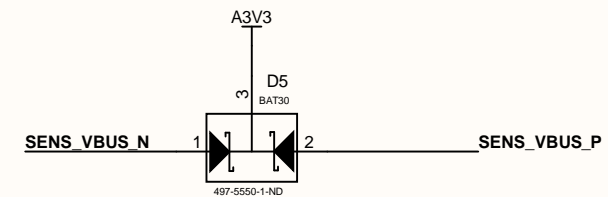
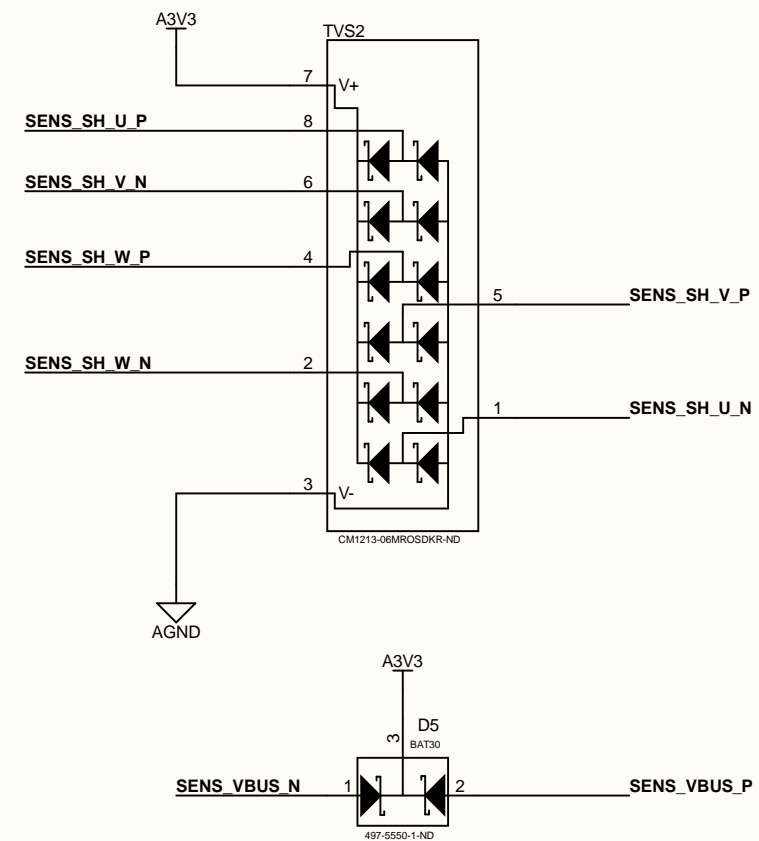
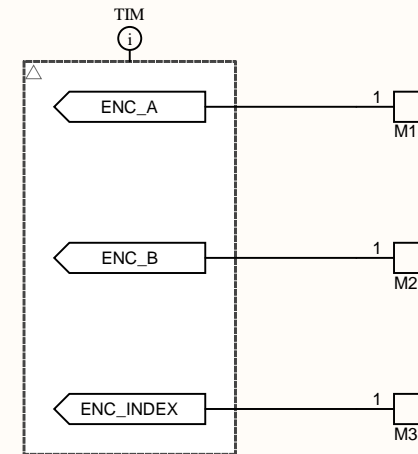
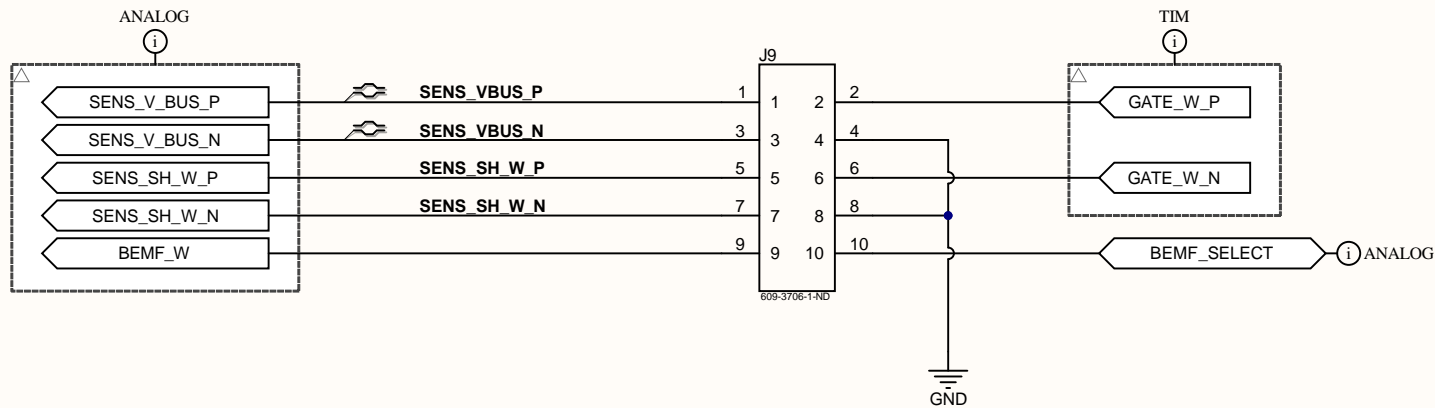
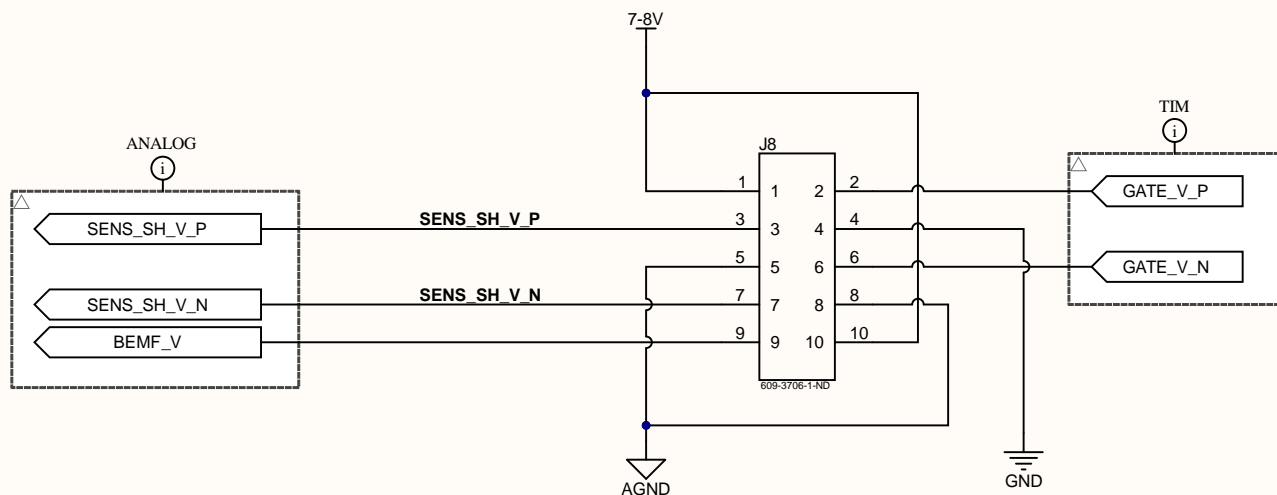
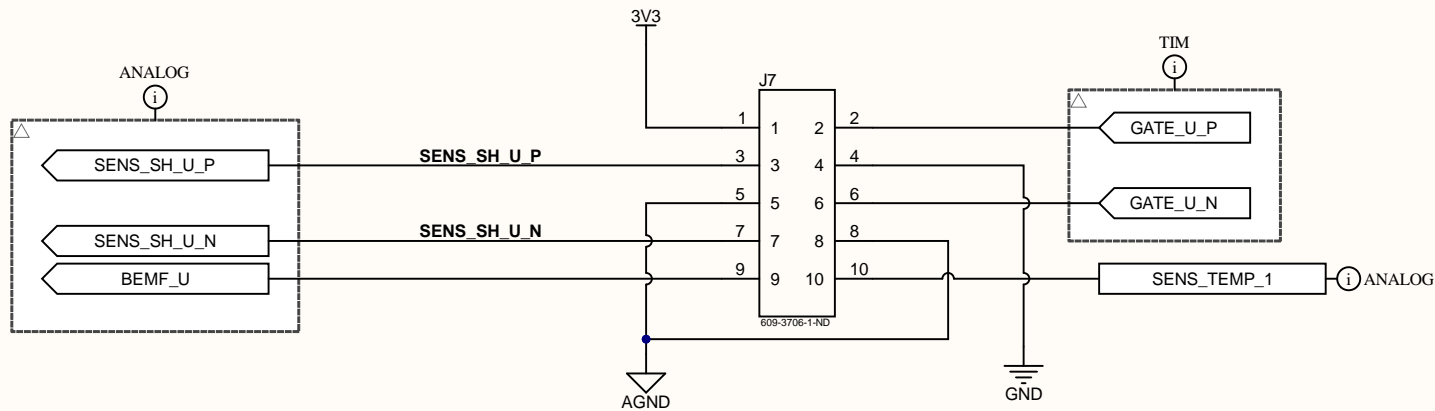
Date: 6/2/2020

Time: 10:40:49 AM

Sheet 13 of 15

File: 4-JTAG.SchDoc





CAREFULL ON PIN OUT! FEMELLE BOARD UPSIDE DOWN

1

2

3

4

A

A

B

B

C

C

D

D

FID1

FID2

FID3

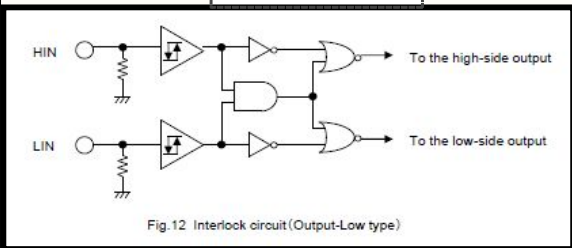
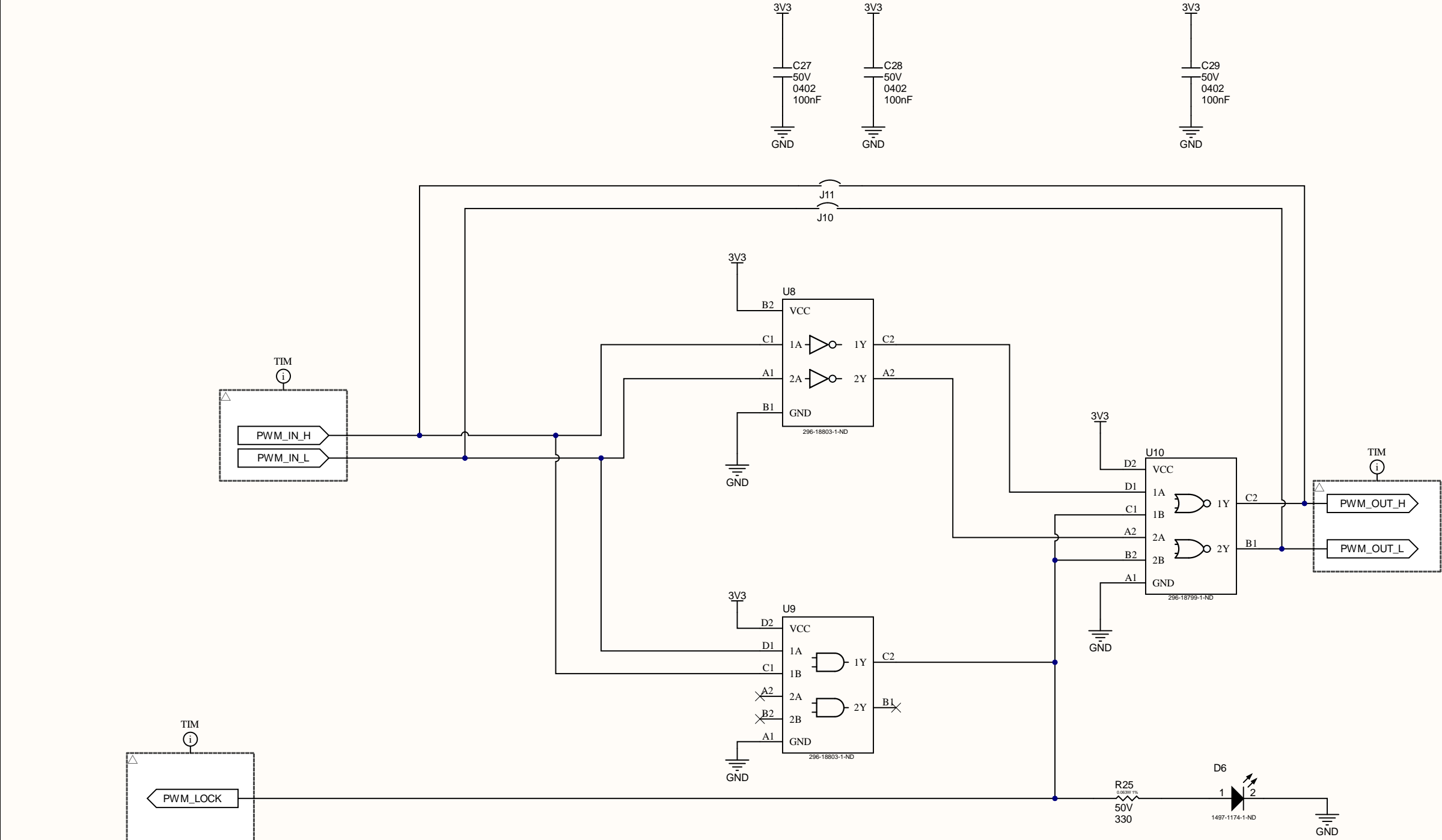
Title: ***MECHANICALS***

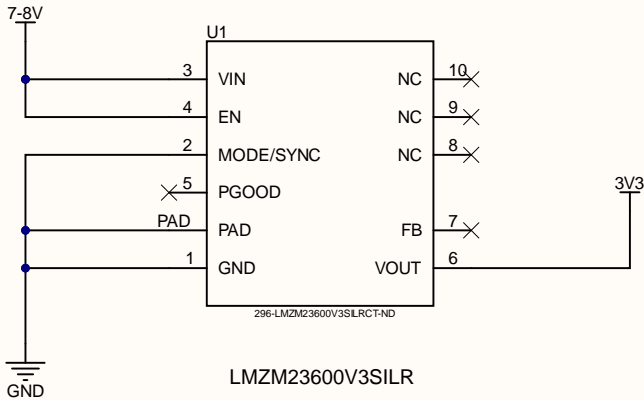
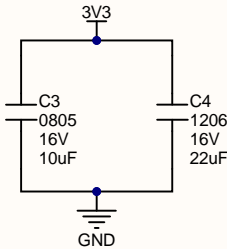
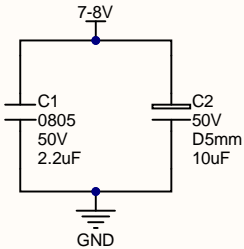
Size: A	Revision: 1.0	Author: Alexandre Cosneau
---------	---------------	---------------------------

Date: 6/2/2020	Time: 10:40:50 AM	Sheet 15 of 15
----------------	-------------------	----------------

File: 8-MECHANICALS.SchDoc



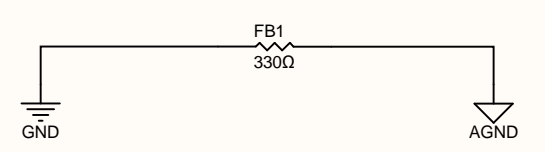
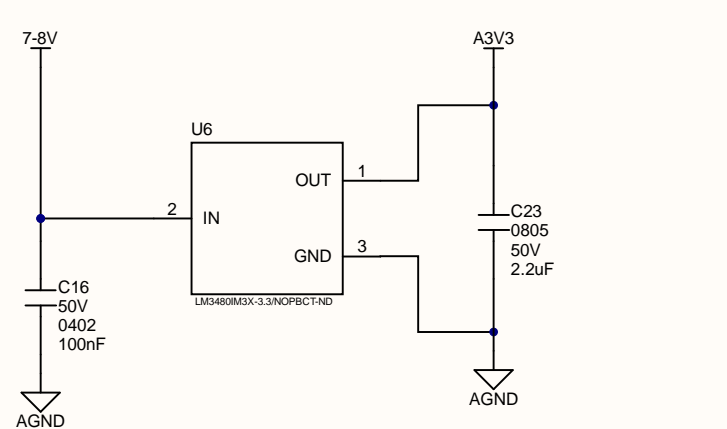
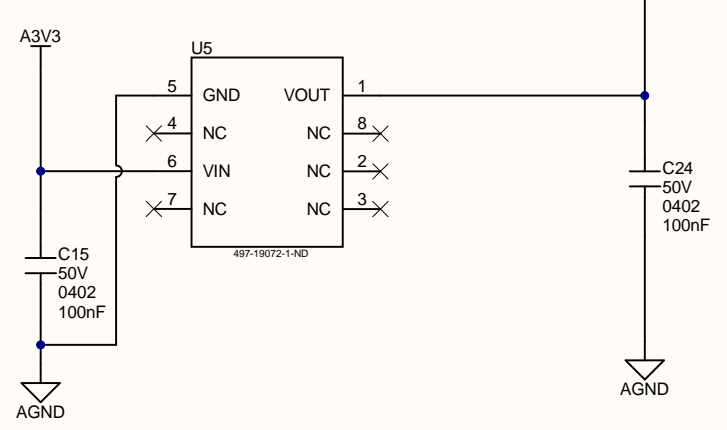
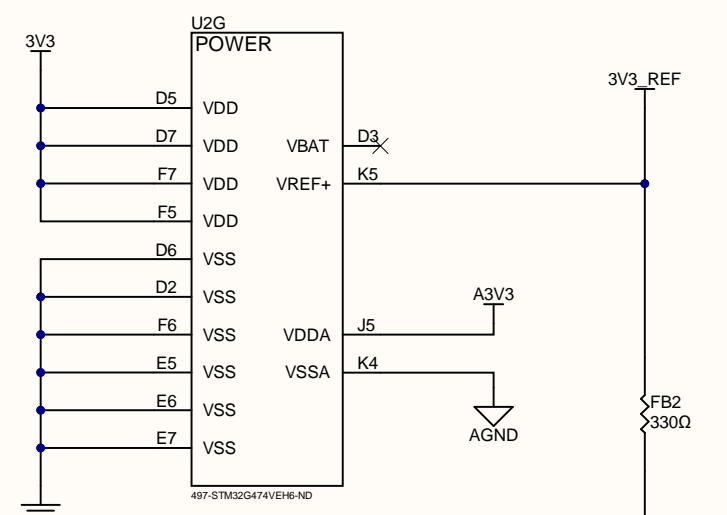
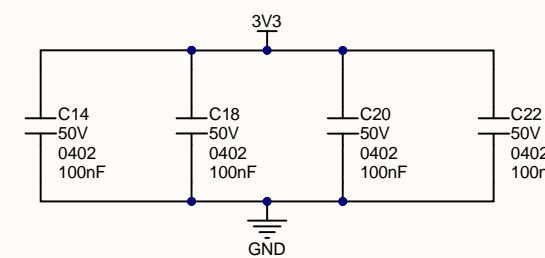
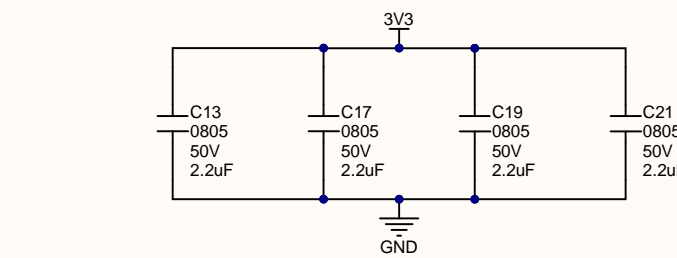
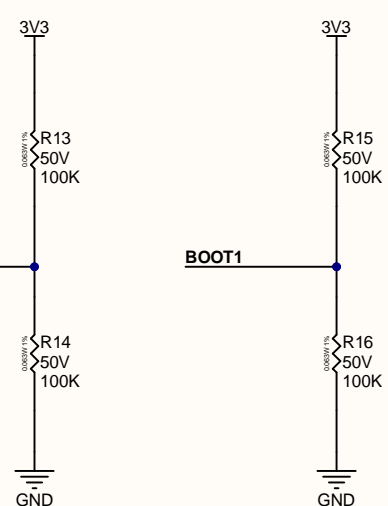
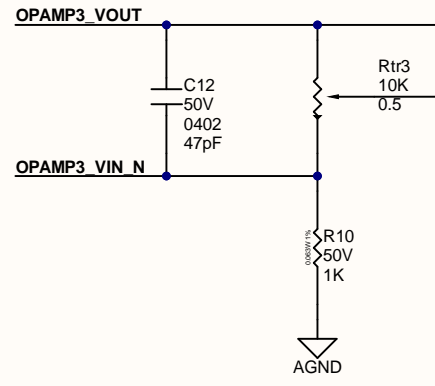
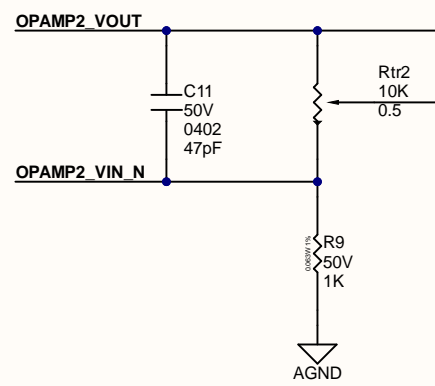
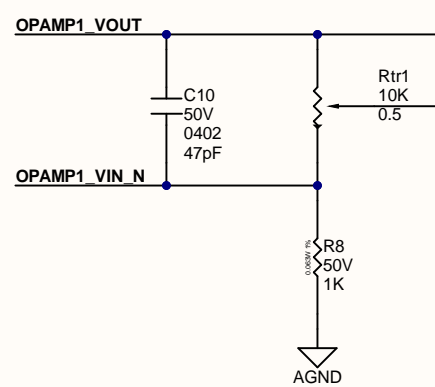
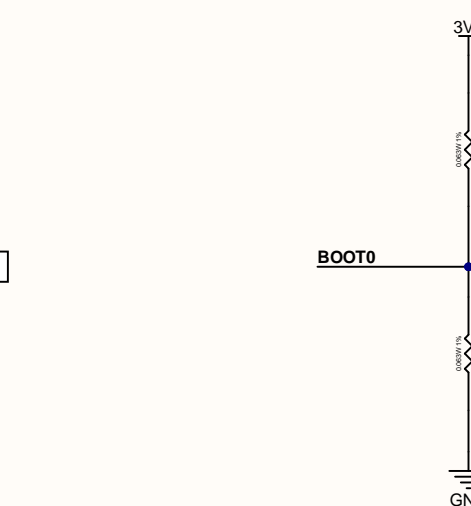
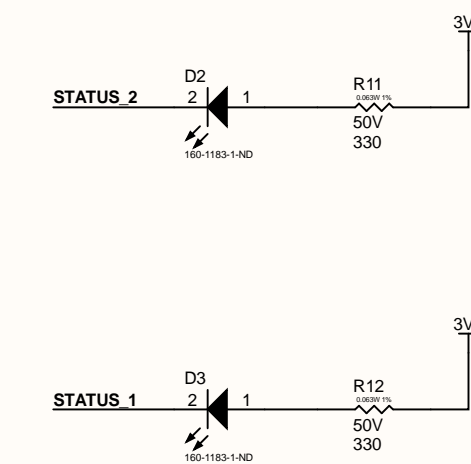
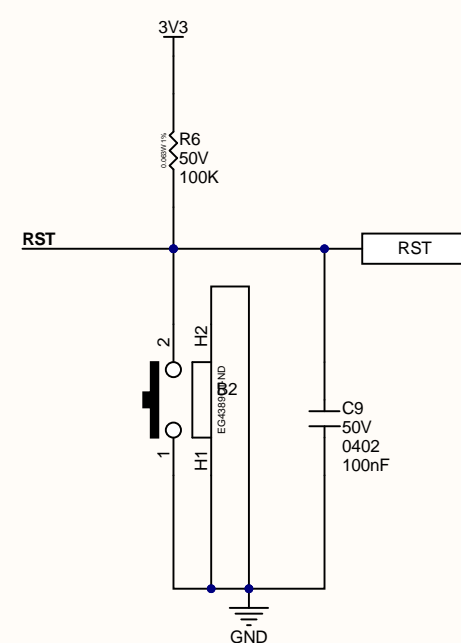
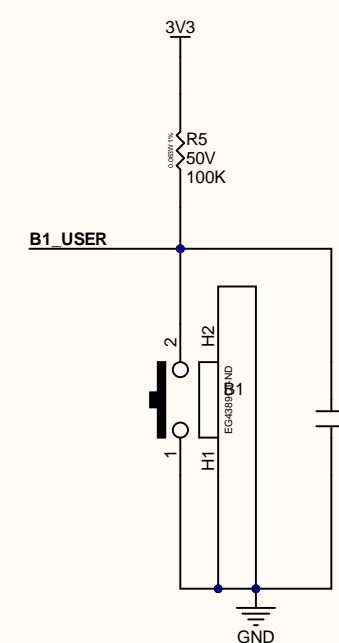
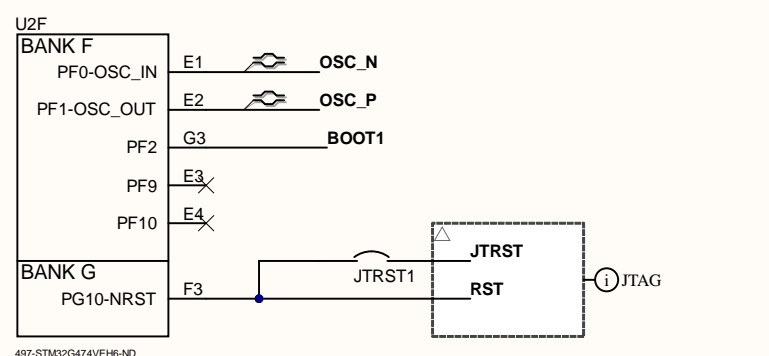
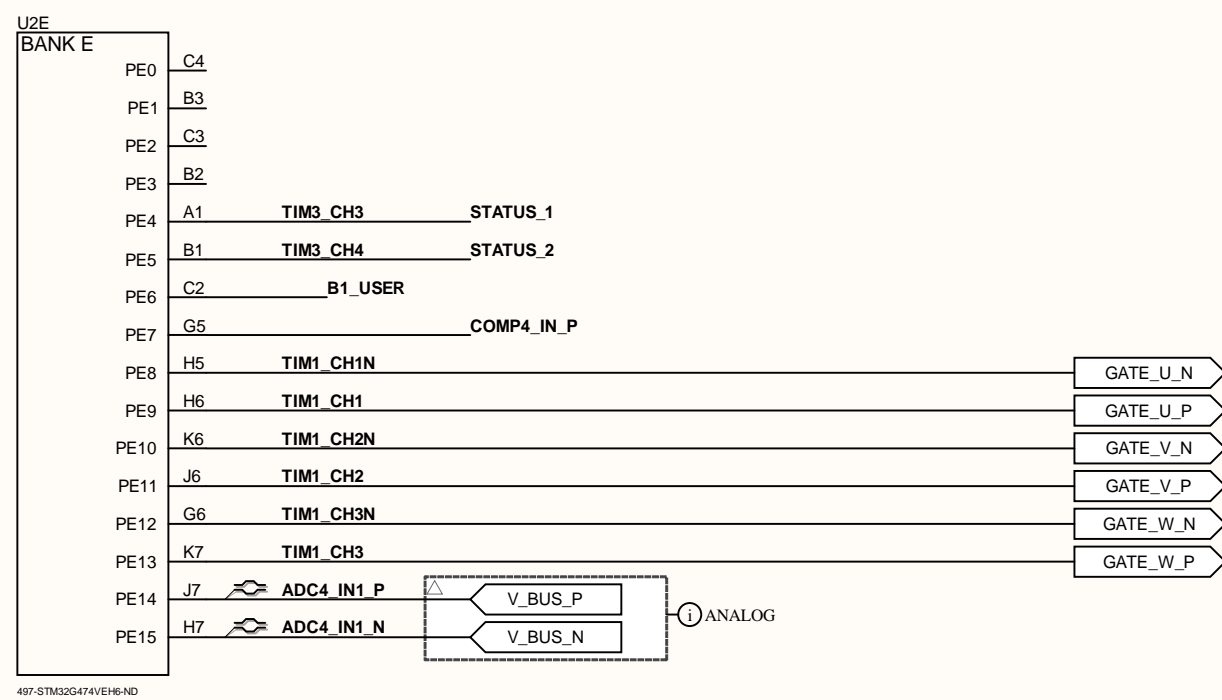
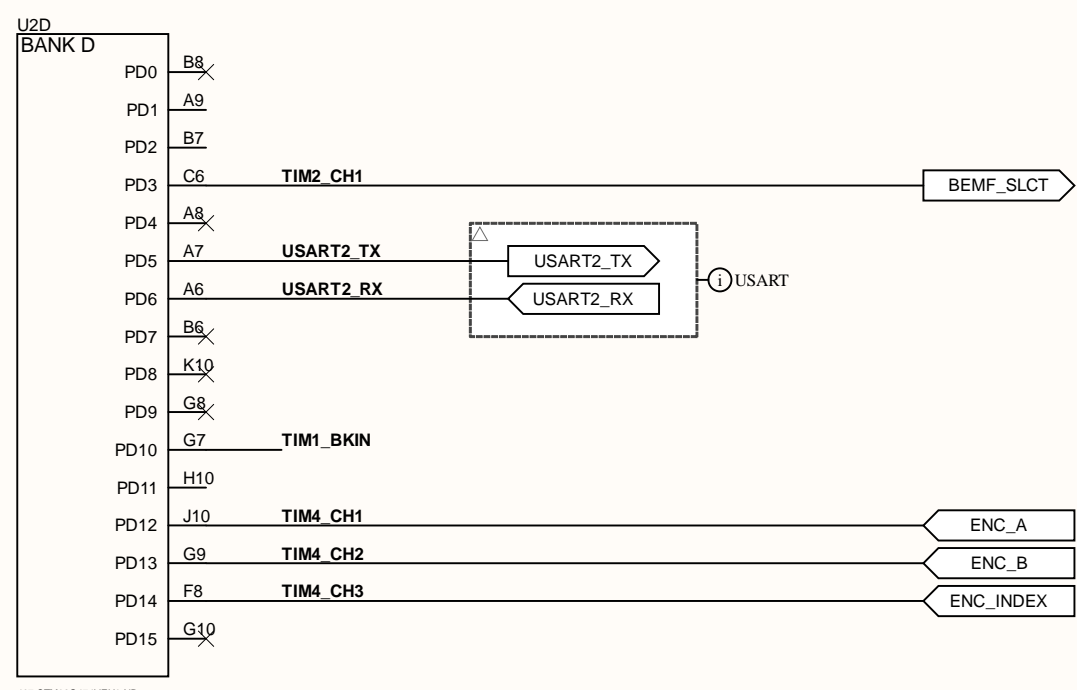
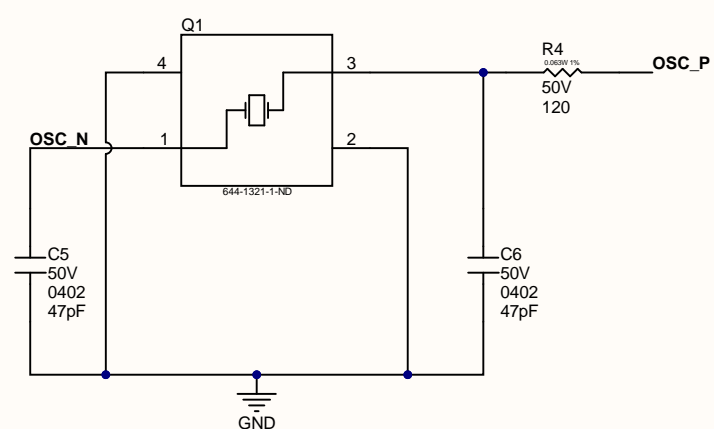
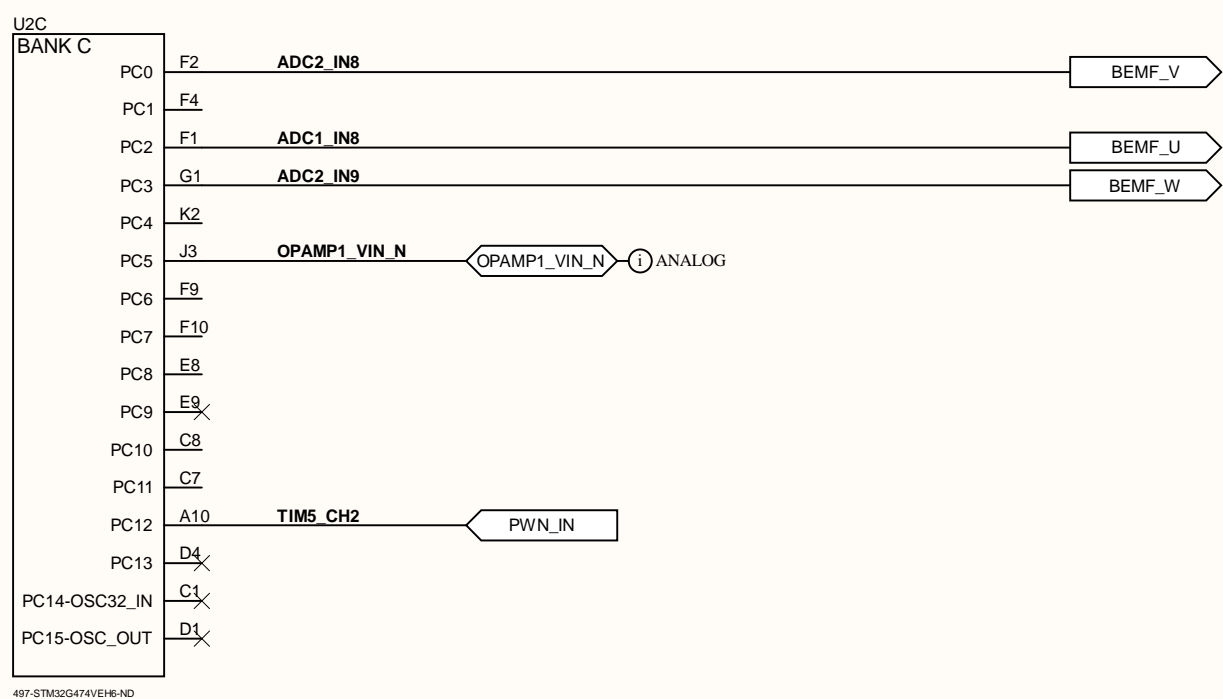
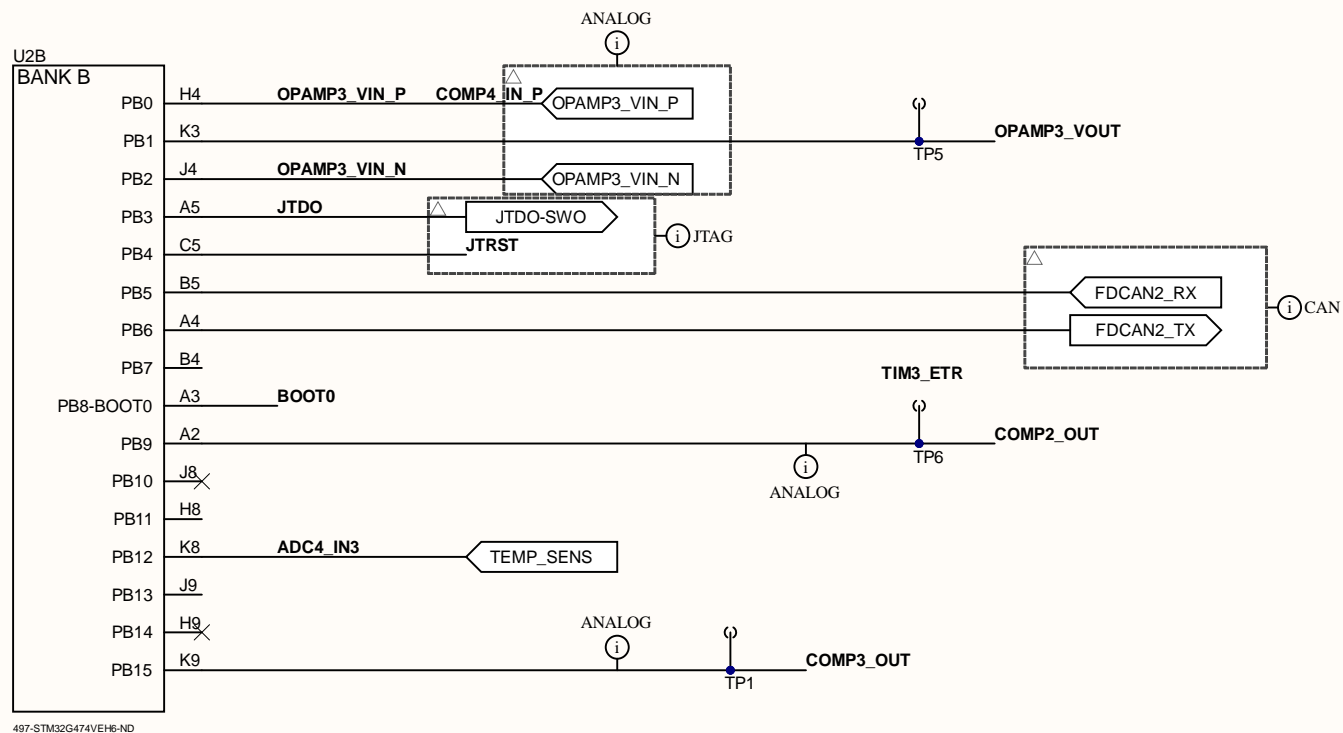
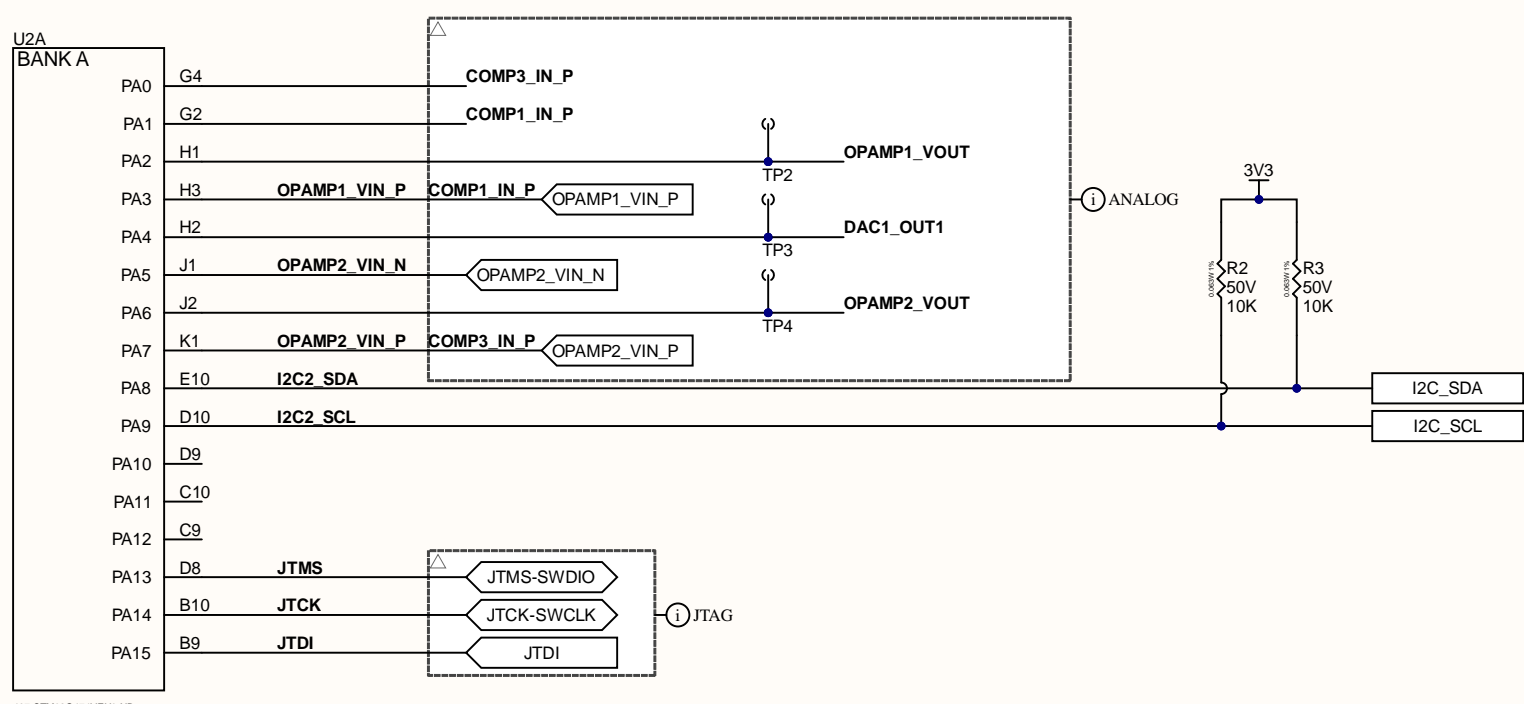




Title: **BUCK 3V3 MCU**

Size: A	Revision:	Author: Alexandre Cosneau
Date: 6/2/2020	Time: 10:40:50 AM	Sheet of
File: 2-BUCK_3V3_MCU.SchDoc		





Boot mode selection pins		Boot mode	Aliasing
BOOT1	BOOT0		
x	0	Main Flash memory	Main Flash memory is selected as the boot space
0	1	System memory	System memory is selected as the boot space
1	1	Embedded SRAM	Embedded SRAM is selected as the boot space