Power supplies

Note: *Please contact your local STMicroelectronics representative or visit www.st.com*

4 Package

4.1 Package Selection

Package should be selected by taking into

5   Debug management

The Host/Target interface is the hardware equipment that connects the host to the application board. This interface is made of

6.2   LSE OSC clock

The low-speed external clock signal (LSE) can be generated from two possible clock sources:

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