
The availability of break inputs and the break sources depends on the selected STM32 family.
This is summarized in [Table 3](#).

Table 3. Peripherals availability per STM32 devices

STM32F0	STM32F1	STM32F2	STM32F3	STM32F4	STM32
---------	---------	---------	---------	---------	-------









