

```
VSIM 10> vsim -gui work.tb_MUX4tol
# vsim -gui work.tb_MUX4tol
# Loading sv_std.std
# Loading work.tb_MUX4tol
# Loading work.MUX4tol
VSIM 11> run
# Starting MUX testbench...
# Sel=00 -> Output=11
# Sel=01 -> Output=22
# Sel=10 -> Output=33
# Sel=11 -> Output=44
# MUX test completed.
# ** Note: $finish      : D:/modelsim/tb_MUX4tol.sv(47)
#   Time: 50 ns   Iteration: 0   Instance: /tb_MUX4tol
# 1
# Break in Module tb_MUX4tol at D:/modelsim/tb_MUX4tol.sv line 47

# Loading work.tb_ALU
# Loading work.ALU
add wave -position insertpoint sim:/tb_ALU/*
VSIM 17> run
# Starting ALU testbench...
# ALU_Sel=000 | A=10 | B=3 | Result=13 | Carry=0 | Zero=0
# ALU_Sel=001 | A=10 | B=3 | Result=7 | Carry=0 | Zero=0
# ALU_Sel=010 | A=10 | B=3 | Result=2 | Carry=0 | Zero=0
# ALU_Sel=011 | A=10 | B=3 | Result=11 | Carry=0 | Zero=0
# ALU_Sel=100 | A=10 | B=3 | Result=9 | Carry=0 | Zero=0
# ALU_Sel=101 | A=10 | B=3 | Result=244 | Carry=1 | Zero=0
# ALU_Sel=110 | A=10 | B=3 | Result=20 | Carry=0 | Zero=0
# ALU_Sel=111 | A=10 | B=3 | Result=5 | Carry=0 | Zero=0
# ALU test completed.
# ** Note: $finish      : D:/modelsim/tb_ALU.sv(45)
#   Time: 90 ns   Iteration: 0   Instance: /tb_ALU
```