# Digital Signal Processing (DSP)

Lecture 1
Analog to Digital Converter

#### Dr. Ahmed Said Eltrass

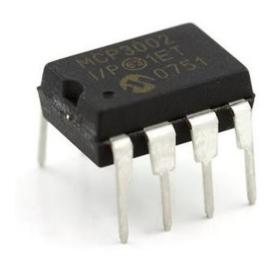
Electrical Engineering Department Alexandria University, Alexandria, Egypt

Email: ahmed4@vt.edu

Office hours: Wednesday 12:00 p.m. to 01:30 p.m.

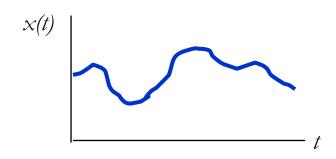
4<sup>th</sup> floor, Electrical Engineering Building

# Analog-to-Digital Converter (ADC)



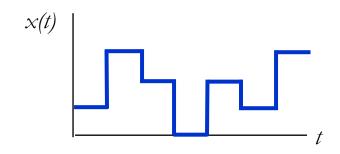
## Type of signals

- Analog signals
  - Value varies continuously over a continuous range
  - Examples of analog data
     Video, Audio

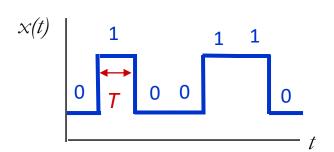


- Quantized signals
  - Value limited to a finite set
  - Examples of digital data

Text: printed English language (26 letters, 10 numbers, space, and punctuation)

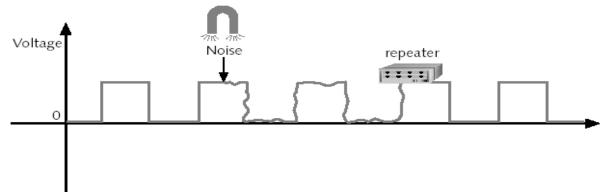


- Binary/Digital signals
  - Has at most 2 values (on and off)
  - Used to represent bit values
  - Computers can only perform processing on digitized signals

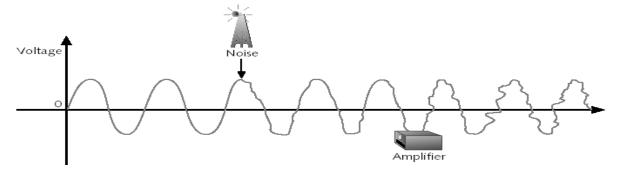


## Analog versus digital

- Digital signals can be regenerated using repeaters
  - Cleaned up to prevent the accumulation of noise and distortion
  - Allows signal to be transmitted over greater distances



 What happens to analog signals over distances even if they are amplified? Can you reconstruct the original signal



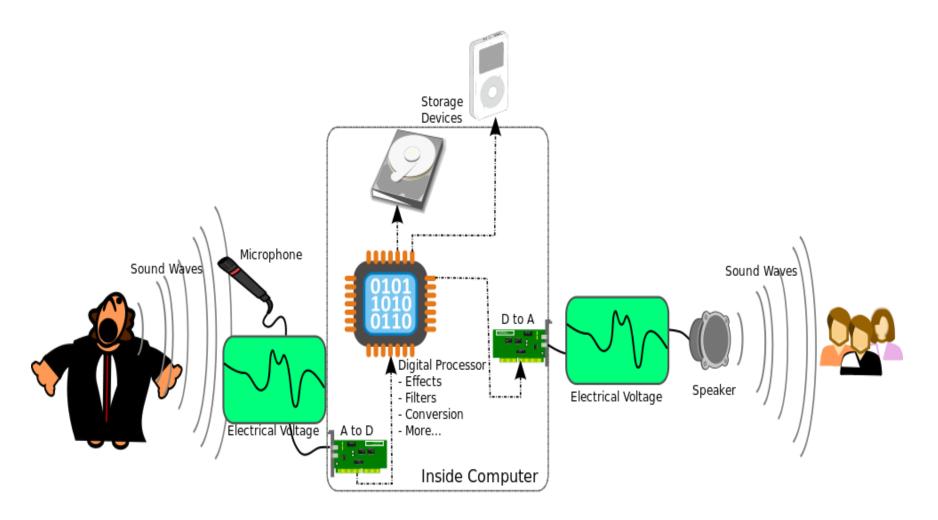
## Advantages of Digital Signals

- Digital circuits have only two states so:
  - Changes in value have little effect on digital signals
  - Noise and other forms of interference have little effect on digital signals
  - Little chance of error because voltage in a digital circuit must be in one state or the other
  - Information storage is easy
  - Operation can be readily programmed
  - Can fabricate more digital circuitry onto integrated circuits

## Disadvantages of Digital Signals

- The ONE major disadvantage is that the real-world is analog in nature
- When dealing with analog inputs and outputs you will always have to:
- 1) convert analog to digital (ADC)
- 2) process the digital data
- 3) convert the digital data back to analog output (DAC)

# Example

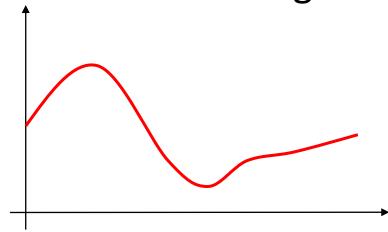


# Analog-Digital Converter (ADC)

- An electronic integrated circuit which converts a signal from analog (continuous) to digital (discrete) form
- Provides a link between the analog world of transducers and the digital world of signal processing and data handling

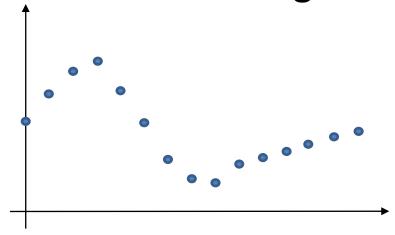
# Analog-Digital Converter (ADC)

- An electronic integrated circuit which converts a signal from analog (continuous) to digital (discrete) form
- Provides a link between the analog world of transducers and the digital world of signal processing and data handling



# Analog-Digital Converter (ADC)

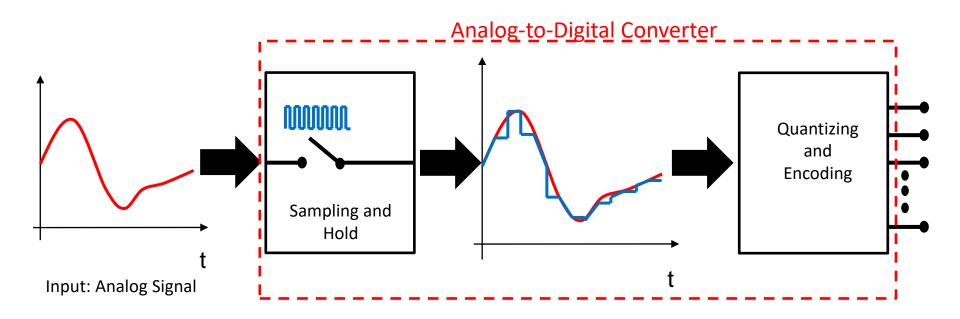
- An electronic integrated circuit which converts a signal from analog (continuous) to digital (discrete) form
- Provides a link between the analog world of transducers and the digital world of signal processing and data handling

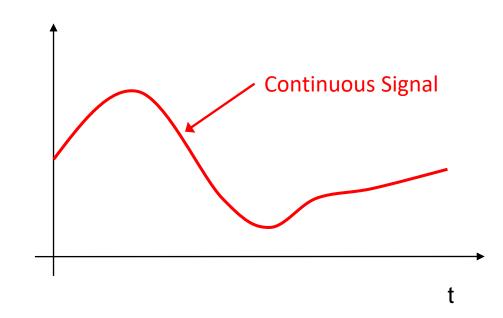


## **ADC Conversion Process**

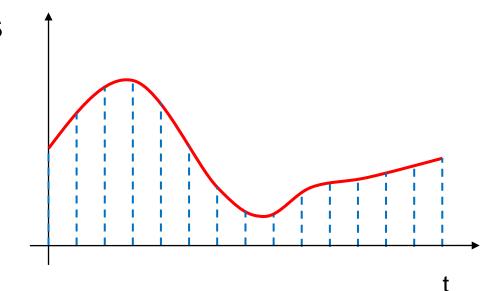
Two main steps of process

- 1. Sampling and Holding
- 2. Quantization and Encoding

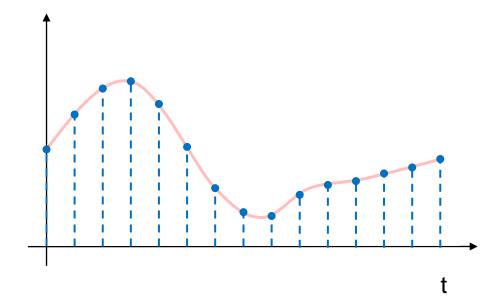




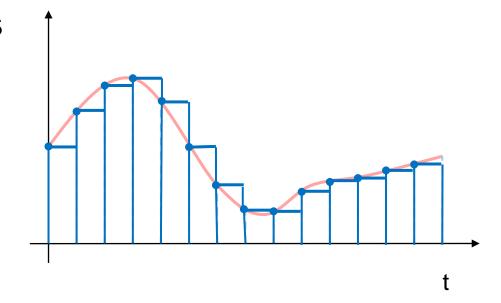
- Measuring analog signals at uniform time intervals
  - Ideally twice as fast as what we are sampling



- Measuring analog signals at uniform time intervals
  - Ideally twice as fast as what we are sampling
- Digital system works with discrete states
  - Taking a sample from each location



- Measuring analog signals at uniform time intervals
  - Ideally twice as fast as what we are sampling
- Digital system works with discrete states
  - Taking samples from each location



- Reflects sampled and hold signal
  - Digital approximation

#### Quantizing

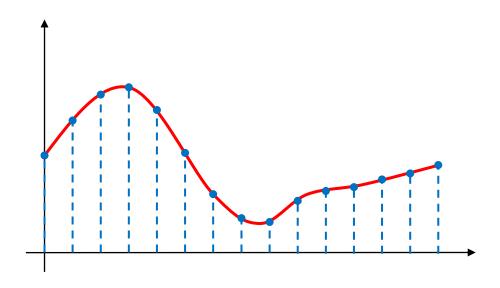
- Separating the input signal into a discrete states with K increments
- K=2<sup>N</sup>
  - N is the number of bits of the ADC
- Analog quantization size
  - $Q=(V_{max}-V_{min})/2^N$
  - Q is the Resolution

#### **Encoding**

 Assigning a unique digital code to each state for input into the microprocessor

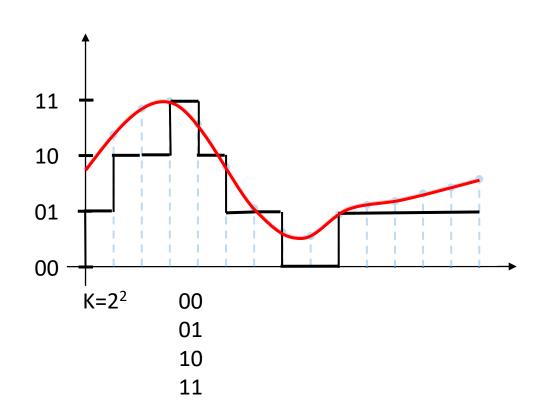
#### **Quantization & Coding**

 Use original analog signal



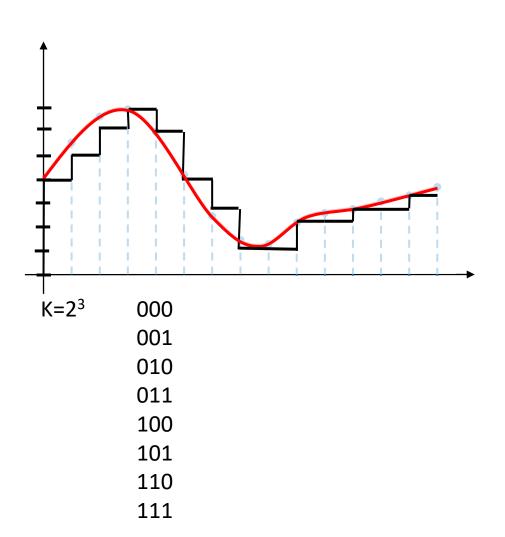
#### **Quantization & Coding**

- Use original analog signal
- Apply 2 bit coding



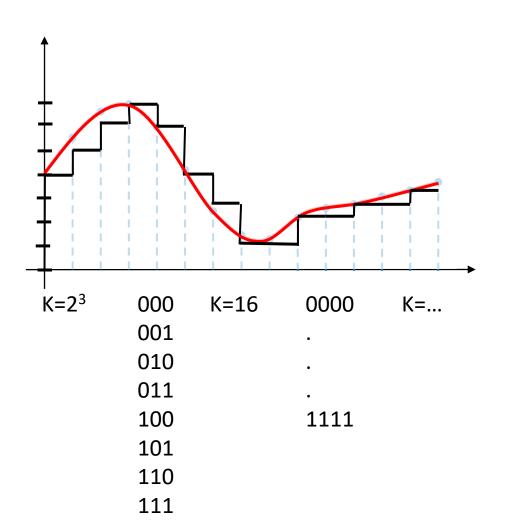
#### **Quantization & Coding**

- Use original analog signal
- Apply 3 bit coding



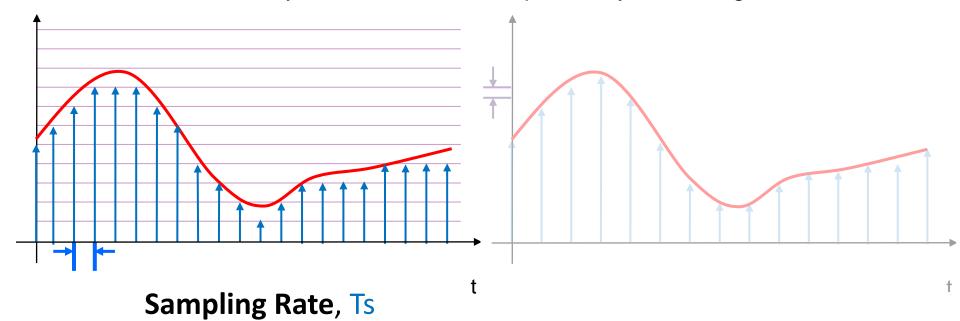
#### **Quantization & Coding**

- Use original analog signal
- Apply 3 bit coding
- Better representation of input information with additional bits



# **ADC Process-Accuracy**

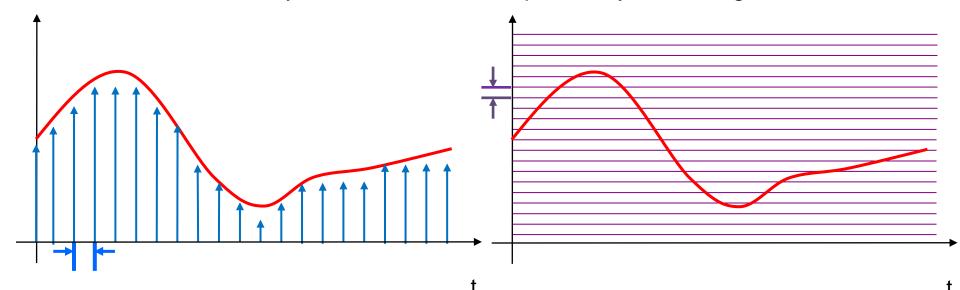
The accuracy of an ADC can be improved by increasing:



- Based on number of steps required in the conversion process
- Increases the maximum frequency that can be measured

# **ADC Process-Accuracy**

The accuracy of an ADC can be improved by increasing:



#### Sampling Rate, Ts

- Based on number of steps required in the conversion process
- Increases the maximum frequency that can be measured

#### Resolution (bit depth), Q

 Improves accuracy in measuring amplitude of analog signal

### **ADC-Error Possibilities**

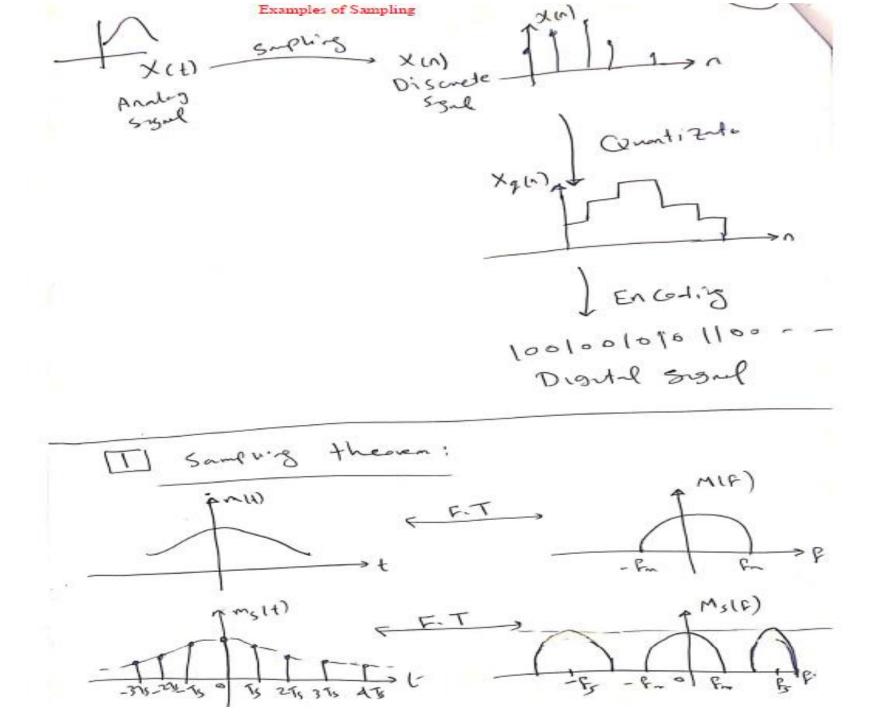
- Aliasing (sampling)
  - Occurs when the input signal is changing much faster than the sample rate
  - Should follow the Nyquist Rule when sampling
    - Answers question of what sample rate is required
    - Use a sampling frequency at least twice as high as the maximum frequency in the signal to avoid aliasing

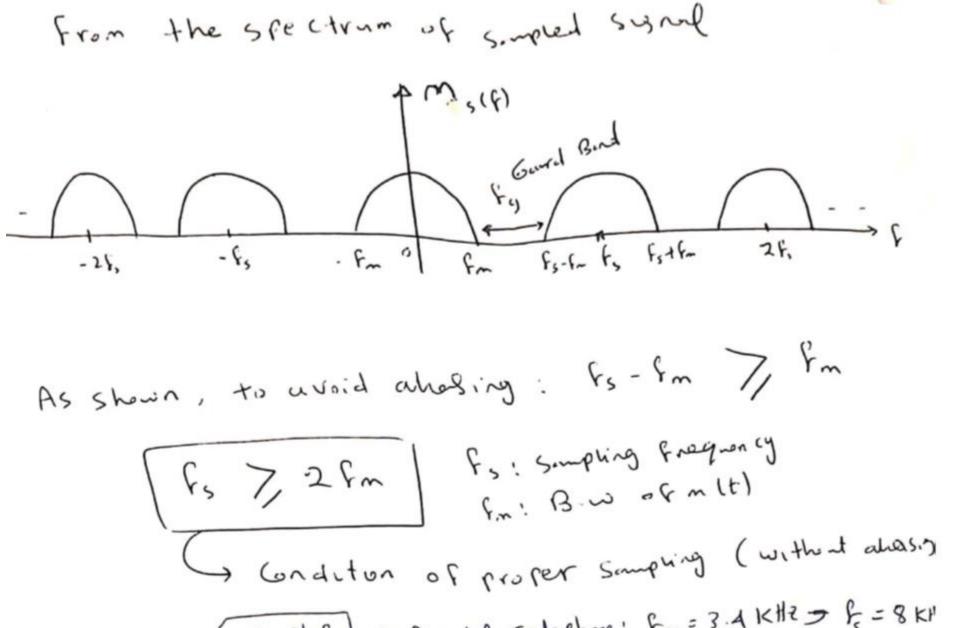
Where f<sub>signal</sub> is the BW of analog signal

Quantization Error (resolution)

# **ADC Applications**

- ADC are used virtually everywhere where an analog signal has to be processed, stored, or transported in digital form
- Analog data such as voice and video are converted to digital data for transmission over a digital link.
- We can transmit digital data
  - Faster
  - Cheaper
  - With fewer errors





(examples) 1- Digital Telephone: Fm = 3.4 KHZ > f= 8 KH 2- High Quility Aruby music: Fm= 20 KHZ fs= 44.1 KAZ

2) The condition of proper simpling without abosing

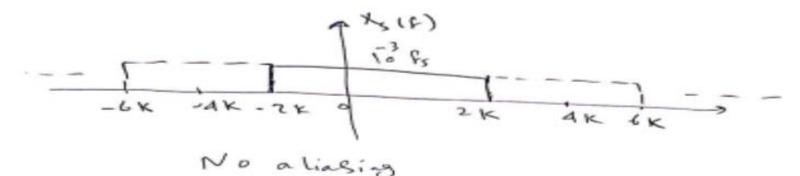
3) Fs: Sompling Frequency = Sompling rate 8, = # of somplex/sec

Reconstruction of original signed ) w eces 4 M(4) Re GASW-Won get original signel in Gregues cy Domm Condition OF Reconstruction Fs 7 2 Fm Some as proper sorpling Consider Notes - If \$s > 2 fm > over sompling if fs = 2 fm > critical (Nyques) surpring ( alrested - if to < 2 Fm > under simpling (alrasing)

Given: X(t) = A sinc ( A o o t)

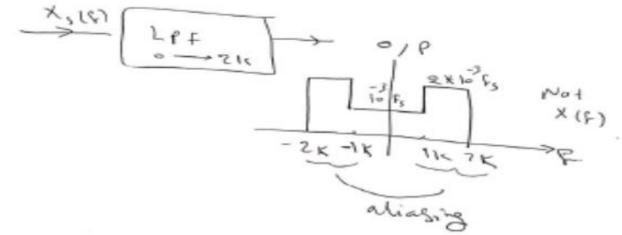
Apply sampling theorem and then reconstruct the original signal in the following cases:

i) 
$$P_s = 6K$$
 ii)  $F_s = 4K$  iii)  $F_s = 3K$ 



We can reconstruct original signal by LPF (0 -> fn)

Recall: Spectron of X, (f) => vegent spectron of x H) at 0 ) + Fc=+3 K , + 2 Fs =+6 K - -a multiply by fs. -5 k -3 K -- 2 K 0 1 k 2 1 5 3 k alinging, we can not reconstruct original signs (Note) If we apply LPF o - F== ZK

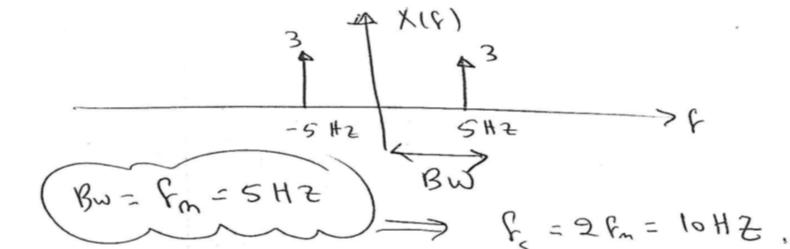


example:

XIt) = 6 GS (211(5)t), apply I deal sampling for the following Surpling frequencies then veconstruct the original signal (1)  $F_s = 14 \text{ Hz}$  (2)  $F_s = 7 \text{ Hz}$ Given:  $A GS (217Ft) \leftarrow F \cdot T \rightarrow \frac{A}{2} \left[S(F-F) + S(F+F)\right]$ 



XH) = 6 68 (211 (5)t)



(Ps = 14 HZ) => Fs > 2 8m as 2 8m = 10 HZ'. 85=14 728m No aliasing we can re Gastruct original signal. by

No aliasing.

F5=7 HZ; not 7, 2 6 = 10 # 2 = G < 2 cm : alrasing occurs as shown in the spectrum con not reconstruct the original signal If we apply LPF (0 - 5 HZ), we will not get the original signel but get 2 times (2HZ 85HZ) X