

ASSEMBLY LANGUAGE (CS401)

MID TERM MEGA FILE

SOLVED BY

VU HELPER

❖ Suppose AL contains 5 decimal then after two left shifts produces the value as

- ▶ 5
- ▶ **10**
- ▶ 15
- ▶ 20

❖ In STOS instruction, the implied source will always be in

- ▶ **AL or AX registers**
- ▶ DL or DX registers
- ▶ BL or BX registers
- ▶ CL or CX registers

❖ After the execution of STOSW the CX will be

- ▶ **Decrement by 1**
- ▶ Decrement by 2
- ▶ Increment by 1
- ▶ Increment by 2

❖ The basic function of SCAS instruction is to

- ▶ Compare
- ▶ **Scan (Pending)**
- ▶ Sort
- ▶ Move data

❖ Which is the unidirectional bus ?

- (I) Control Bus
- (II) Data Bus
- (III) Address Bus

- | | | | | | | | | |
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| <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | ▶ <input type="checkbox"/> I only |
| <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | ▶ <input type="checkbox"/> II only |
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| <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | ▶ <input type="checkbox"/> I and II only |

❖ The operation of CMP is to

- | | | | | | | | | |
|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|---|
| <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | ▶ Subtract Source from Destination |
| <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | ▶ Subtract Destination to from Source |
| <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | ▶ Add 1 to the Destination |

☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ Add Source and Destination

Question No: 7 (Marks: 1) - Please choose one

The registers IP, SP, BP, SI, DI, and BX all can contain a _____offset.

☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ 8-bit

☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☒ 16-bit

☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ 32-bit

☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ 64-bit

Question No: 8 (Marks: 1) - Please choose one

In assembly the CX register is used normally as a _____register.

▶ source

▶ **counter**

▶ index

▶ pointer

Question No: 9 (Marks: 1) - Please choose one

All the addressing mechanisms in iAPX88 return a number called _____address.

▶ **effective**

▶ faulty

▶ indirect

▶ direct

Question No: 10 (Marks: 1) - Please choose one

Which bit of the attributes byte represents the blue component of foreground color

▶ 3

▶ 2

▶ 1

▶ **0**

Question No: 11 (Marks: 1) - Please choose one

When a 32 bit number is divided by a 16 bit number, the quotient will be stored in

▶ **AX**

▶ BX

▶ CX

▶ DX

Question No: 12 (Marks: 1) - Please choose one

“mov byte [num1], 5” is _____ instruction.

▶ **legal**

▶ illegal

▶ stack based

▶ memory indirect

Question No: 13 (Marks: 1) - Please choose one

Which of the following options contain the set of instructions to open a window to the video memory?

▶ mov AX, 0xb008

mov ES, AX

▶ **mov AX, 0xb800**

mov ES, AX

▶ mov AX, 0x8b00

mov ES, AX
▶ mov AX, 0x800b
mov ES, AX

Question No: 14 (Marks: 1) - Please choose one

The execution of the instruction “mov word [ES : 0], 0x0741” will print character “A” on screen, color of the character will be

- ▶ Black
- ▶ **White**
- ▶ Red
- ▶ Blue

Question No: 15 (Marks: 1) - Please choose one

Which of the following flags will be affected by MOVSW?

- ▶ DF
- ▶ PF
- ▶ ZF
- ▶ **No effect on flags**

Question No: 16 (Marks: 1) - Please choose one

Which bit of the attributes byte represents the blue component of background color ?

- ▶ 3
- ▶ **4**
- ▶ 5
- ▶ 6

Question No: 1 (Marks: 1) - Please choose one

After the execution of SAR instruction

- ▶ **The msb is replaced by a 0**
- ▶ The msb is replaced by 1
- ▶ The msb retains its original value
- ▶ The msb is replaced by the value of CF

Question No: 2 (Marks: 1) - Please choose one

RETF will pop the offset in the

- ▶ BP
- ▶ **IP**
- ▶ SP
- ▶ SI

Question No: 3 (Marks: 1) - Please choose one

The routine that executes in response to an INT instruction is called

- ▶ **ISR**
- ▶ IRS
- ▶ ISP
- ▶ IRT

Question No: 4 (Marks: 1) - Please choose one

The first instruction of “COM” file must be at offset:

- ▶ 0x0010
- ▶ **0x0100**
- ▶ 0x1000
- ▶ 0x0000

Question No: 5 (Marks: 1) - Please choose one

"Far" jump is not position relative but is _____

- ▶ memory dependent
- ▶ **Absolute**
- ▶ temporary
- ▶ indirect

Question No: 6 (Marks: 1) - Please choose one

Only _____ instructions allow moving data from memory to memory.

- ▶ **string**
- ▶ word
- ▶ indirect
- ▶ stack

Question No: 7 (Marks: 1) - Please choose one

After the execution of instruction "RET 2"

- ▶ **SP is incremented by 2**
- ▶ SP is decremented by 2
- ▶ SP is incremented by 4
- ▶ SP is decremented by 4

Question No: 8 (Marks: 1) - Please choose one

DIV instruction has

- ▶ **Two forms**
- ▶ Three forms
- ▶ Four forms
- ▶ Five forms

Question No: 9 (Marks: 1) - Please choose one

When the operand of DIV instruction is of 16 bits then implied dividend will be of

- ▶ 8 bits
- ▶ 16 bits
- ▶ **32 bits**
- ▶ 64 bits

Question No: 10 (Marks: 1) - Please choose one

After the execution of MOVS instruction which of the following registers are updated

- ▶ SI only
- ▶ DI only
- ▶ **SI and DI only**
- ▶ SI, DI and BP only

Question No: 11 (Marks: 1) - Please choose one

In 8088 architecture, whenever an element is pushed on the stack

- ▶ SP is decremented by 1
- ▶ **SP is decremented by 2**
- ▶ SP is decremented by 3
- ▶ SP is decremented by 4

Question No: 12 (Marks: 1) - Please choose one

When a very large number is divided by very small number so that the quotient is larger than the space provided, this is called

- ▶ Divide logical error

▶ **Divide overflow error**

- ▶ Divide syntax error
- ▶ An illegal instruction

Question No: 13 (Marks: 1) - Please choose one

In the word designated for one screen location, the higher address contains

- ▶ **The character code**
- ▶ The attribute byte
- ▶ The parameters
- ▶ The dimensions

Question No: 14 (Marks: 1) - Please choose one

Which of the following options contain the set of instructions to open a window to the video memory?

- ▶ `mov AX, 0xb008`
`mov ES, AX`
- ▶ **`mov AX, 0xb800`**
`mov ES, AX`
- ▶ `mov AX, 0x8b00`
`mov ES, AX`
- ▶ `mov AX, 0x800b`
`mov ES, AX`

Question No: 15 (Marks: 1) - Please choose one

In a video memory, each screen location corresponds to

- ▶ One byte
- ▶ **Two bytes**
- ▶ Four bytes
- ▶ Eight bytes

Question No: 16 (Marks: 1) - Please choose one

The execution of the instruction “`mov word [ES : 0], 0x0741`” will print character “A” on screen , background color of the screen will be

- ▶ **Black**
- ▶ White
- ▶ Red
- ▶ Blue

Question No: 1 (Marks: 1) - Please choose one

The physical address of the stack is obtained by

- ▶ SS:SI combination
- ▶ **SS:SP combination**
- ▶ ES:BP combination
- ▶ ES:SP combination

Question No: 2 (Marks: 1) - Please choose one

After the execution of instruction “`RET` ”

- ▶ **SP is incremented by 2**
- ▶ SP is decremented by 2
- ▶ SP is incremented by 1
- ▶ SP is decremented by 1

Question No: 3 (Marks: 1) - Please choose one

The second byte in the word designated for one screen location holds

- ▶ Increment CX by 1
- ▶ Increment CX by 2
- ▶ **Decrement CX by 1**
- ▶ Decrement CX by 2

- ▶ **Compare**
 - ▶ Scan
 - ▶ Sort
 - ▶ Move data

Data
 Intermediate result
 Address
 Both data and addresses

► □ index register
□ □ □ □ □ base register
Flags register
□ □ □ □ □ accumulator

- ▶ **Add 0x30 in the digit**
 - ▶ Subtract 0x30 from the digit
 - ▶ Add 0x61 in the digit
 - ▶ Subtract 0x61 from the digit

- ▶ 32 bits
 - ▶ 16 bits
 - ▶ 8 bits
 - ▶ **4 bits**

5

- ▶ AX
- ▶ **AL**
- ▶ AH
- ▶ DX

Question No: 11 (Marks: 1) - Please choose one

Which mathematical operation is dominant during the execution of SCAS instruction

- ▶ **Division**
- ▶ Multiplication
- ▶ Addition
- ▶ Subtraction

Question No: 12 (Marks: 1) - Please choose one

If AX contains decimal -2 and BX contains decimal 2 then after the execution of instructions:
CMP AX, BX
JA label

- ▶ Jump will be taken
- ▶ **Zero flag will set**
- ▶ ZF will contain value -4
- ▶ Jump will not be taken

Question No: 13 (Marks: 1) - Please choose one

The execution of the instruction "mov word [ES : 160], 0x1230" will print a character "0" on the screen at

- ▶ Second column of first row
- ▶ **First column of second row**
- ▶ Second column of second row
- ▶ First column of third row

Question No: 14 (Marks: 1) - Please choose one

If the direction of the processing of a string is from higher addresses towards lower addresses then

- ▶ ZF is cleared
- ▶ **DF is cleared**
- ▶ ZF is set
- ▶ DF is set

Question No: 15 (Marks: 1) - Please choose one

The instruction ADC has _____ Operand(s)

- ▶ 0
- ▶ 1
- ▶ 2
- ▶ **3**

Question No: 16 (Marks: 1) - Please choose one

Which bit of the attributes byte represents the red component of background color ?

- ▶ 3
- ▶ 4
- ▶ 5
- ▶ 6

Stack is a _____ that behaves in a first in last out manner.

- Program
- **data structure**
- Heap
- None of the Given

The physical address of the stack is obtained by

- SS:SI combination
- **SS:SP combination**
- ES:BP combination
- ES:SP combination

Foreground and background parameter will be

- 32bits
- 16bits
- **8bits**
- 4bits

The clear screen operation initialize whole block of memory

- **0741**
- 0417
- 0714
- 0174

In STOSB instruction, when DF is Set, SI is

- Incremented by 1
- Incremented by 2
- **Decrement by 1 (Not confirmed)**
- Decrement by 2

Assembly language is:

- **Low-level programming language**
- High-level programming language
- Also known as machine language
- Not considered closer to the computer

A 32 Bit processor has accumulator of -----

- 8 bit
- 16 bit
- **32 bit**
- 64 bit

To transfer control back the RET instruction take

- 1 argument
- 1 argument

- 3 arguments
- **No arguments**

RET is executed, it recovers the values from

- Register
- **Stack**
- Data segment
- Code segment

To convert any digit to its ASCII representation

- **Add 0x30 in the digit**
- Subtract 0x30 from the digit
- Add 0x61 in the digit
- Subtract 0x61 from the digit

The prevalent convention in most high level languages is stack clearing by the

- **Caller**
- **Callee**
- **RET**
- **Stack**

After execution of JCXZ instruction CX will changed with flag affect.

- CF
- OF
- DF
- **None of Above**

Execution of the instruction "mov word [ES : 0], 0x0741" will print

- **"A" appear on the top left of screen**
- "A" appear on the top right of screen
- "A" appear on the center of screen
- "A" appear on the bottom left of screen

if contains decimal -2 and BX contains decimal 2 then after the execution of instructions:

CMP AX, BX

JA label

- Jump will be taken
- **Zero flag will set**
- ZF will contain value -4
- Jump will not be taken

Which of the following options contain the set of instructions to open a window to the video memory?

- mov AX, 0xb008
mov ES, AX
- **mov AX, 0xb800**
mov ES, AX
- mov AX, 0x8b00
mov ES, AX
- mov AX, 0x800b

mov ES, AX

If D is "35" is shift to left 2 bits the new value

- 35
- 70
- **140**
- 17

Execution of the instruction "mov word [ES : 0], 0x1230" will print the character color will

- **Green**
- White
- Red
- Black

1. Assembly language is not a low level language.

a. True

b. False

2. In case of COM File first command parameter is stored at _____ offset of program segment prefix.

a. 0x80 (Not Confirm)

b. 0x82

c. 0x84

d. 0x86

3. Address always goes from

a. Processor to meory

b. Memory to processor

c. Memory to memory

d. None of the above

4. The source register in OUT is

a. AL or AX

b. BL or BX

c. CL or CX

d. DL or DX

5. By default CS is associated with

a. SS

b. BP

c. CX

d. IP

6. Which of the following pins of parallel port are grounded

a. 10-18

b. 18-25

c. 25-32

d. 32-39

7. In the instruction mov word [es:160], 0x1230, 30 represents the character

a. A

b. B

c. 0

d. 1

8. On executing 0x21 0x3D, if file cant be opened then

a. CF will contain 1

b. CF will contain 0

c. ZF will contain 1

d. ZF will contain 0

9. Which of the following IRQ is cascading interrupt

a. IRQ 0

b. IRQ 1

c. IRQ 2

d. IRQ 3

10. The execution of instruction `mov word [es:160], 0x1230`, will print a character on the screen at
- First column of second row
 - Second column of first row
 - Second column of second row
 - First column of third row**

❖ One screen location corresponds to a

Byte
Word
Double byte
Double word

After the execution of “**PUSH AX**” statement

AX register will reside on the stack
A copy of AX will go on the stack
The value of AX disappear after moving on stack
Stack will send an acceptance message

❖ Physical address of the stack is obtained by

SS:SI combination
SS:SP combination
ES:BP combination
ES:SP combination

❖ If the address of memory location Num1 is 0117 and its content is 0005 then after execution of the instruction “`mov bx, Num1`” bx will contain

0005 (Pending)

0117
Num1
1701

In STOS instruction, the implied source will always be in

AL or AX registers
DL or DX registers
BL or BX registers
CL or CX registers

The shift logical right operation inserts

A zero at right
A zero at left
A one at right
A one at right

REP will always

- Increment CX by 1
- Increment CX by 2
- Decrement CX by 1
- Decrement CX by 2

When an item is pushed on the decrementing stack, the top of the stack is

- First decremented and then element copied on to the stack
- First incremented and then element copied on to the stack
- Decrement after the element copied on to the stack
- Incremented after the element copied on to the stack

assembly the CX register is used normally as a _____ register.

- source
- counter
- index
- pointer

Which is the unidirectional bus ?

- I) Control Bus
- II) Data Bus
- III) Address Bus
- I only
- II only
- III only
- I and II only

The basic function of SCAS instruction is to

- Compare
- Scan
- Sort
- Move data

_____ register holds the address of next instruction is to be executed

- Base pointer
- Code segment
- Source index
- Program counter

JC and JNC test the _____ flag.

- carry
- parity
- zero
- sign

After the execution of REP instruction CX will be decremented then which of the following flags will be affected?

- CF
- OF
- DF

No flags will be affected

In string manipulation whenever an instruction needs a memory source, which of the following will hold the pointer to it?

ES: DI

ES: BP

DS:BP

DS:SI

which bit sets the character "blinking" on the screen?

- 5
- 6
- 7
- 8

If we want to divide a signed number by 2, this operation can better be accomplished by

- SHR
- SAR
- SRL
- SAL

After the execution of STOSWB, the CX will be.....

- Incremented by 1
- Incremented by 2
- Decrement by 1
- Decrement by 2

Each screen location corresponds to a word, the lower byte of this word contains ____

The character code

- The attribute byte
- The parameters
- The dimensions

❖ In a video memory, each screen location corresponds to

One byte

- Two bytes
- Four bytes
- Eight bytes

❖ Immediate operands of Push are called:-

- ▶ **Source Operands**
- ▶ Destination Operands
- ▶ Data Operands
- ▶ Stack Operands

❖ Extended ASCII has how many characters:-

- ▶ 64

- ▶ 128
- ▶ **256**
- ▶ 512

❖ The video attribute byte of Red component of foreground color is:-

- ▶ **2**
- ▶ 3
- ▶ 4
- ▶ 5

❖ Process of Signals bak & forth is called:-

- ▶ Programming
- ▶ **Activity**
- ▶ Interruption
- ▶ Communication

❖ 32 Bit Processor has an accumulator of:-

- ▶ 8 Bits
- ▶ 16 Bits
- ▶ **32 Bits**
- ▶ 64 Bits

❖ When a relative address is stored in 16 Bits then the jump is known as:-

- ▶ Near
- ▶ **Short**
- ▶ Far
- ▶ Close

❖ In order to convert digit into ASCII value following is added:-

- ▶ 0x20
- ▶ **0x30**
- ▶ 0x40
- ▶ 0x100

Question No: 1 (Marks: 1) - Please choose one

The physical address of the stack is obtained by

- ▶ SS:SI combination
- ▶ **SS:SP combination**
- ▶ ES:BP combination
- ▶ ES:SP combination

Question No: 2 (Marks: 1) - Please choose one

Value of AH in the write Graphics pixel service is

- ▶ 0Ch
- ▶ 0Bh
- ▶ 1Ch
- ▶ 2Ch

Question No: 3 (Marks: 1) - Please choose one

Threads can have function calls, parameters and _____ variables.

- ▶ **global**
- ▶ local
- ▶ legal
- ▶ illegal

Question No: 4 (Marks: 1) - Please choose one
Creation of threads can be

- ▶ static
- ▶ **dynamic**
- ▶ easy
- ▶ difficult

Question No: 5 (Marks: 1) - Please choose one
How many prevalent calling conventions do exist

- ▶ 1
- ▶ **2**
- ▶ 3
- ▶ 4

Question No: 6 (Marks: 1) - Please choose one
VESA VBE 2.0 is a standard for

- ▶ **High resolution Mode**
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

Question No: 7 (Marks: 1) - Please choose one
The serial port connection is a ----- connector

- ▶ **9pin DB 9**
- ▶ 8pin DB 9
- ▶ 3pin DB 9
- ▶ 9pin DB 5

Question No: 8 (Marks: 1) - Please choose one
Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ **DOS**
- ▶ Both
- ▶ None

Question No: 9 (Marks: 1) - Please choose one
In STOSB instruction, when DF is clear, SI is

- ▶ **Incremented by 1**
- ▶ Incremented by 2
- ▶ Decrement by 1
- ▶ Decrement by 2

Question No: 10 (Marks: 1) - Please choose one
After the execution of STOSW the CX will be

- ▶ Decrement by 1
- ▶ Decrement by 2
- ▶ **Incremented by 1**
- ▶ Incremented by 2

Question No: 11 (Marks: 1) - Please choose one

IRQ is referred to

- ▶ **Eight input signals**
- ▶ One output signal
- ▶ One input signals
- ▶ Eight output signals

Question No: 12 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ **IRQ 1**
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 13 (Marks: 1) - Please choose one

Which of the following IRQs is connected to serial port COM 1?

- ▶ **IRQ 4**
- ▶ IRQ 5
- ▶ IRQ 6
- ▶ IRQ 7

Question No: 14 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ **IDTR**
- ▶ IVT
- ▶ IDTT

Question No: 15 (Marks: 1) - Please choose one

Assembly language is:

- ▶ **Low-level programming language**
- ▶ High-level programming language
- ▶ Also known as machine language
- ▶ Not considered closer to the computer

Question No: 16 (Marks: 1) - Please choose one

The number of bits required to access 1MB of memory are

- ▶ 16 bits
- ▶ 32 bits
- ▶ **Depends on the processor architecture**
- ▶ 20 bits

Question No: 17 (Marks: 1) - Please choose one

In STOSB instruction, SI is decremented or incremented by

- ▶ 3
- ▶ 2
- ▶ **1**
- ▶ 4

Question No: 18 (Marks: 1) - Please choose one

In programmable interrupt controller, which of the following ports is referred as a *control port*.

- ▶ 19
- ▶ **20**

- ▶ 21
- ▶ 22

Question No: 19 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ **AL**
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 20 (Marks: 1) - Please choose one

In device attribute word, which of the following bit decides whether it is a *character device* or a *block device*

- ▶ Bit 12
- ▶ Bit 13
- ▶ Bit 14
- ▶ Bit 15

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to CTS (Clear To Send) ?

- ▶ 6
- ▶ 7
- ▶ **8**
- ▶ 9

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to RD (Received Data) ?

- ▶ 1
- ▶ **2**
- ▶ 3
- ▶ 4

Question No: 23 (Marks: 1) - Please choose one

VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 format
- ▶ **5:6:5 format**
- ▶ 6:5:6 format
- ▶ 5:6:7 format

Question No: 24 (Marks: 1) - Please choose one

Motorola 68K processors have 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ **16**
- ▶ 32

Question No: 25 (Marks: 1) - Please choose one

Programmable Interrupt Controller (PIC) has

- ▶ One input signals and eight output signals
- ▶ One input signal and one output signal
- ▶ **Eight input signals and one output signals**
- ▶ Eight input signals and eight output signals

Question No: 26 (Marks: 1) - Please choose one

Video services are classified into..... broad categories.

- ▶ 5
- ▶ 4
- ▶ 3
- ▶ 2

In primitive form, the block processing instructions operate on _____ of memory at one time

1 whole block

2 A single cell

3 A single bit

4 Whole segment

After the execution of STOSWB, the cx will be

1 Decmented by 1

2 Decmented by 2

3 Incremented by 1

4 Incremented by 2

In STOSW instructions when DF is set SI is

1 / 3

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1 Decmented by 1

2 Decmented by 2

3 Incremented by 1

4 Incremented by 2

Which bit of the attribute byte represents the intensity component of foreground color?

1 4th

2 5th

3 6th

4 7rd

Which of the following flags will be affected by Movsw:

1 DF

2 / 3

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2 PF

3 ZF

4 Not effect on flag

- ❖ The Transfer control back the RET instruction take:
 - ▶ 1 argument
 - ▶ 2 argument
 - ▶ 3 argument
 - ▶ No argument

- ❖ The STOSB instruction SI is decremented or incremented by:
 - ▶ 4
 - ▶ 1
 - ▶ 2
 - ▶ 3

- ❖ CMPS instruction subtracts the source location to the destination location. Destination location always lies in
 - ▶ DS:SI
 - ▶ DS:DI
 - ▶ ES:SI
 - ▶ ES:DI

- ❖ Regarding assembler, which statement is true:
 - ▶ Assembler converts mnemonics to the corresponding OP CODE
 - ▶ Assembler converts OP CODE to the corresponding mnemonics.
 - ▶ Assembler executes the Assembly code all at once.
 - ▶ Assembler executes the Assembly code step by step.

- ❖ If “BB” is the OP CODE of the instruction which states to “move a constant value to AX Register”, the hexadecimal Representation (Using little Endian Notation) of the instruction “Mov AX,36” (“150” in hexadecimal number system) will be:
 - ▶ 0xBB0150
 - ▶ 0x5001BB
 - ▶ 0x01BB50
 - ▶ 0xBB5001

- ❖ In the instruction Mov AX,5 the number of operands are:
 - ▶ 1
 - ▶ 2
 - ▶ 3
 - ▶ 4

- ❖ A maximum parameters a subroutine can receive (with the help of Registers) are:
 - ▶ 6
 - ▶ 7
 - ▶ 8
 - ▶ 9

- ❖ In Assembly the CX Register is used normally as a _____ Register.
 - ▶ Source
 - ▶ Counter
 - ▶ Index
 - ▶ Pointer

- ❖ All the addressing Mechanisams in IAPX88 return a number called _____ address.
 - ▶ effective

- ▶ faulty
- ▶ indirect
- ▶ direct
- ❖ When a 16 bit number is divided by an 8 bit number, the dividend will be in:
 - ▶ AX
 - ▶ BX
 - ▶ CX
 - ▶ DX
- ❖ The Left-Shift-Operation the left most bit _____
 - ▶ will drop
 - ▶ will go into CF
 - ▶ will come to the right most
 - ▶ will be always 1
- ❖ Suppose the decimal number “35” after shifting its binary two bits to left, the new value becomes _____
 - ▶ 35
 - ▶ 70
 - ▶ 140
 - ▶ 17
- ❖ When divide overflow occurs processor will be interrupted this type of interrupt is called:
 - ▶ Hardware Interrupt.
 - ▶ Software Interrupt.
 - ▶ Processor execution
 - ▶ Logical Interrupts
- ❖ Which mathematical operation is dominant during the execution of SCAS instruction:
 - ▶ Division
 - ▶ Multiplication
 - ▶ Addition
 - ▶ Subtraction
- ❖ After the execution of REP instruction CX will be decremented then which of the following flags will be affected?
 - ▶ CF
 - ▶ OF
 - ▶ DF
 - ▶ No flags will be affected
- ❖ _____ is the reasons due to which strings instructions are used in 8088
 - ▶ Efficiency and Accuracy
 - ▶ Reduction in code size and accuracy
 - ▶ Reduction in code size and speed
 - ▶ Reduction in code size and efficiency

