CS401- Computer Architecture and Assembly Language
Quiz No.2 JAN 10, 2013 (QUIZ lectures 24-28)

Question # 1 of 10 (Start time: 02:24:57 PM) Total Marks: 1
Memory address space is selected when which of the following instructions is given to the processor?s
Select correct option:
MOV
DEC
IN
ADD

PCB stands for?
Select correct option:
Process Control Block
Process Clearing Block
Programable Counter Block
Programs Control Block

Question # 3 of 10 (Start time: 02:26:41 PM) Total Marks: 1
Creation of threads can be
Select correct option:
Static
Dynamic
Both
None

Threads can have function calls, parameters and _____ variables.
Select correct option:
global
local
legal
illegal

Which of the following is the highest priority interrupt
Int3
Int2
Int1
Int0

The input frequency of the programmable interval timer (PIT) is
Fixed
Depends on processor clock
Variable

Depends on hardware attached

Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is...........
The Interrupt mask register
Interrupt port
Output port
Input port

The source registers in OUT is ?
AL or AX
BL or BX
CL or CX
DL or DX

The time interval between two timer ticks is ?
40ms
45ms
50ms
55ms

Which of the following interrupts is used for saving and restoring the registers
INT6
Int7
Int8
Int0

In programmable interrupt controller which of the following ports is referred as a interrupt mask register?
19
20
21
22
Refernce:- page no 115
Port 21 is the interrupt mask register which can be used
for selectively enabling or disabling interrupts

The thread registration code initializes the PCB and adds it to the linked list so that the
_____ will give it a turn.
Assembler
Scheduler
Linker
Debugger

The programmable interval timer (PIT) has input frequency of
1.193MHZ
2.192MHZ
3.196MHZ
4.198MHZ

Peripheral address space is selected when which of the following instructions is given to the processor?
MOv
In
Add
Out

Direction flag, the interrupt flag, and the trap flag are
used for mathematical operations
not used for mathematical operations
status flags
not status flags

An End of Interrupt (EOI) signal is sent by
Handler
Processor
IRQ
PIC

The space where all the registers of a task are stored is called the
control block
process control block
stack
memory

The number of pins in a parallel port connector are?
Select correct option:
20
25
30
35

Which of the following interrupt is of highest priority interrupt?
Select correct
Key board interrupt
Timer interrupt
INT 2

INT 3

The instruction used to read a character from the keyboard port is
in al, 0x60
out al, 0x60
in al, 0x80
out al, 0x80

Each thread can have their own
execution area
stack
memory
array

All the registers & stack are saved in
Multitasking
multi-processing
function call
BIOS

Which of the following IRQs is used for sound card or network card?
IRQ 4
IRQ 5
IRQ 6
IRQ 7

In programmable interrupt controller which of the following ports is referred as a control port?
22
19
21
20

In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts ?
19
20
21
22

Which of the following is the highest priority interrupt
Int3
Int2
Int1
Int0

The input frequency of the programmable interval timer (PIT) is

Fixed
Depends on processor clock
Variable
Depends on hardware attached

Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is...........
The Interrupt mask register
Interrupt port
Output port
Inpuit port

The source registers in OUT is ?
AL or AX
BL or BX
CL or CX
DL or DX

Creation of threads can be
Static
Dynamic
Both
None

The time interval between two timer ticks is ?
40ms
45ms
50ms
55ms

Which of the following interrupts is used for saving and restoring the registers
INT6
Int7
Int8
Int0

PCB stands for?
Process Control Block
Process Clearing Block
Programable Counter Block
Programs Control Block

In programmable interrupt controller which of the following ports is referred as a interrupt mask register?
19
20
21
22

The thread registration code initializes the PCB and adds it to the linked list so that the
_____ will give it a turn.

Assembler
Scheduler
Linker
Debugger

The programmable interval timer (PIT) has input frequency of
1.193MHZ
2.193MHZ
3.193MHZ
4.193MHZ

Threads can have function calls, parameters and _____ variables.
Global
Local
Legal
Illegal

Peripheral address space is selected when which of the following instructions is given to the processor?
MOv
In
Add
Out

Direction flag, the interrupt flag, and the trap flag are
used for mathematical operations
not used for mathematical operations
status flags
not status flags

An End of Interrupt (EOI) signal is sent by
Handler
Processor
IRQ
PIC

The space where all the registers of a task are stored is called the
control block
process control block
stack
memory

The instruction used to read a character from the keyboard port is
in al, 0x60
out al, 0x60

in al, 0x80
out al, 0x80

Which of the following pins of a parallel port connector are grounded?
25-32
10-18
18-25
32-39

Each thread can have their own
execution area
stack
memory
array

All the registers & stack are saved in
Multitasking (page 139)
multi-processing
function call
BIOS

Question # 1 of 10 (Start time: 02:58:14 PM)   Total Marks: 1
The instruction used to read a character from the keyboard port is
Select correct option:
in al, 0x60
out al, 0x60
in al, 0x80
out al, 0x80

Question # 2 of 10 (Start time: 02:59:00 PM)   Total Marks: 1
Each thread can have their own
Select correct option:
execution area
stack
memory
array

Question # 3 of 10 (Start time: 03:00:28 PM)   Total Marks: 1
There is no instruction to clear the
Select correct option:
Trap flag
Interrupt flag
Direction flag
None of the above

Question # 4 of 10 (Start time: 03:01:36 PM)   Total Marks: 1
PCB stands for?

Select correct option:
Process Control Block
Process Clearing Block
Programable Counter Block
Programs Control Block

From the following which is related to read a char from key board?
Select correct option:
out al, 0x60
in al, 0x60
out dx, al
ou dx, 0x378

The input frequency of the programmable interval timer (PIT) is
Select correct option:
Fixed
Depends on processor clock
Variable
Depends on hardware attached

The thread registration code initializes the PCB and adds it to the linked list so that the
_____ will give it a turn.
Select correct option:
Assembler
Scheduler
Linker
Debugger

Question # 8 of 10 (Start time: 03:02:37 PM)   Total Marks: 1
The source registers in OUT is ?
Select correct option:
AL or AX
BL or BX
CL or CX
DL or DX

Question # 9 of 10 (Start time: 03:03:11 PM)   Total Marks: 1
The time interval between two timer ticks is ?
Select correct option:
 40ms
 45ms
 50ms
55ms

Creation of threads can be
Select correct option:

Static
Dynamic
Both
None

Memory address space is selected when which of the following instructions is given to the processor?s
Select correct option:
MOV
DEC
IN
ADD

Question # 2 of 10 (Start time: 02:25:56 PM) Total Marks: 1
PCB stands for?
Select correct option:
Process Control Block
Process Clearing Block
Programable Counter Block
Programs Control Block

Question # 3 of 10 (Start time: 02:26:41 PM) Total Marks: 1
Creation of threads can be
Select correct option:
Static
Dynamic
Both
None

Question # 4 of 10 (Start time: 02:27:46 PM) Total Marks: 1
Threads can have function calls, parameters and _____ variables.
Select correct option:
global
local
legal
illegal

Which of the following is the highest priority interrupt
Int3
Int2
Int1
Int0

The input frequency of the programmable interval timer (PIT) is
Fixed
Depends on processor clock
Variable
Depends on hardware attached

Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is...........
The Interrupt mask register
Interrupt port
Output port
Input port

The source registers in OUT is ?
AL or AX
BL or BX
CL or CX
DL or DX

The time interval between two timer ticks is ?
40ms
45ms
50ms
55ms

Which of the following interrupts is used for saving and restoring the registers
INT6
Int7
Int8
Int0

In programmable interrupt controller which of the following ports is referred as a interrupt mask register?
19
20
21
22

The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.
Assembler
Scheduler
Linker
Debugger

The programmable interval timer (PIT) has input frequency of
1.193MHZ
2.192MHZ
3.196MHZ
4.198MHZ

Peripheral address space is selected when which of the following instructions is given to the processor?
MOv
IN
Add
Out

Direction flag, the interrupt flag, and the trap flag are
used for mathematical operations
not used for mathematical operations
status flags
not status flags

An End of Interrupt (EOI) signal is sent by
Handler
Processor
IRQ
PIC

The space where all the registers of a task are stored is called the
control block
process control block
stack
memory

Question # 3 of 10 (Start time: 12:20:27 AM) Total Marks: 1
The number of pins in a parallel port connector are?
Select correct option:
 20
25
30
35

Which of the following interrupt is of highest priority interrupt?
Select correct
option:
Key board interrupt
Timer interrupt
INT 2
INT 3

The instruction used to read a character from the keyboard port is
in al, 0x60
out al, 0x60
in al, 0x80

out al, 0x80

Each thread can have their own
execution area
stack
memory
array
Refernce:-

All the registers & stack are saved in
Multitasking
multi-processing
function call
BIOS

Which of the following is the highest priority interrupt
Select correct option:
 INT3
 INT2
 INT1
 INT0

Which of the following interrupts is used for saving and restoring the registers
Select correct option:
 INT6
 INT7
 INT8
 INT0

There is no instruction to clear the
Select correct option:
 Trap flag
Interrupt flag
Direction flag
None of the above

For the external hardware to generate an interrupt there are how many pins out side the processor?
1
2
3
4

Which of the following IRQs is derived by a keyboard?
IRQ 0
IRQ 1
IRQ 2

IRQ 3

Which of the following IRQs is connected to serial port COM 2?
Select correct option:
IRQ 0
IRQ 1
IRQ2
IRQ3

Programmable interrupt controller (PIC) has
Select correct option:
One input signals and eight output signals
One input signal and one output signal not sure
Eight input signals and one output signals
Eight input signals and eight output signals

Which of the following IRQs is used by the parallel port?
IRQ 5
IRQ 4
IRQ 6
IRQ 7

Which of the following IRQs is a cascading interrupt?
IRQ 0
IRQ 1
IRQ 2
IRQ 3

Which of the following pins of a parallel port connector are grounded
10-15
18-25
25-32
32-39

Which of the following IRQs is used for Floppy disk derive?
IRQ 4
IRQ 5
IRQ 6
IRQ 7

When two devices in the system want to use the same IRQ line then what will happen?
A)   An IRQ Collision

B) **An IRQ Conflict**
C)   An IRQ Crash
D)   An IRQ Blockage

The parallel port connector is called ?
Select correct option:
DB-25
BD-25
DB-24
BD-24

IRQ is referred to
Select correct option:
Eight input signals
One output signal
Eight output signals
One input signals