Computer Architecture

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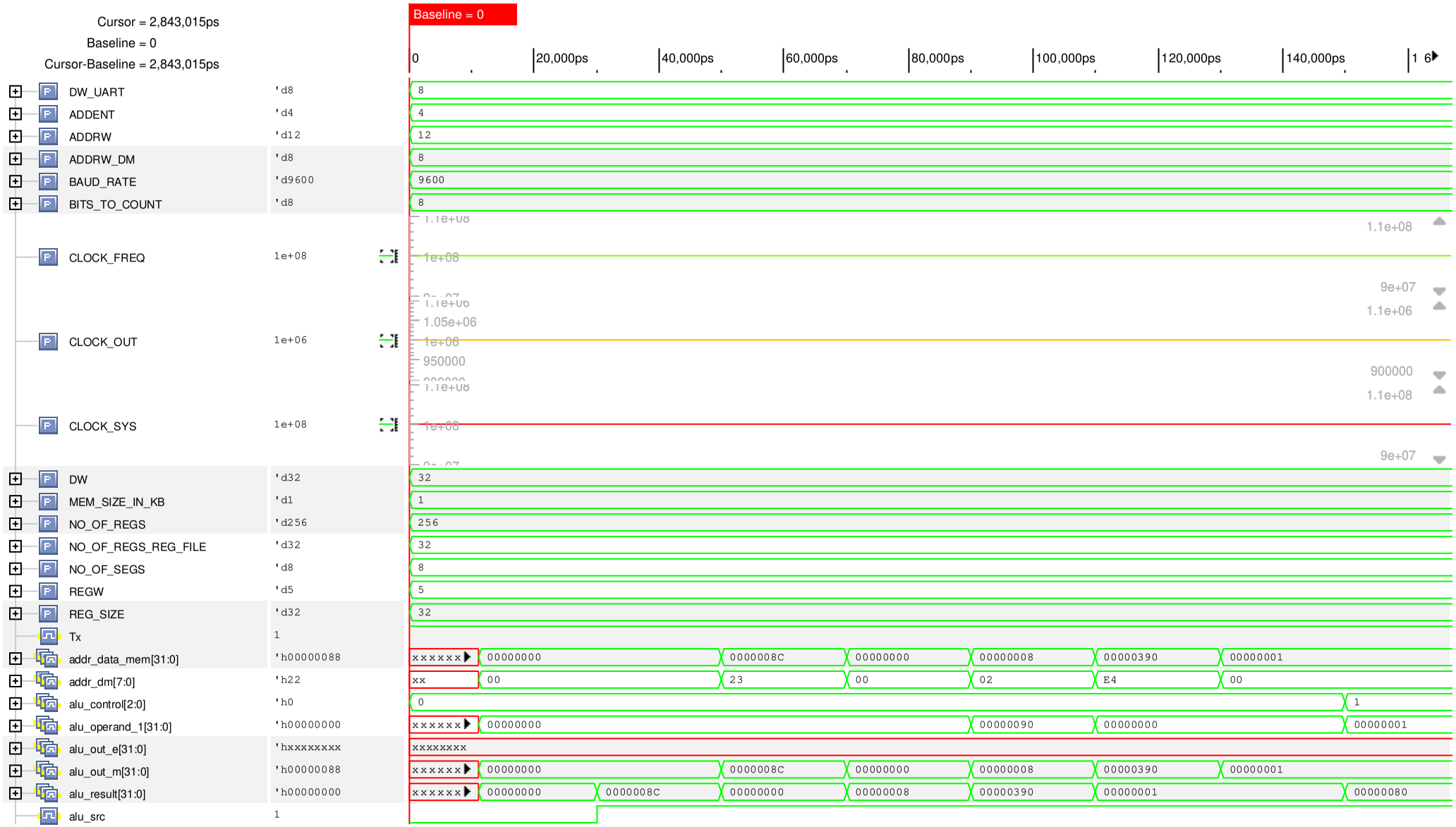
3 stage pipelined RISC-V Core



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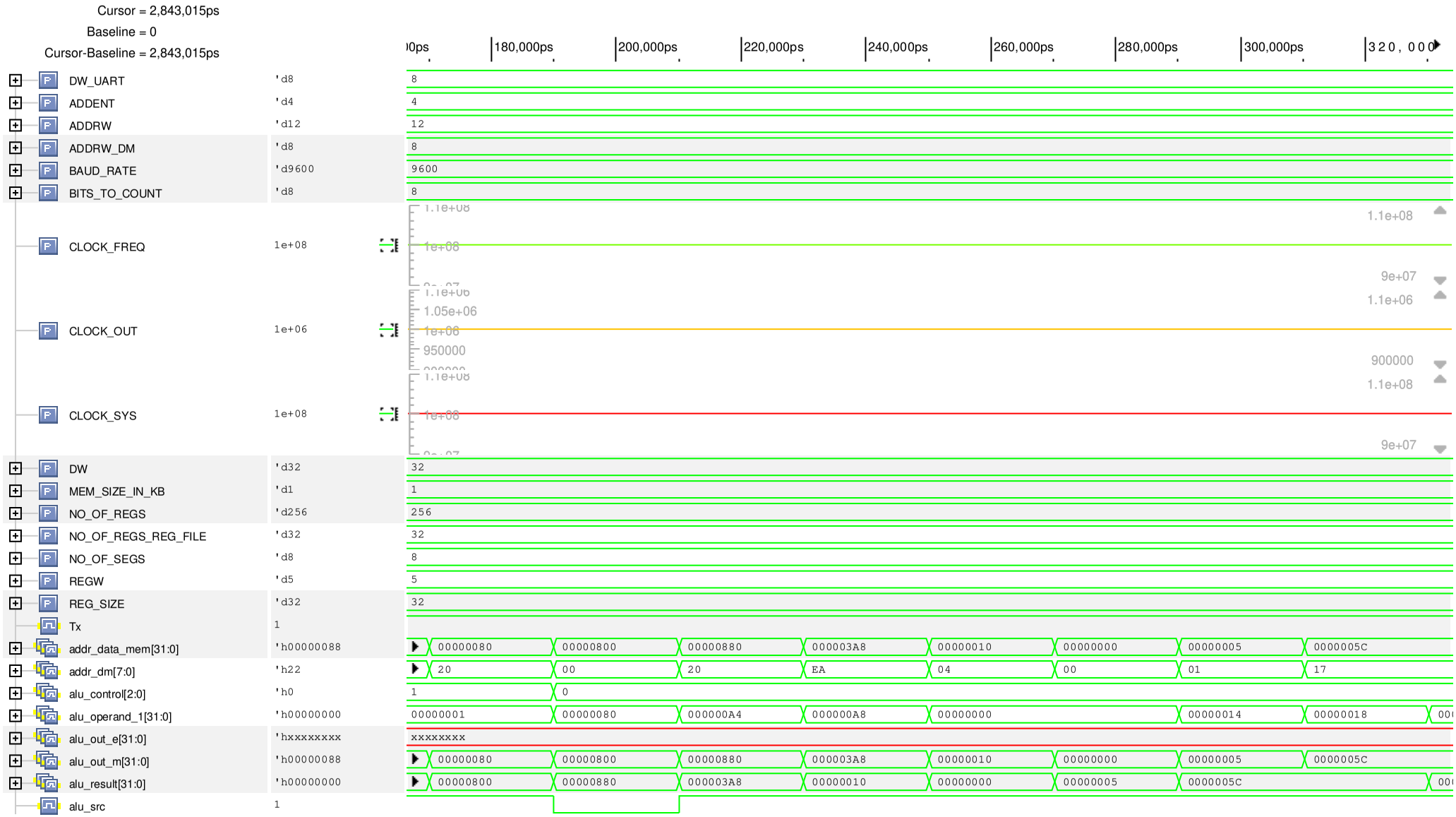

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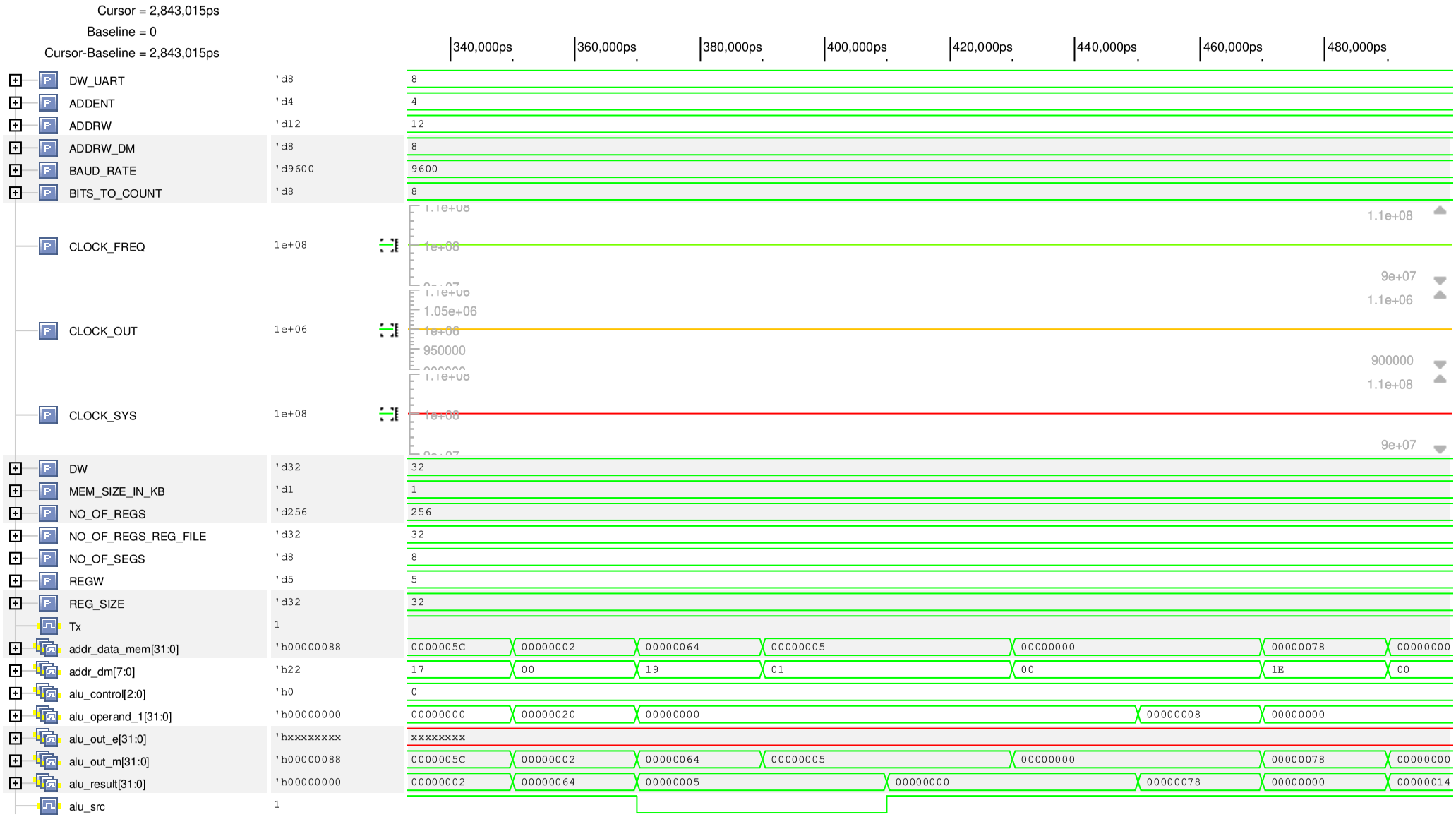

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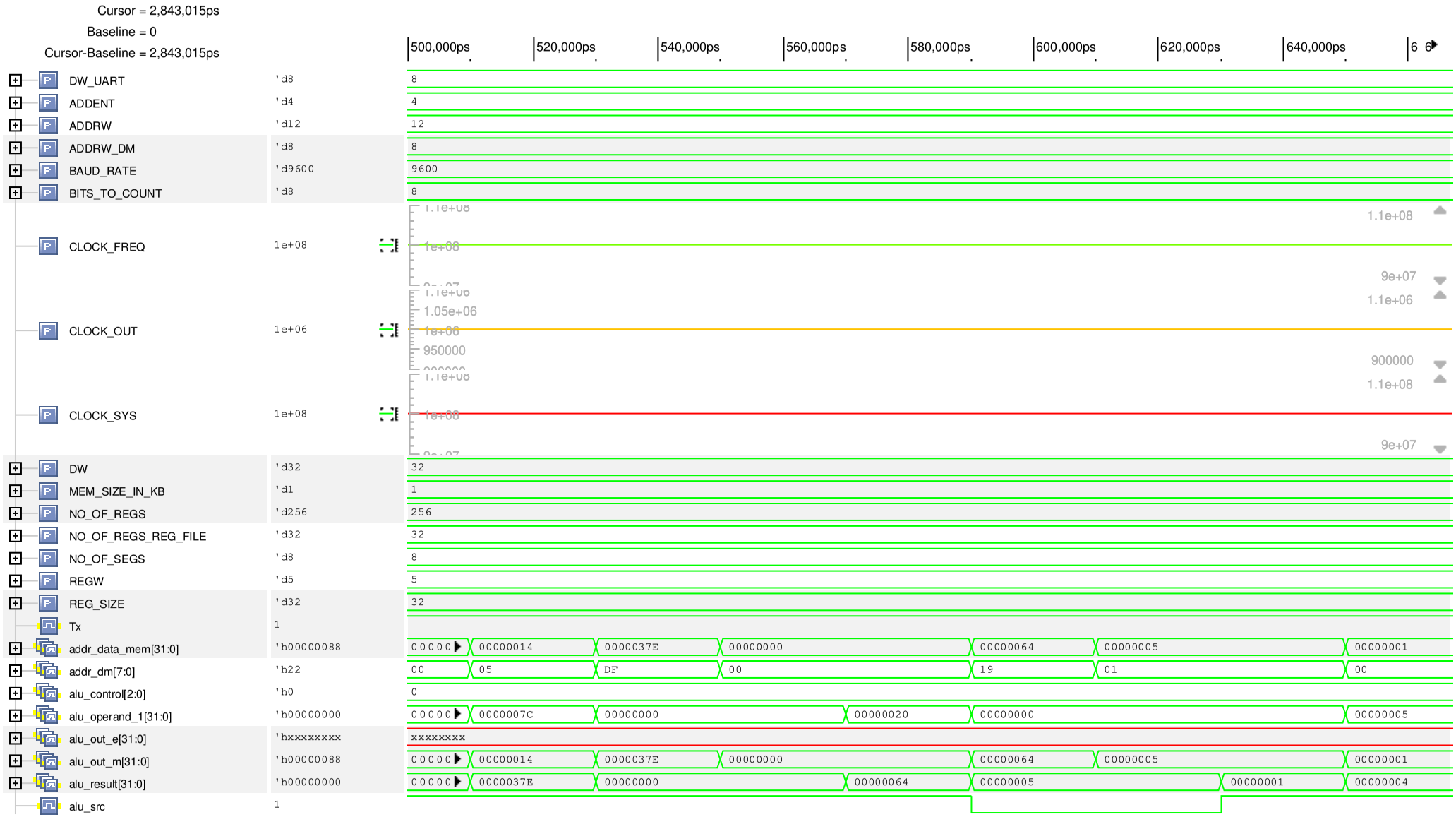

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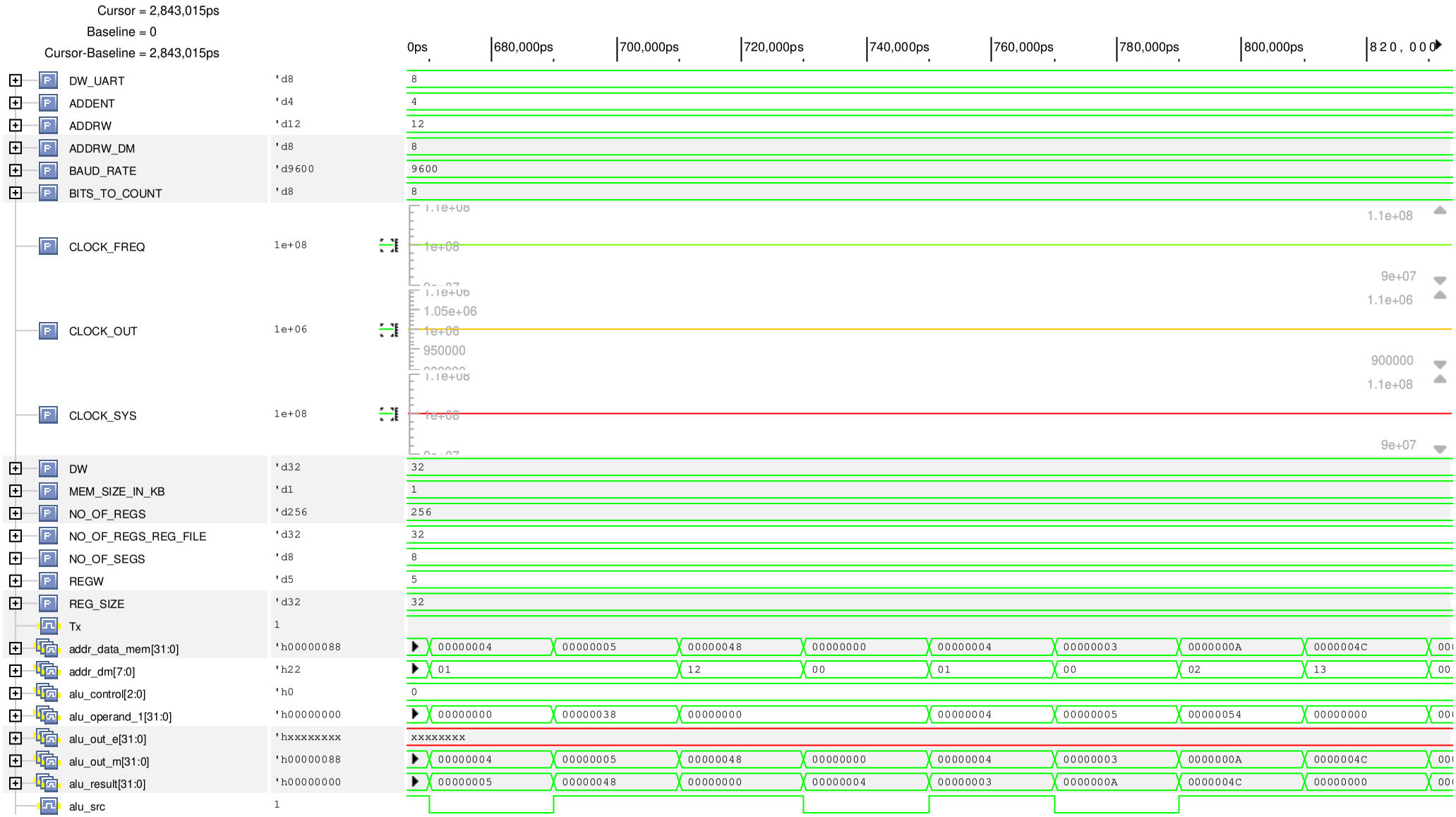

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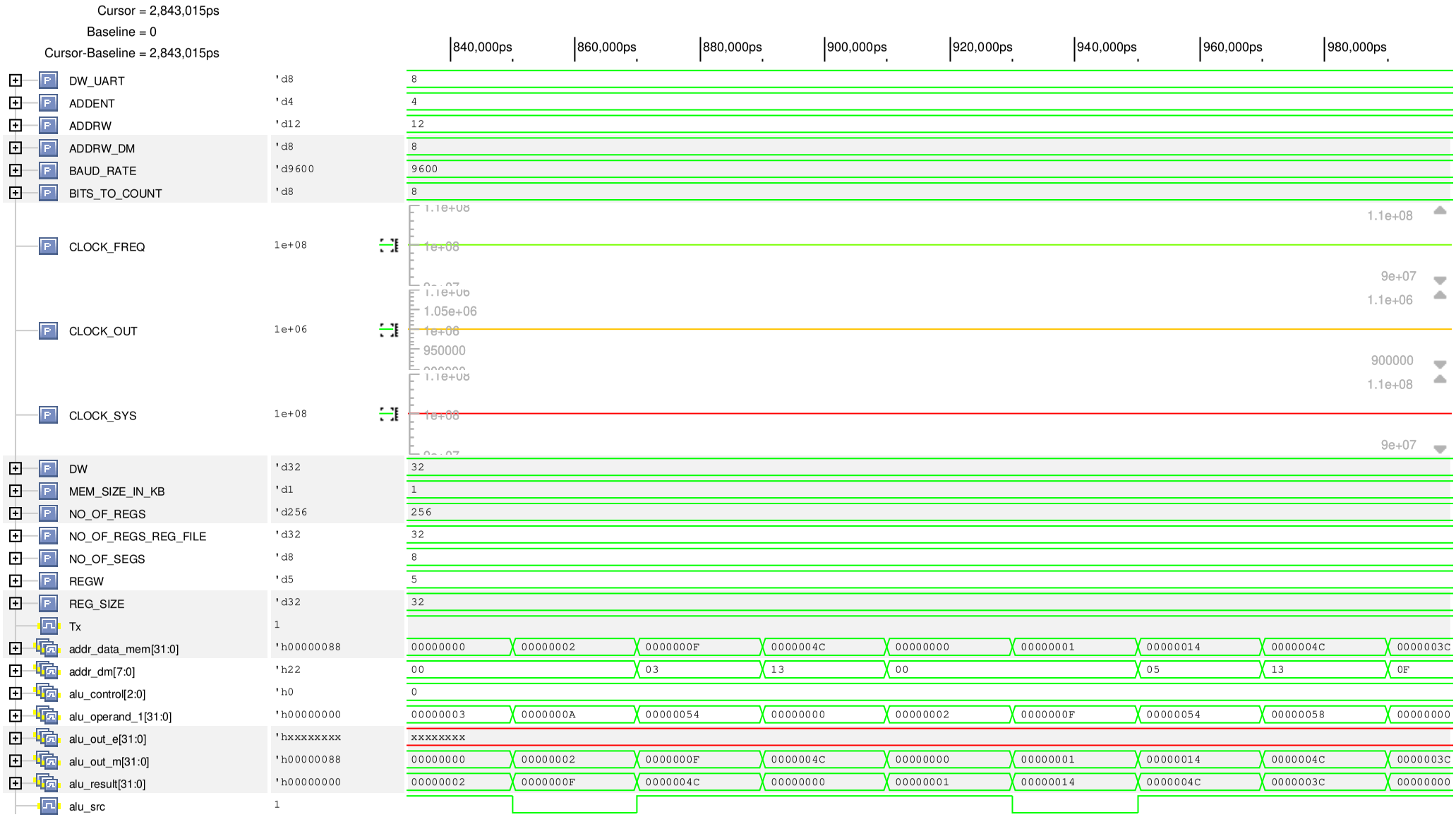

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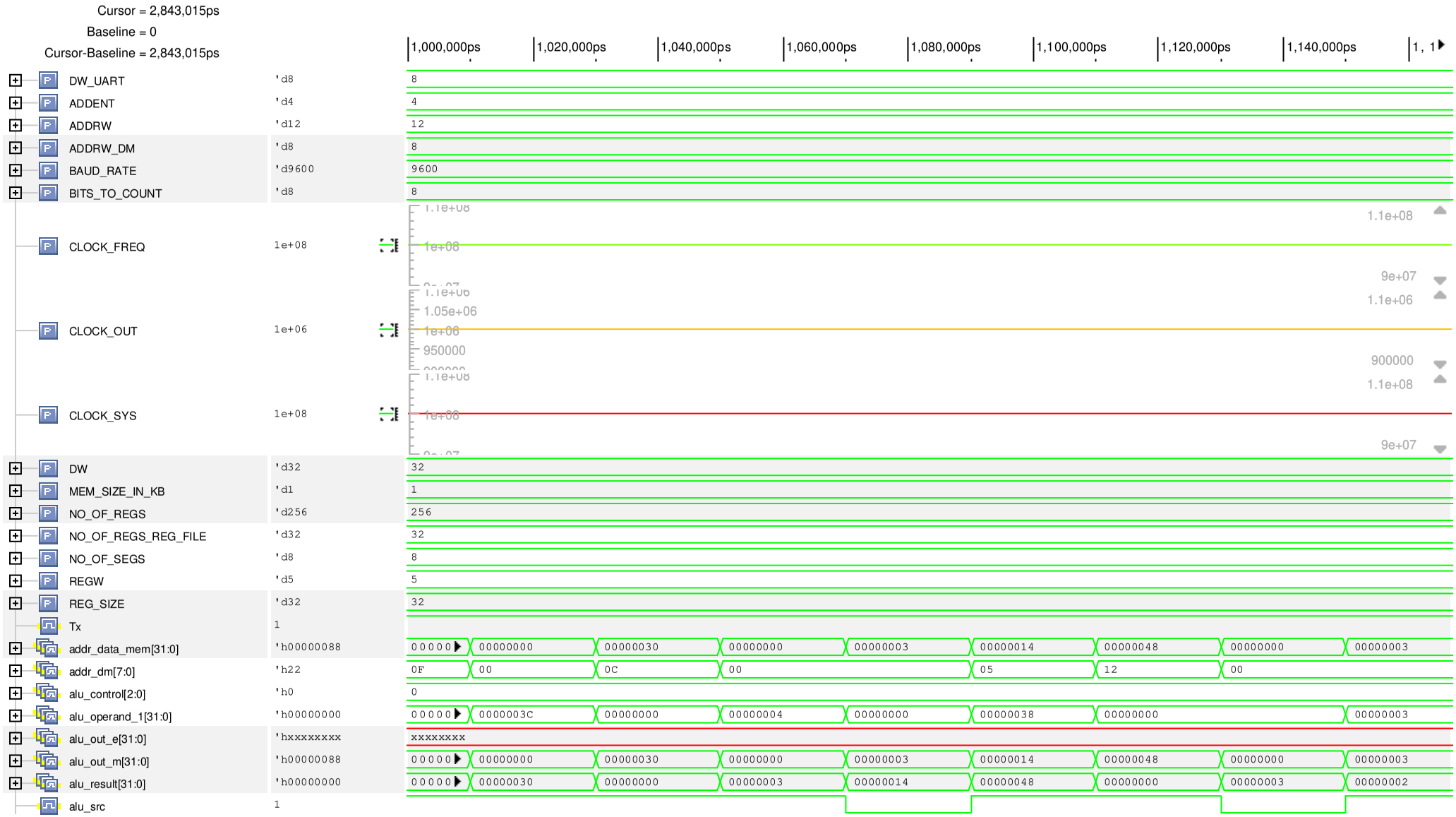

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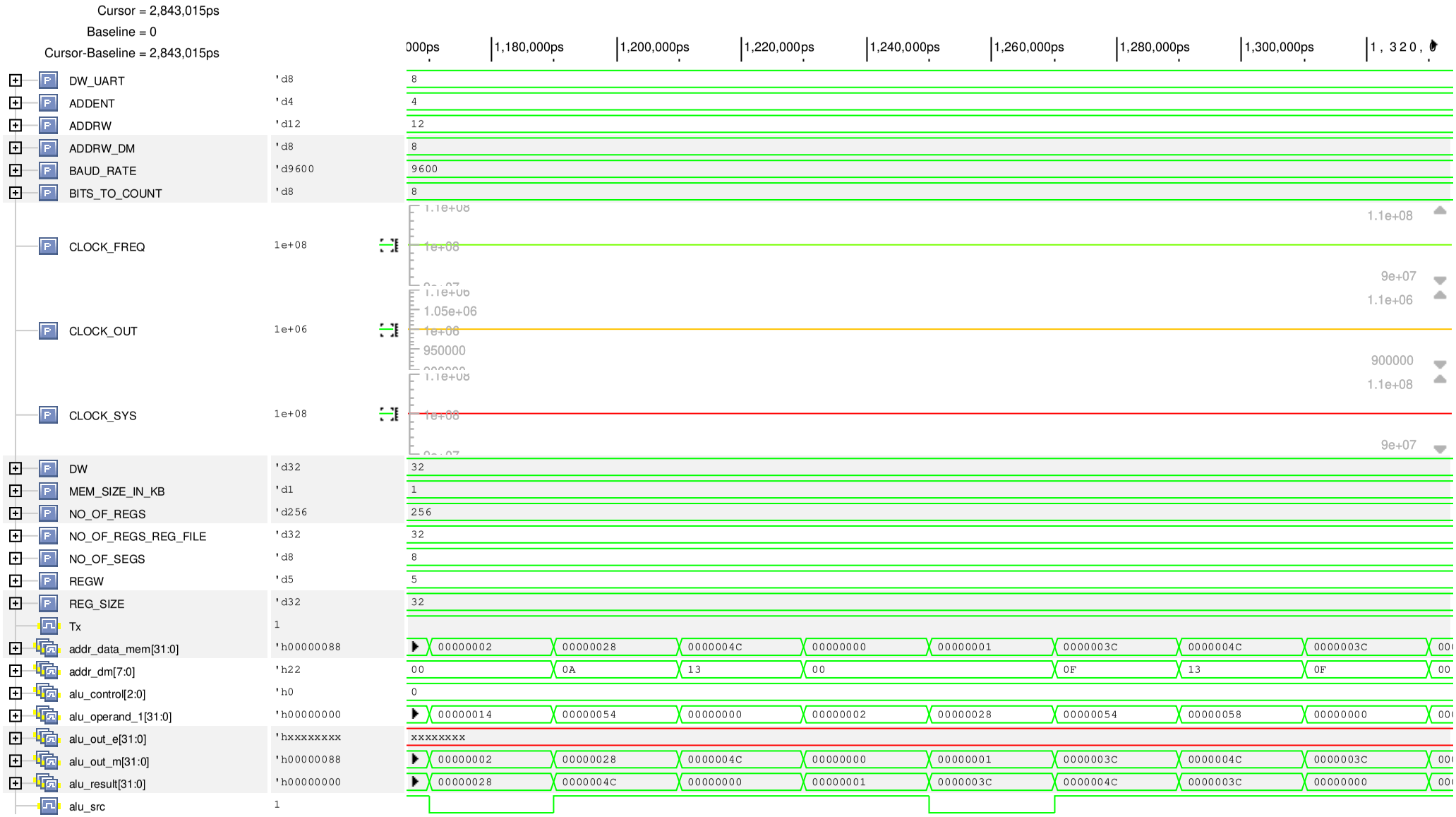

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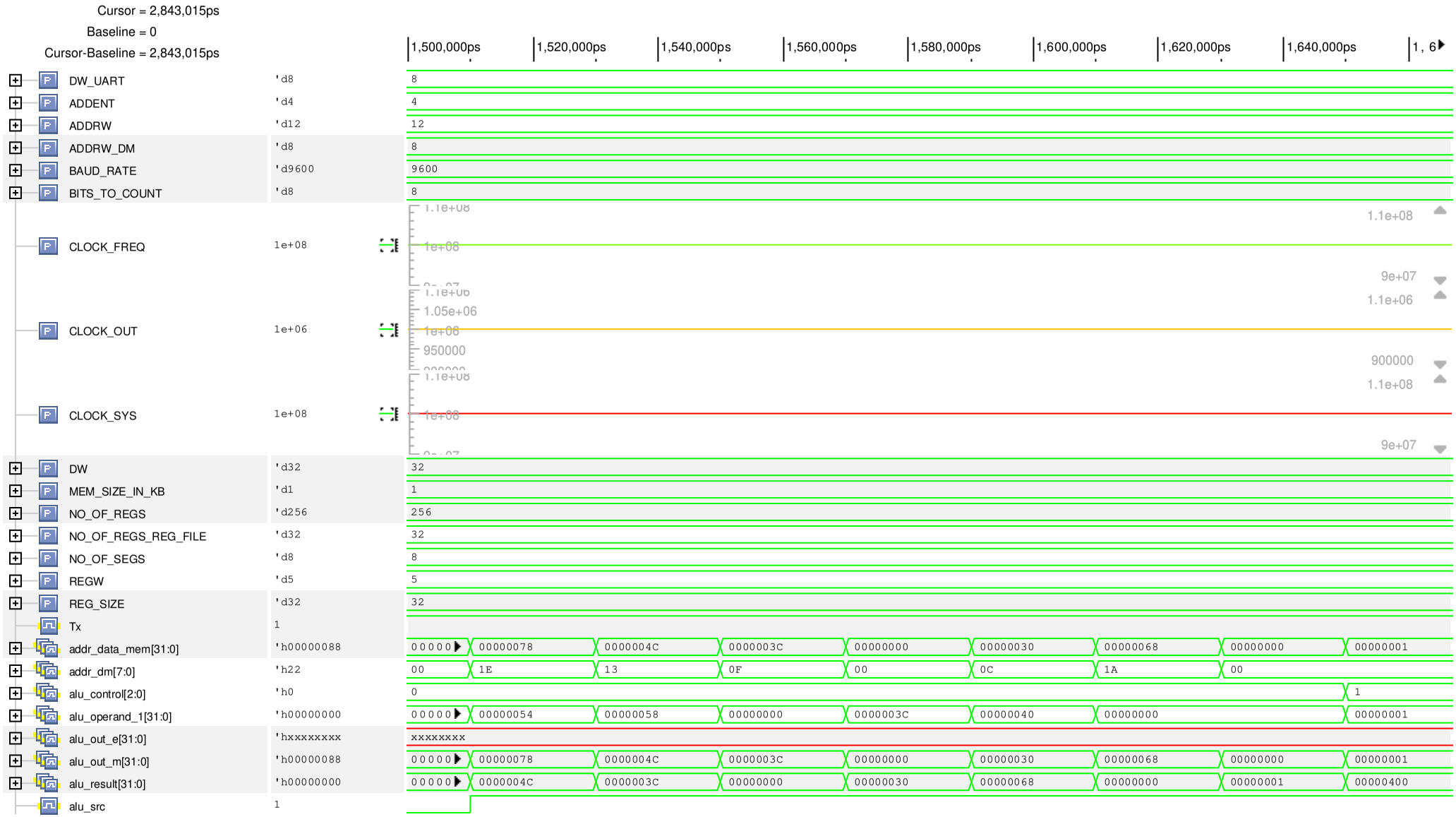

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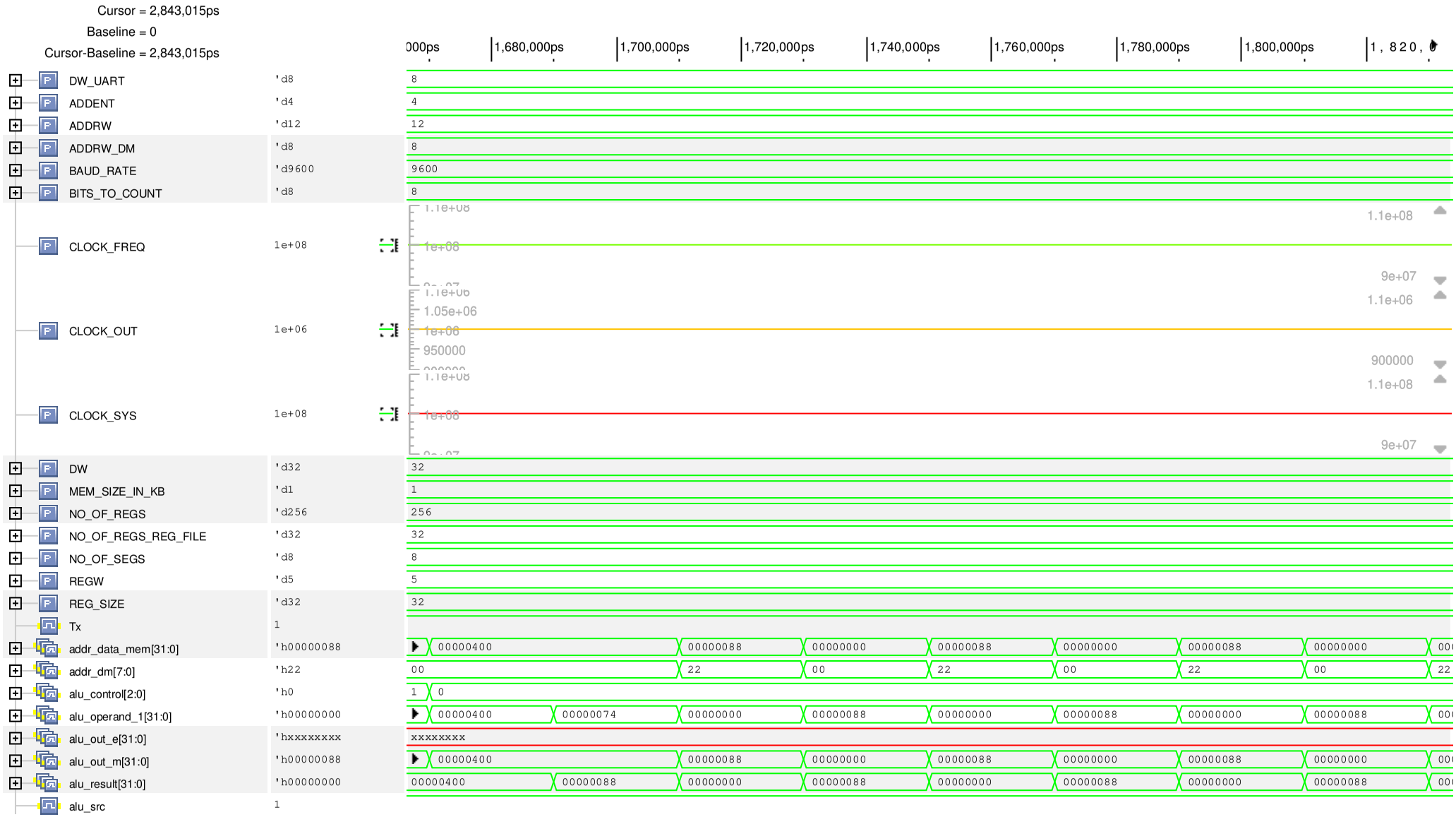

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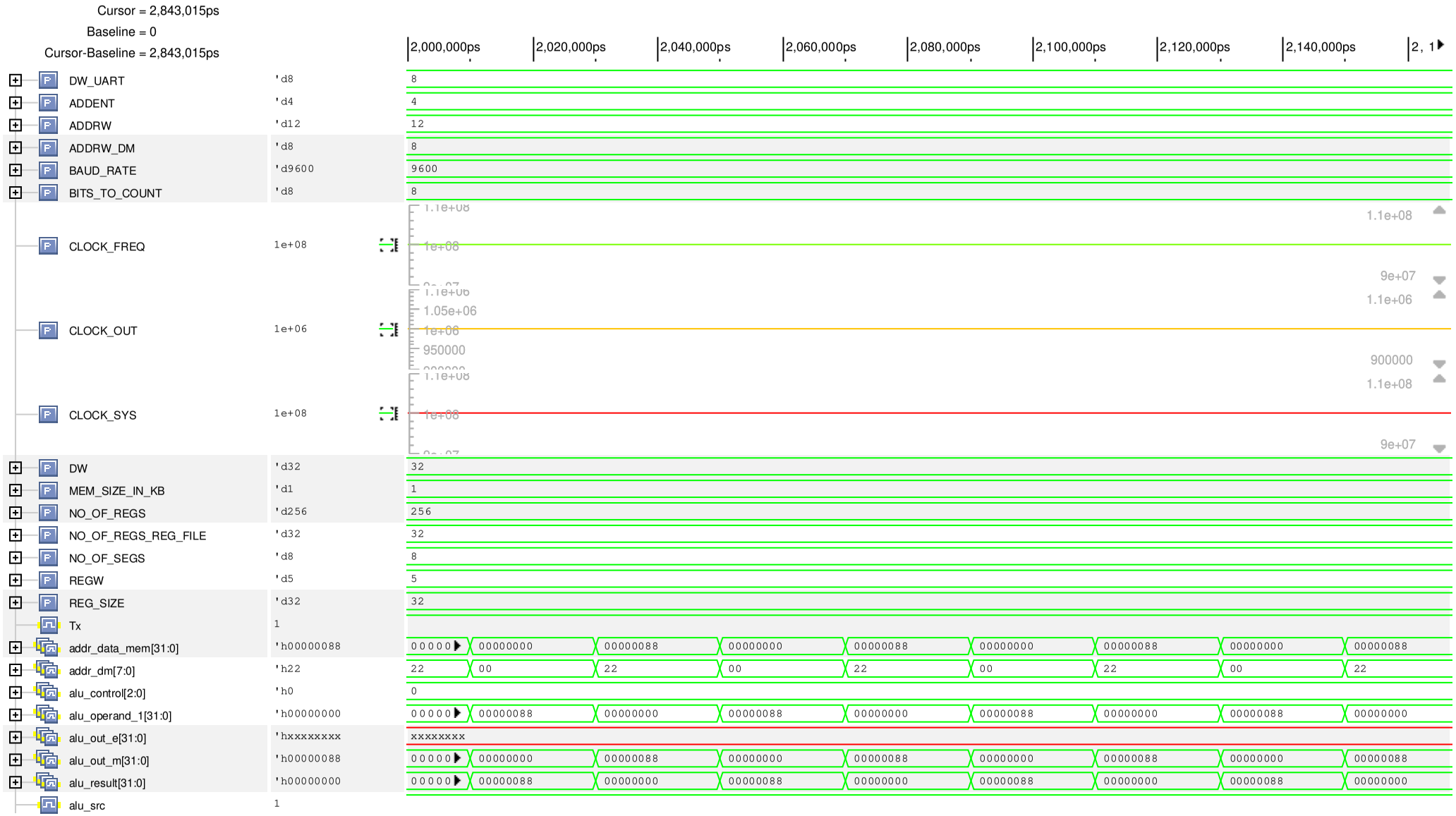

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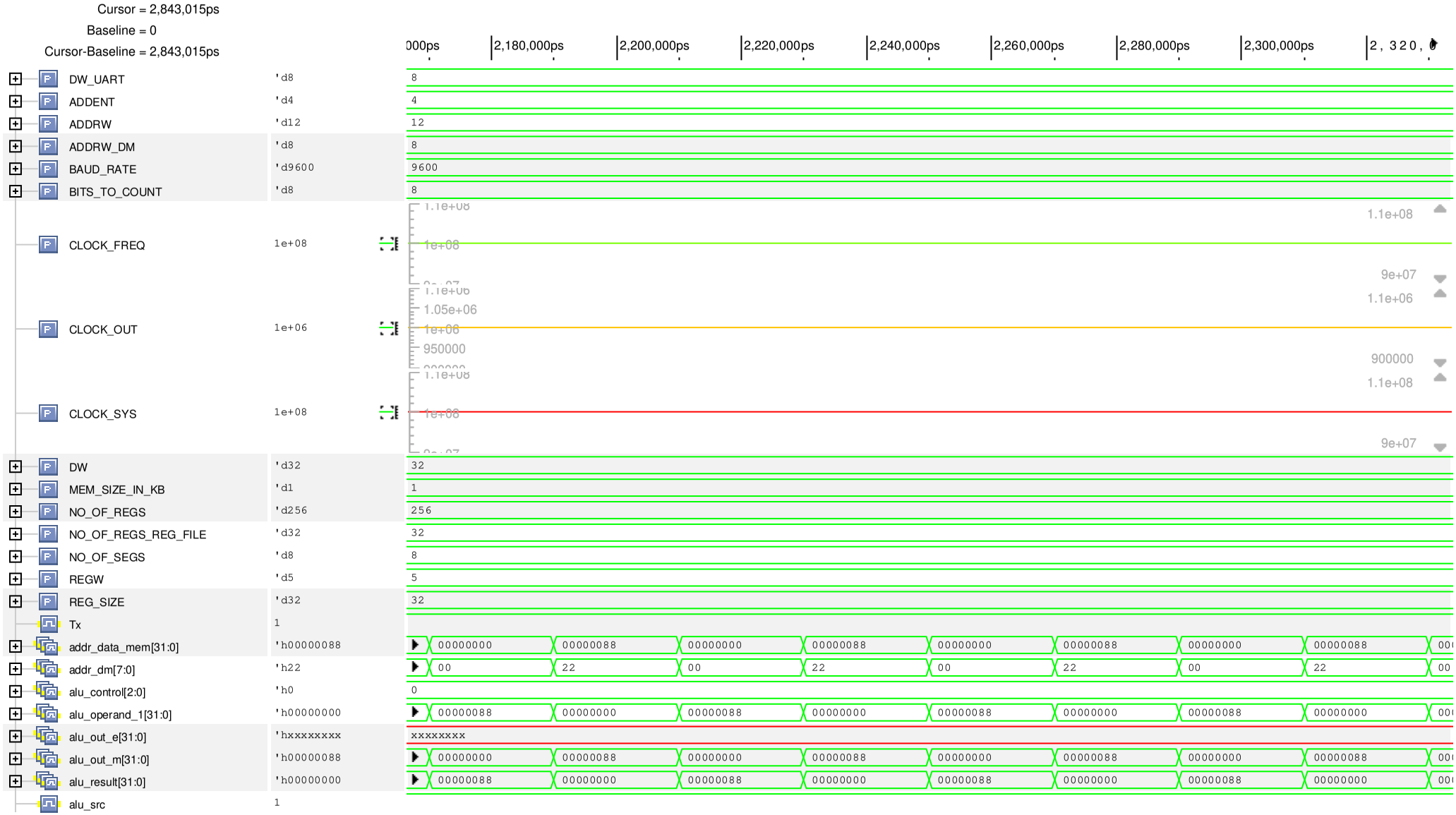

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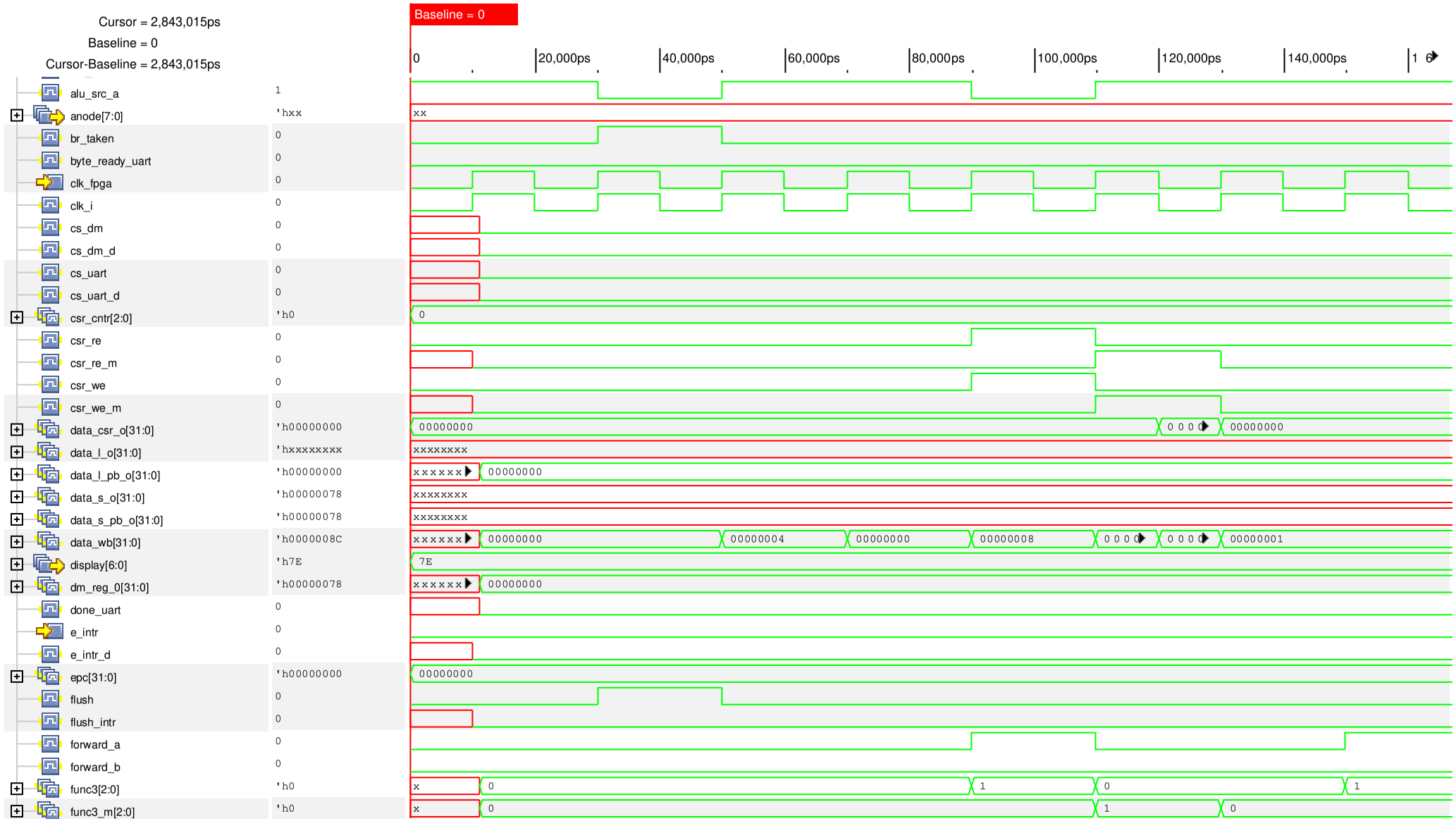

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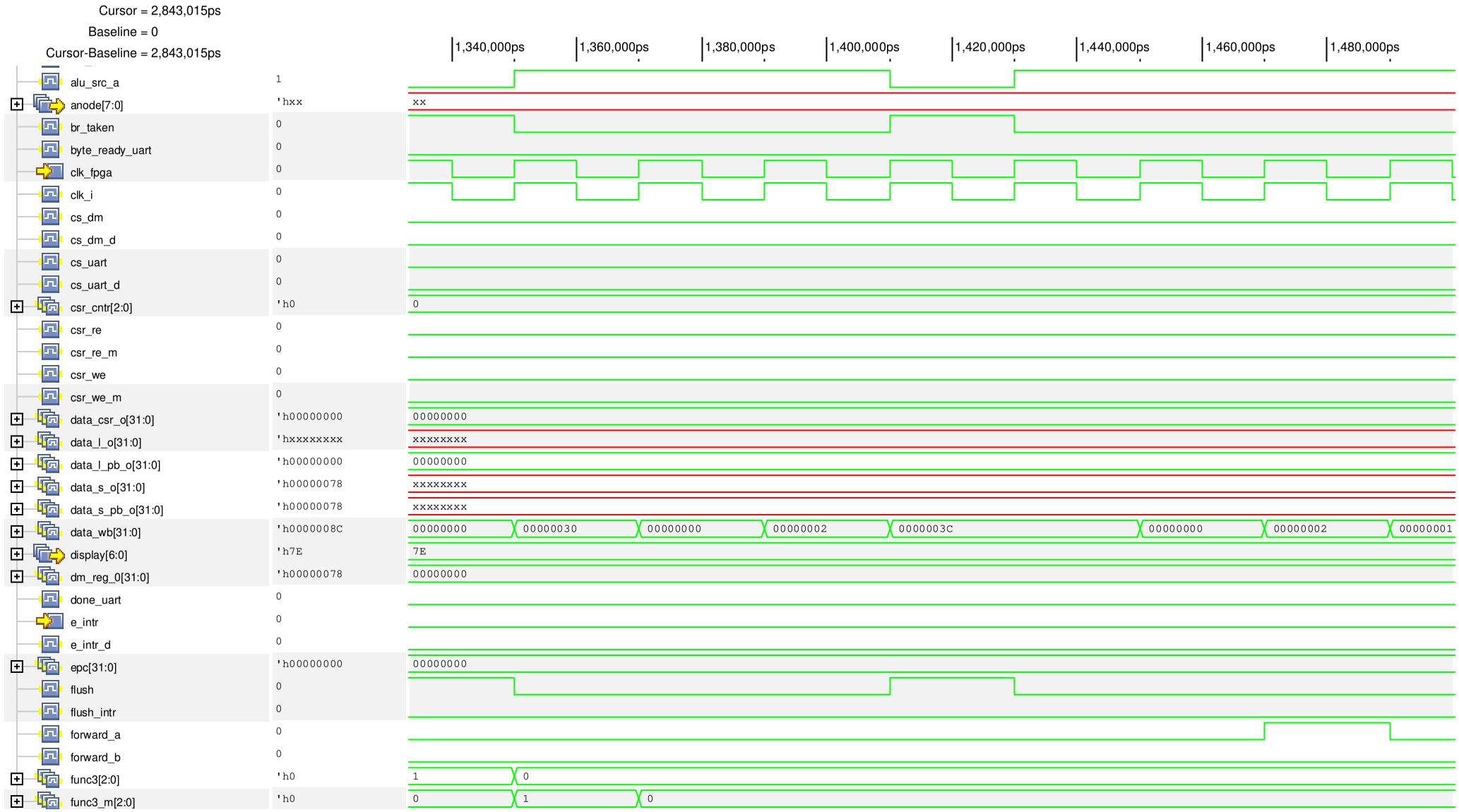

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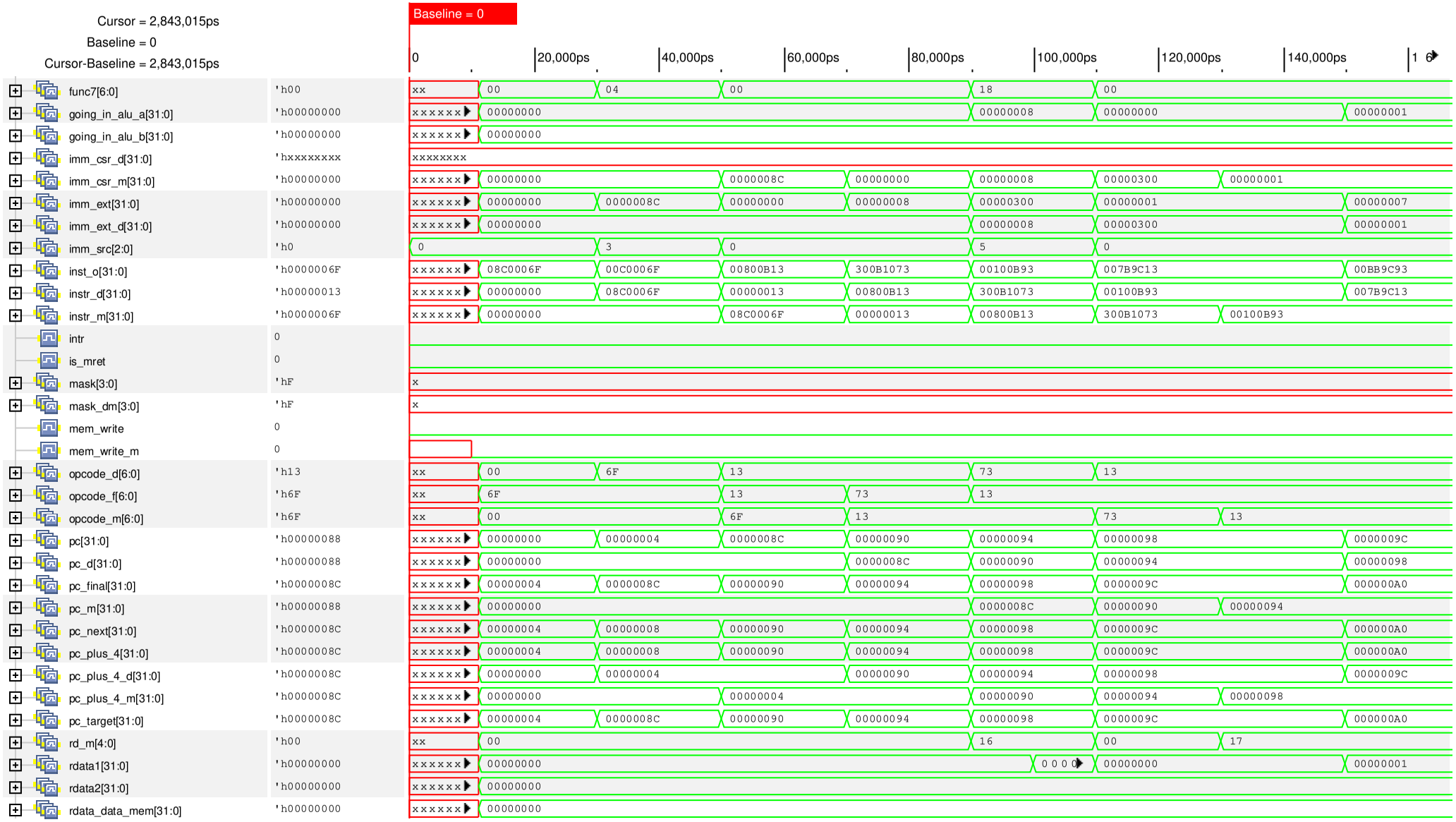

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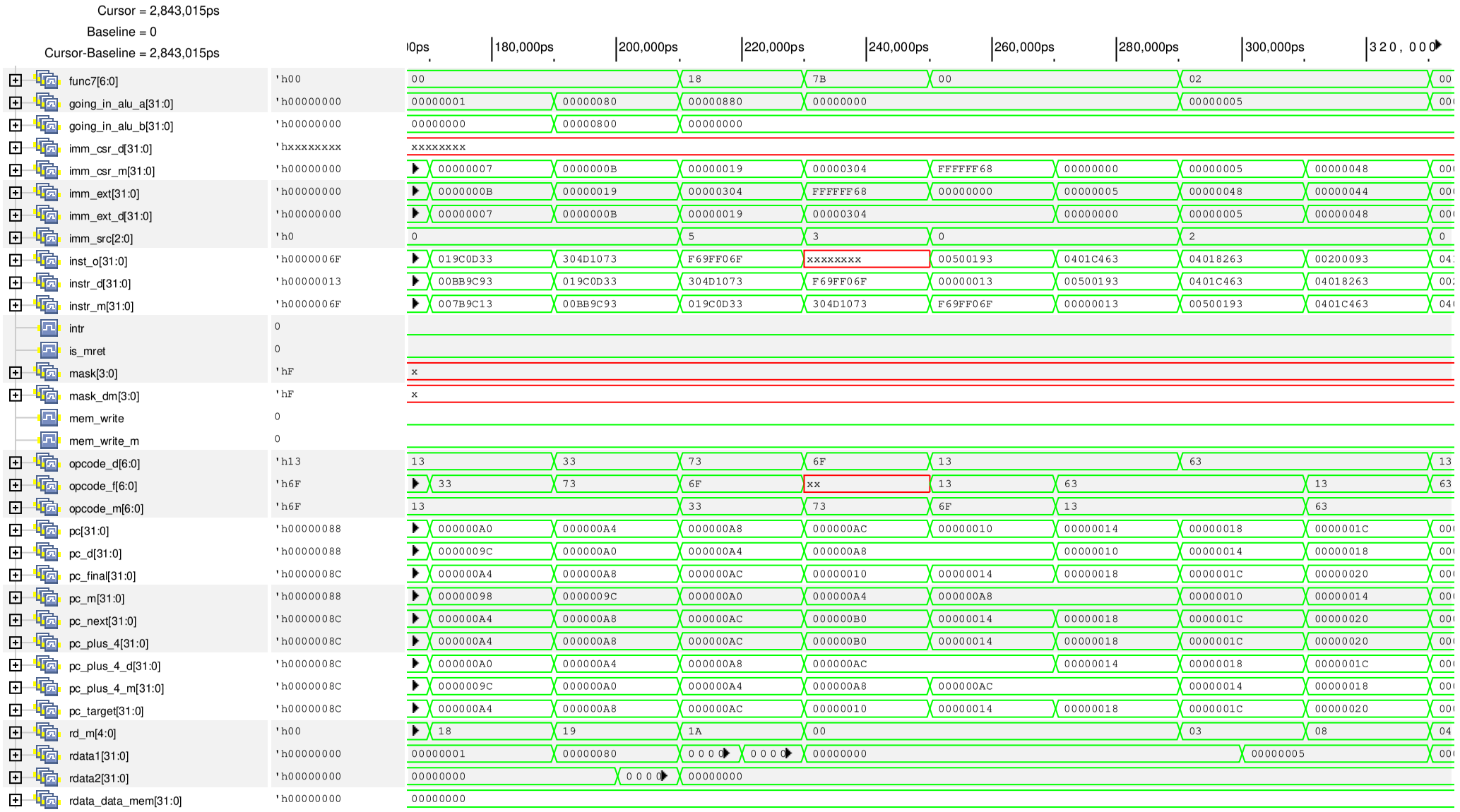

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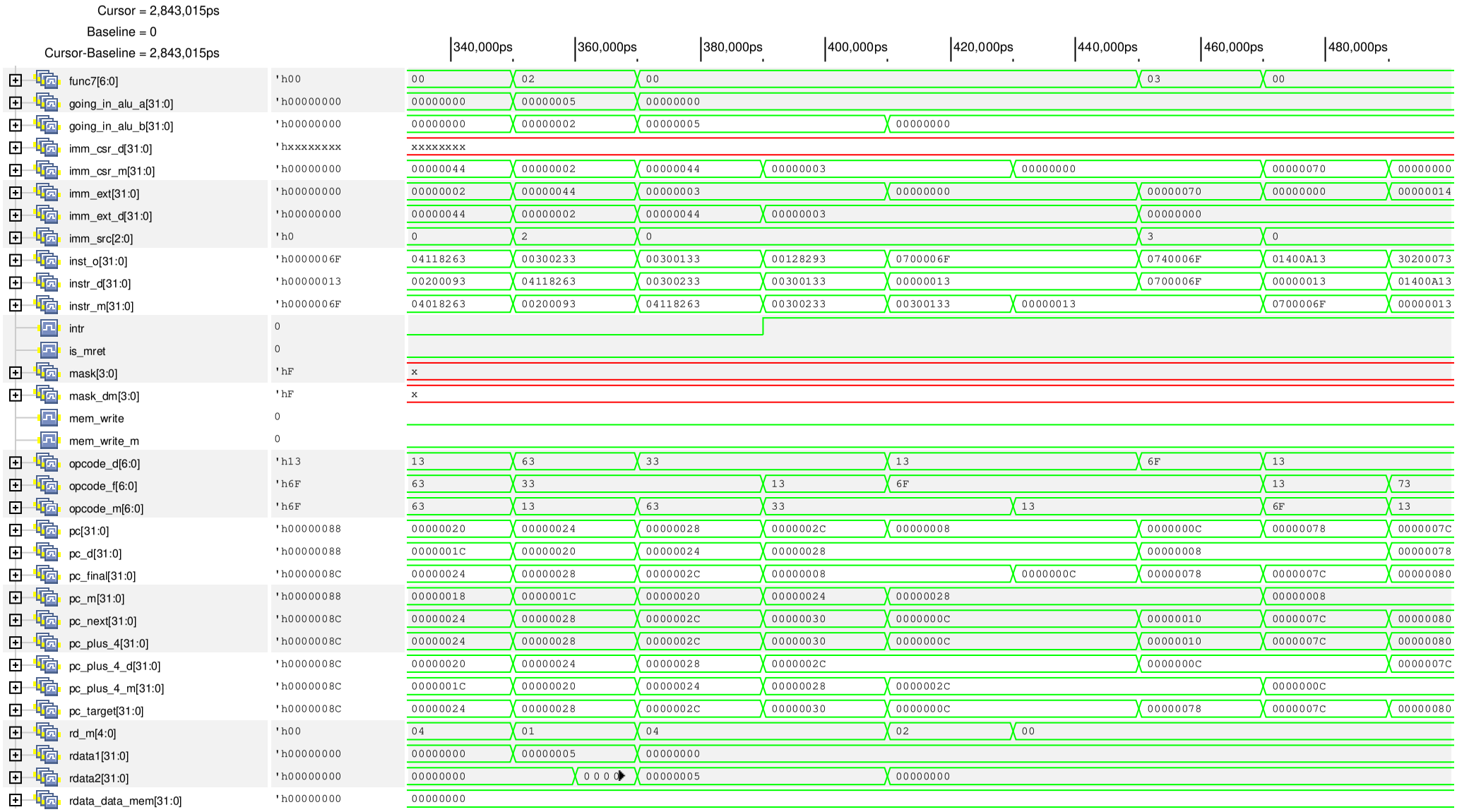

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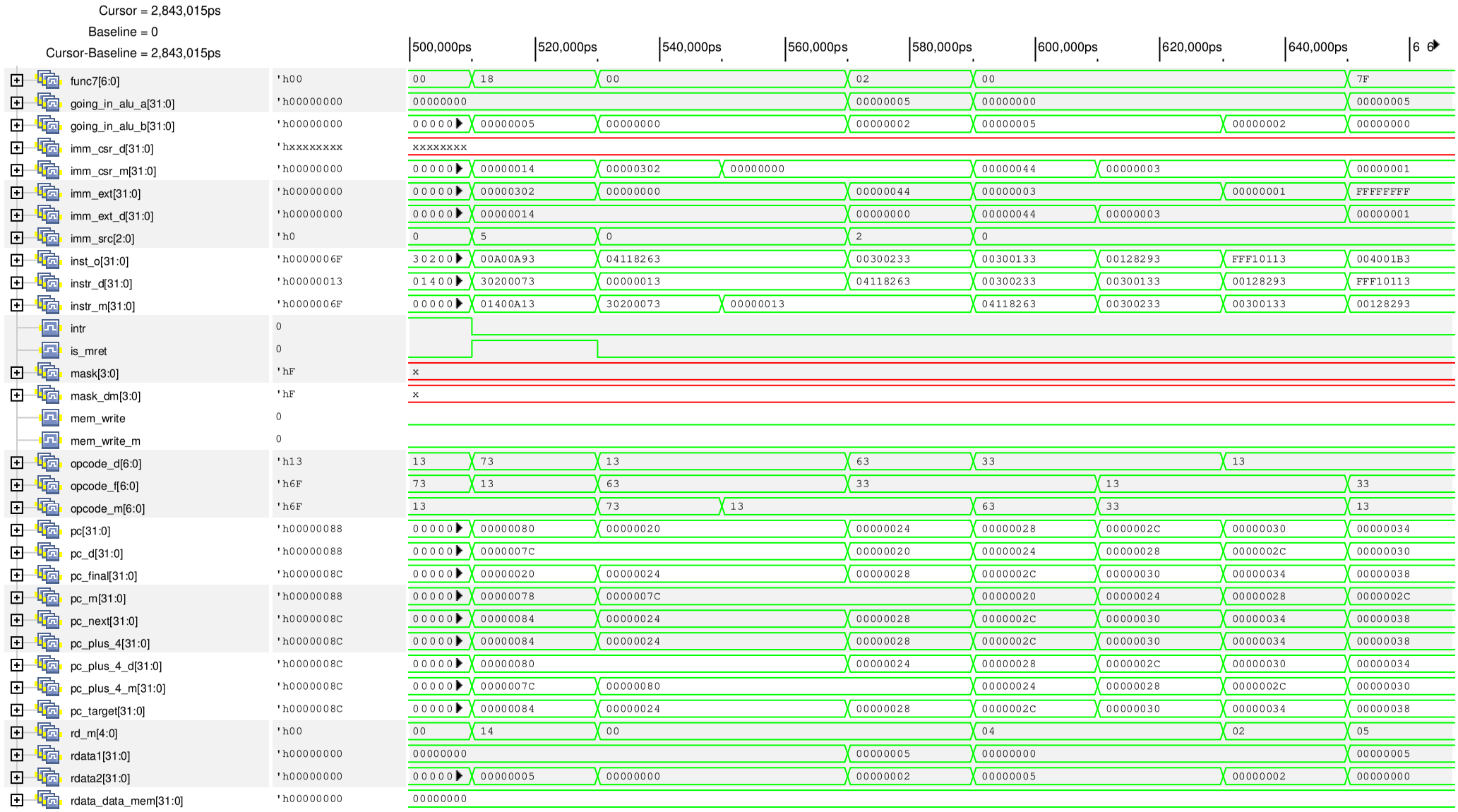

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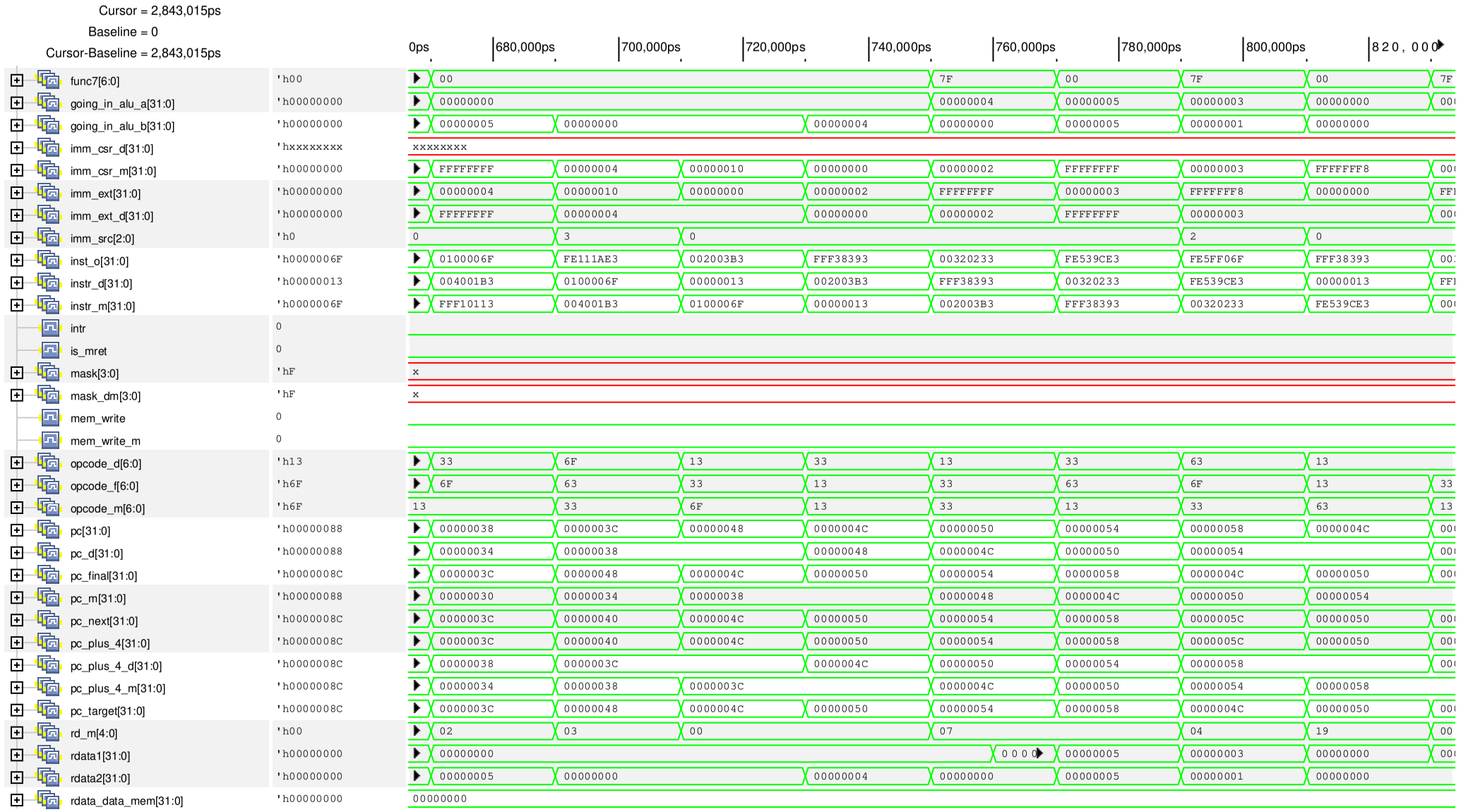
3 stage pipelined RISC-V Core



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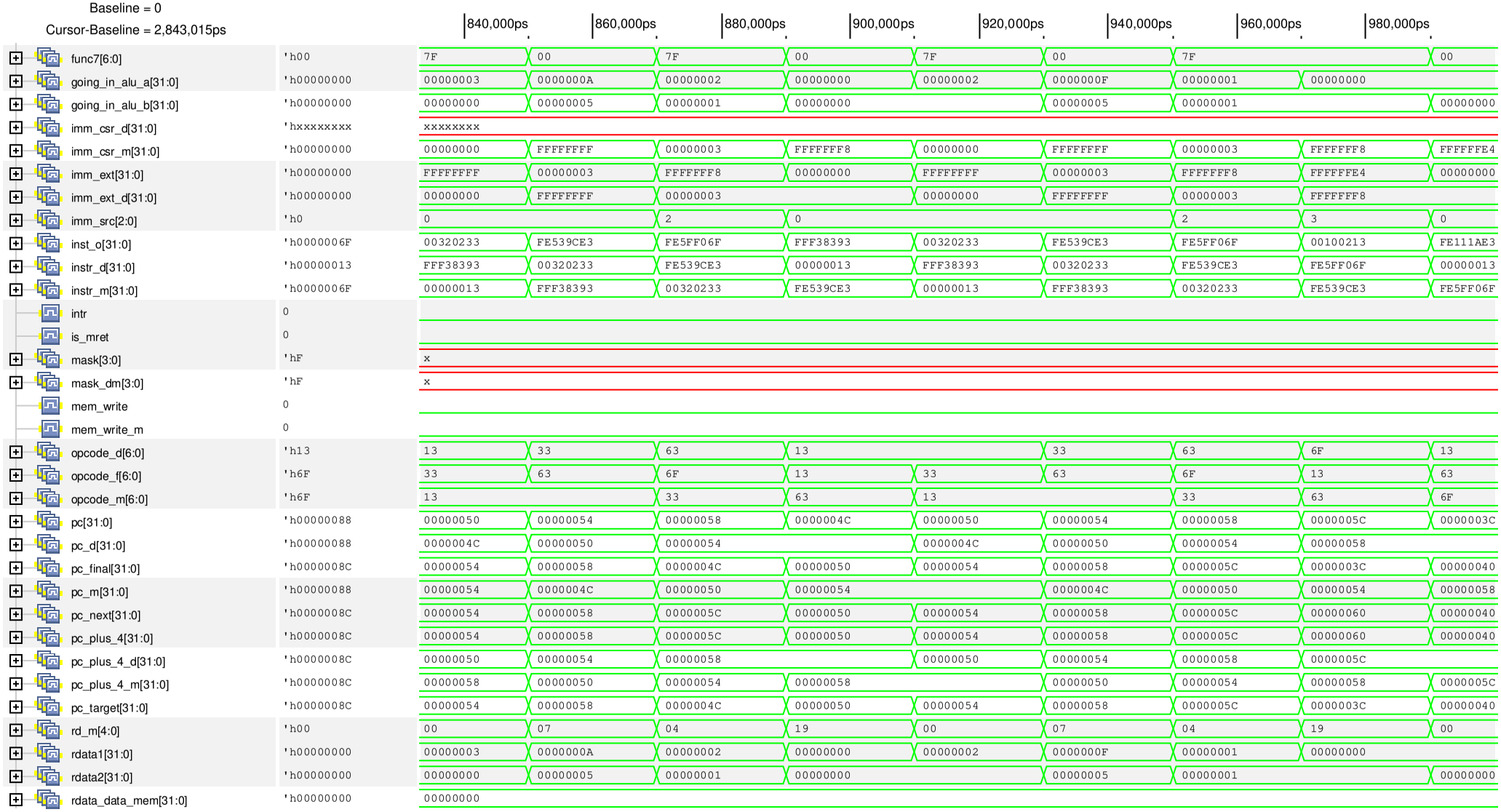
Cursor = 2,843,015ps
Baseline = 0
Cursor-Baseline = 2,843,015ps



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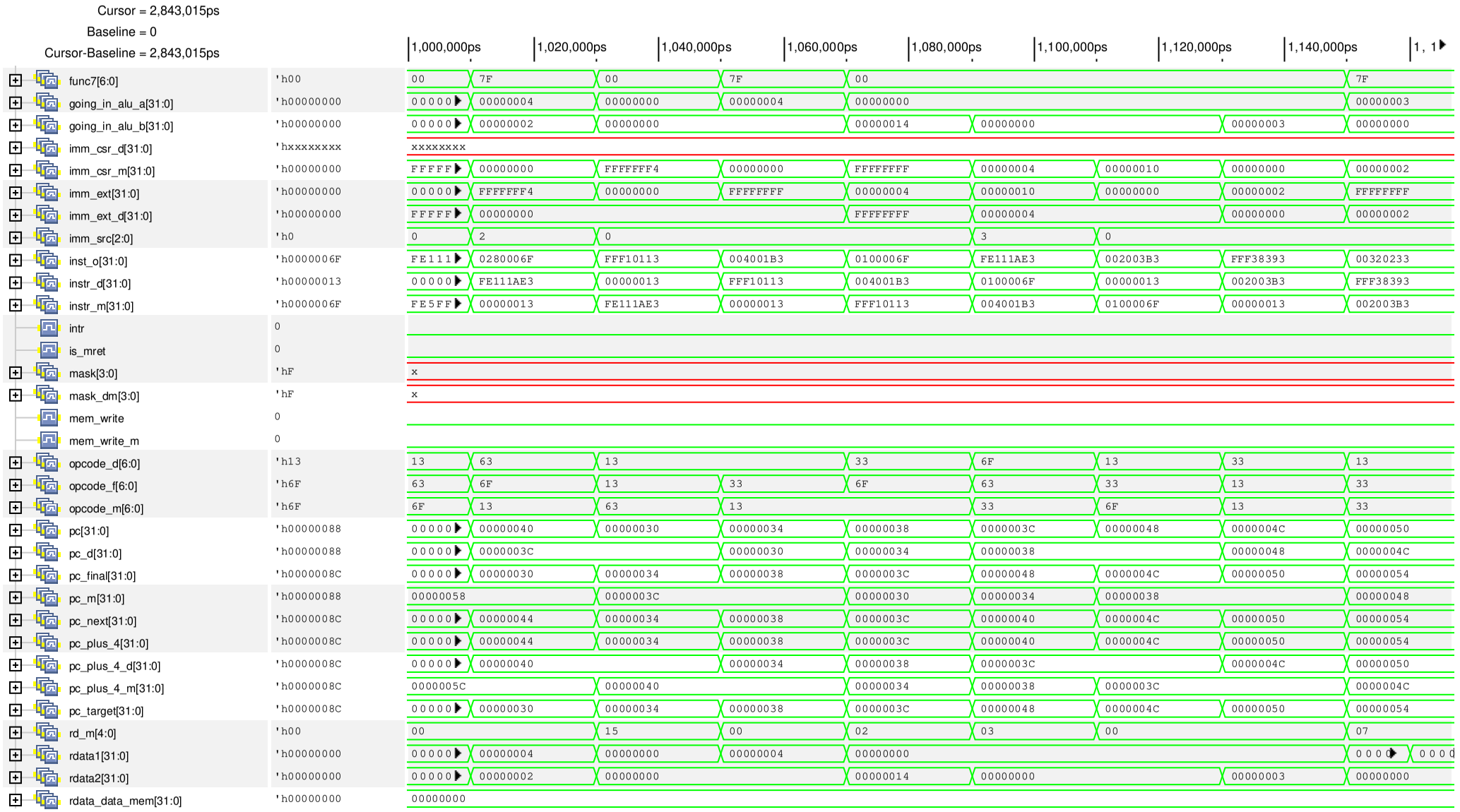
UET

3 stage pipelined RISC-V Core



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UET

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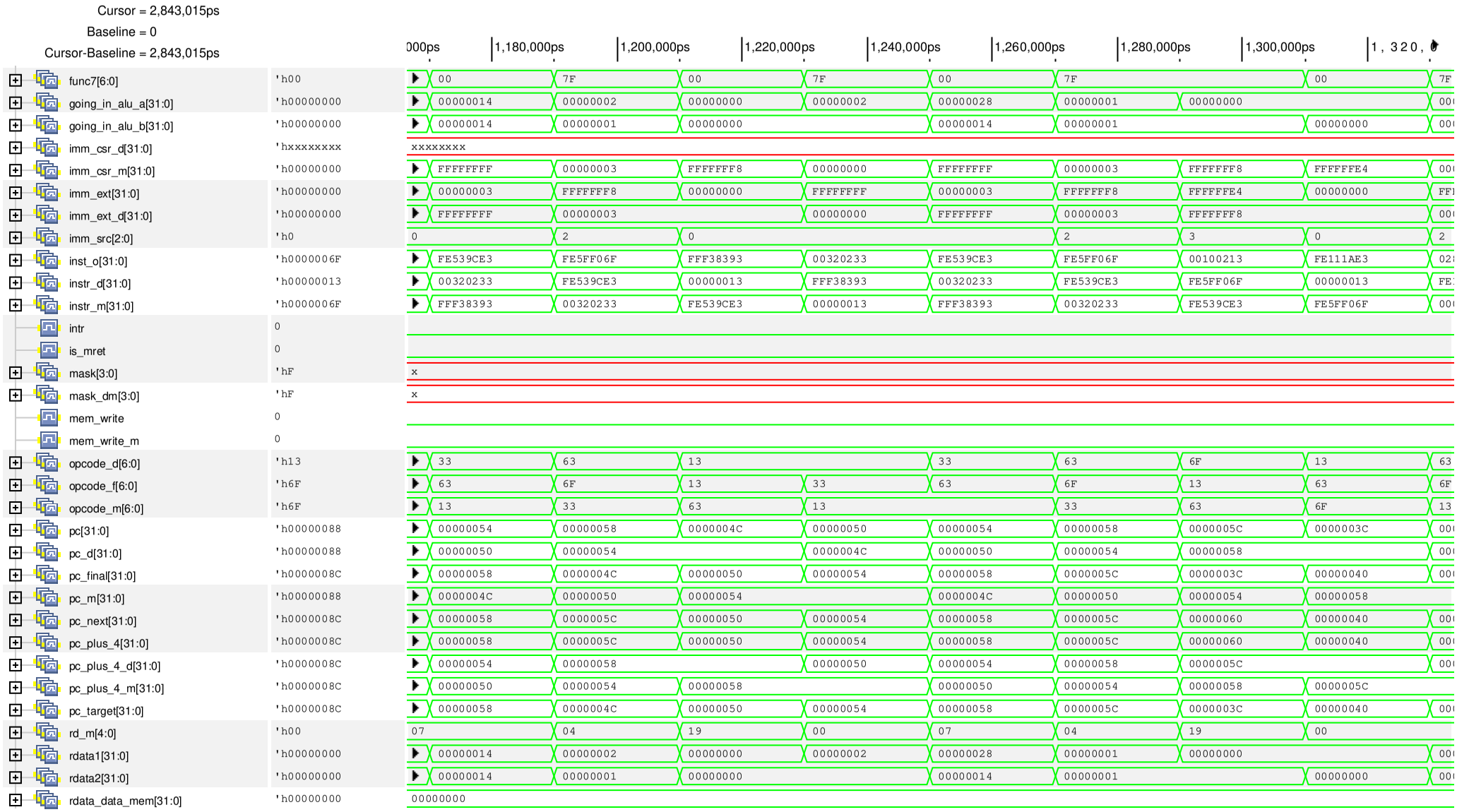

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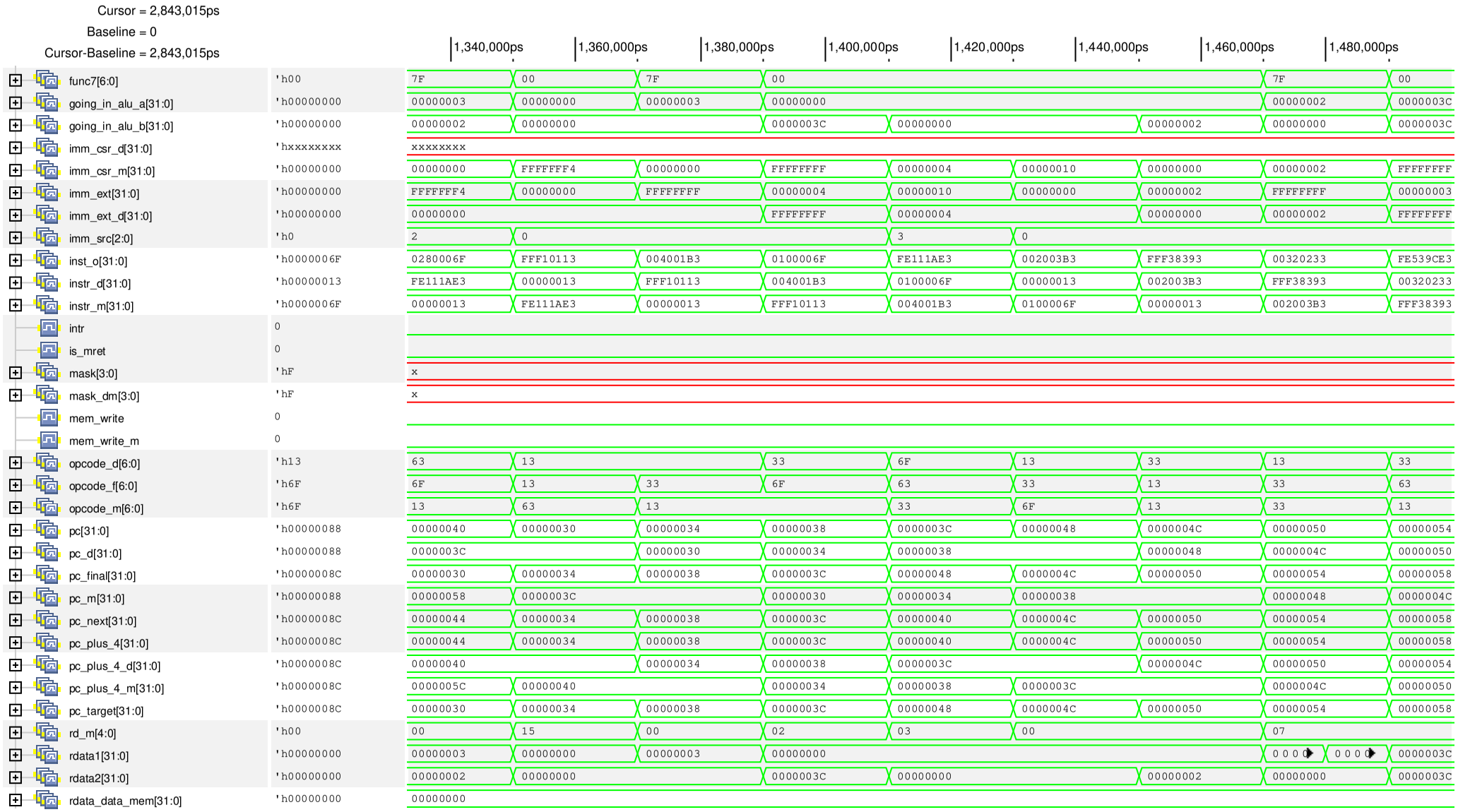
3 stage pipelined RISC-V Core



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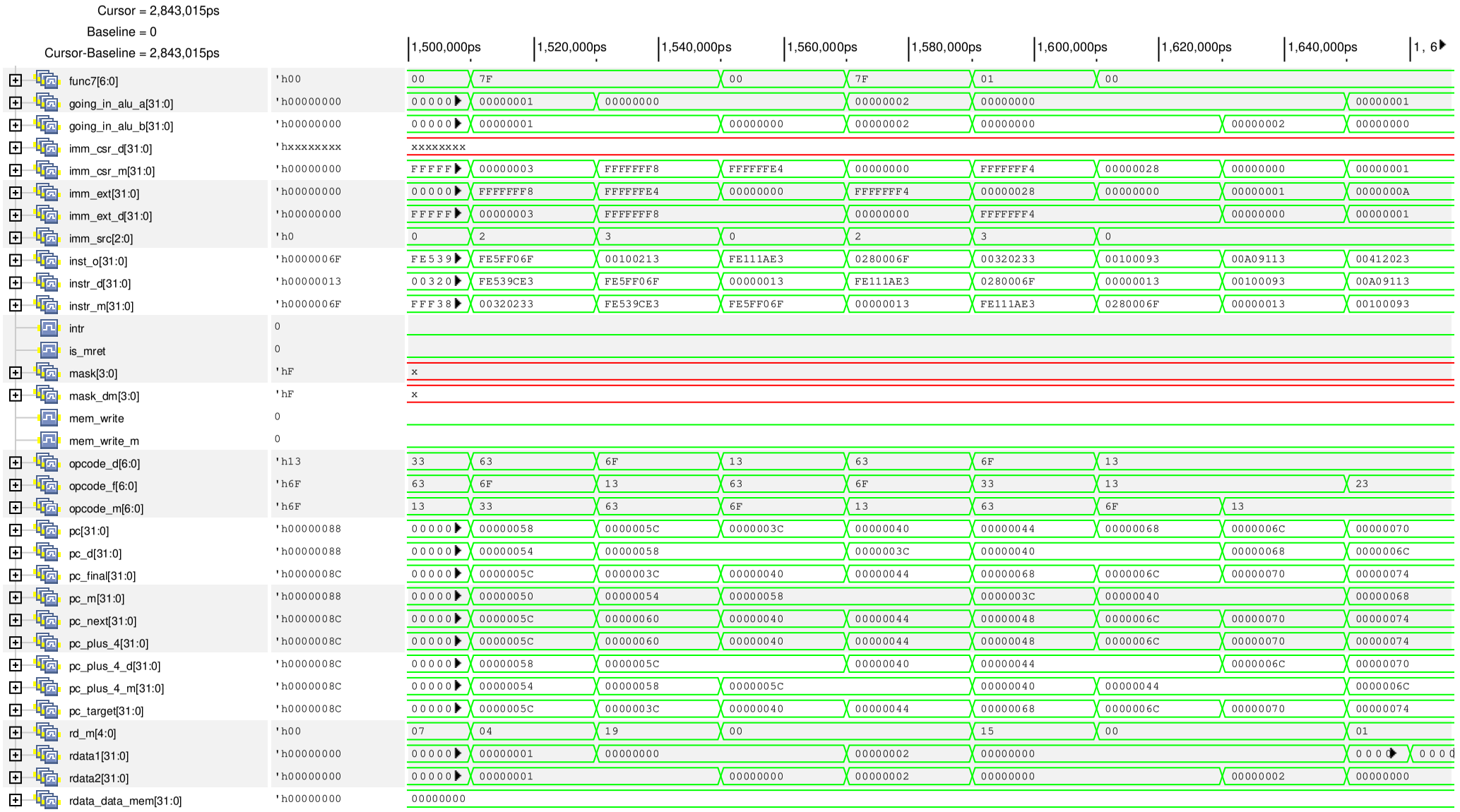
UET

3 stage pipelined RISC-V Core



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UET

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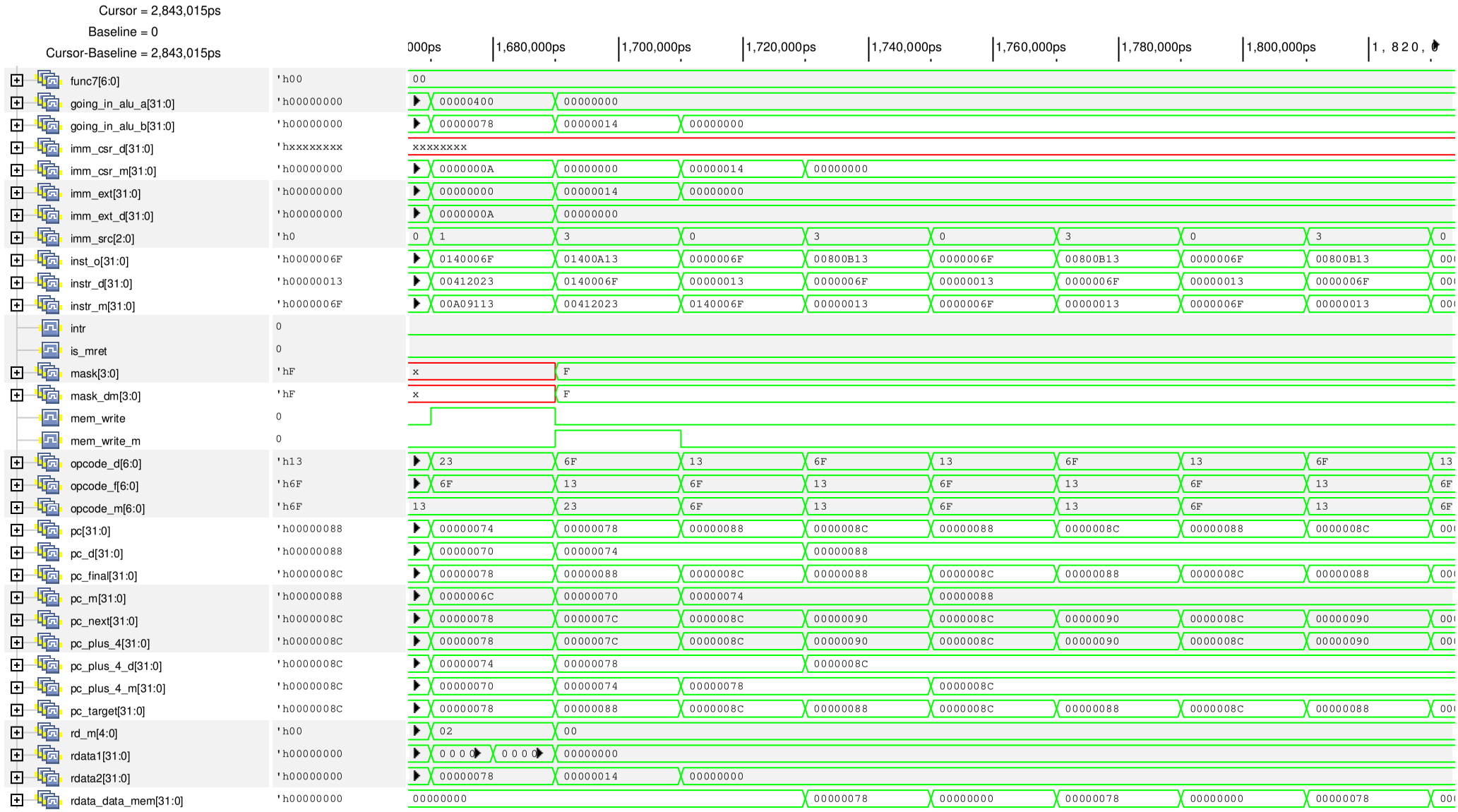

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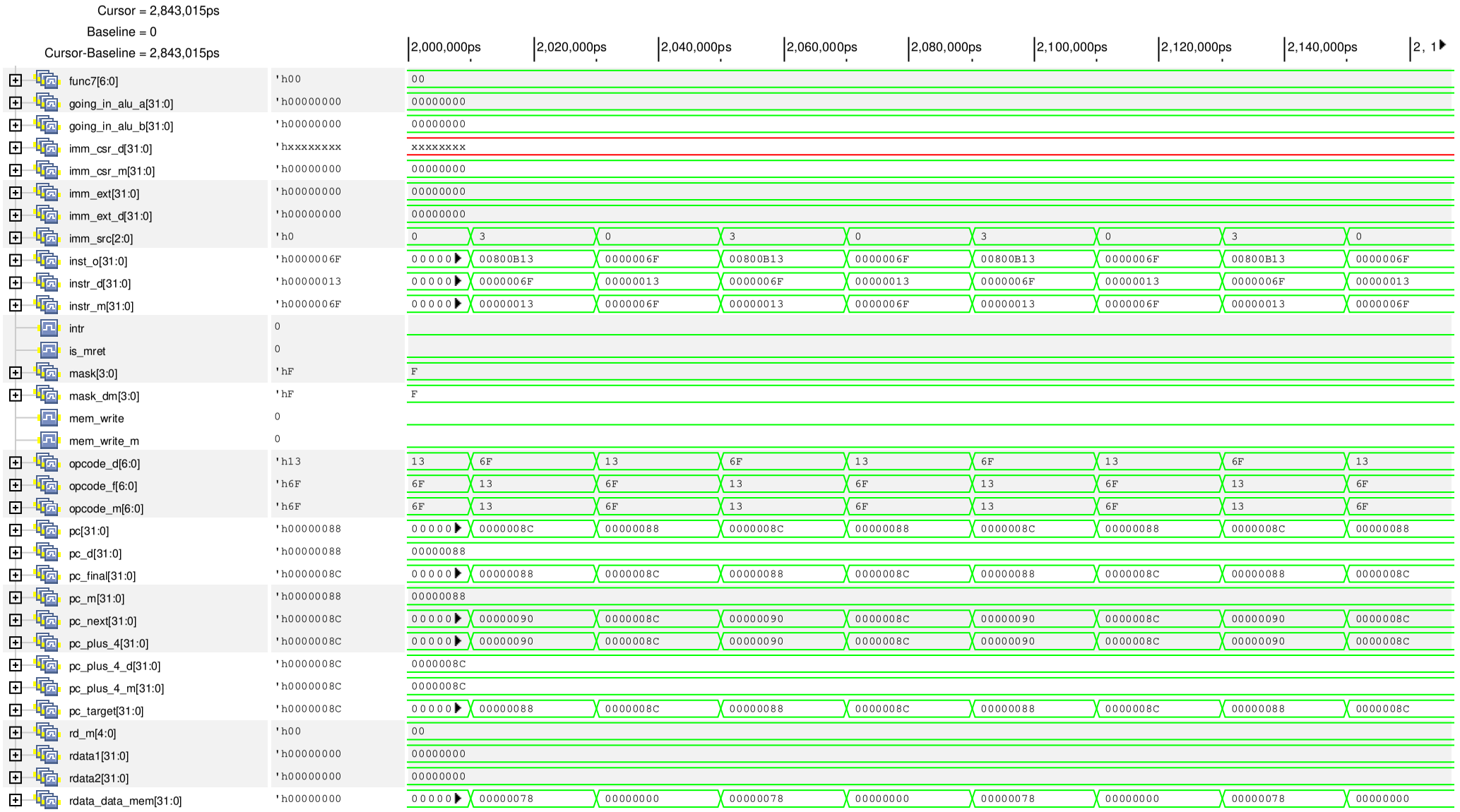

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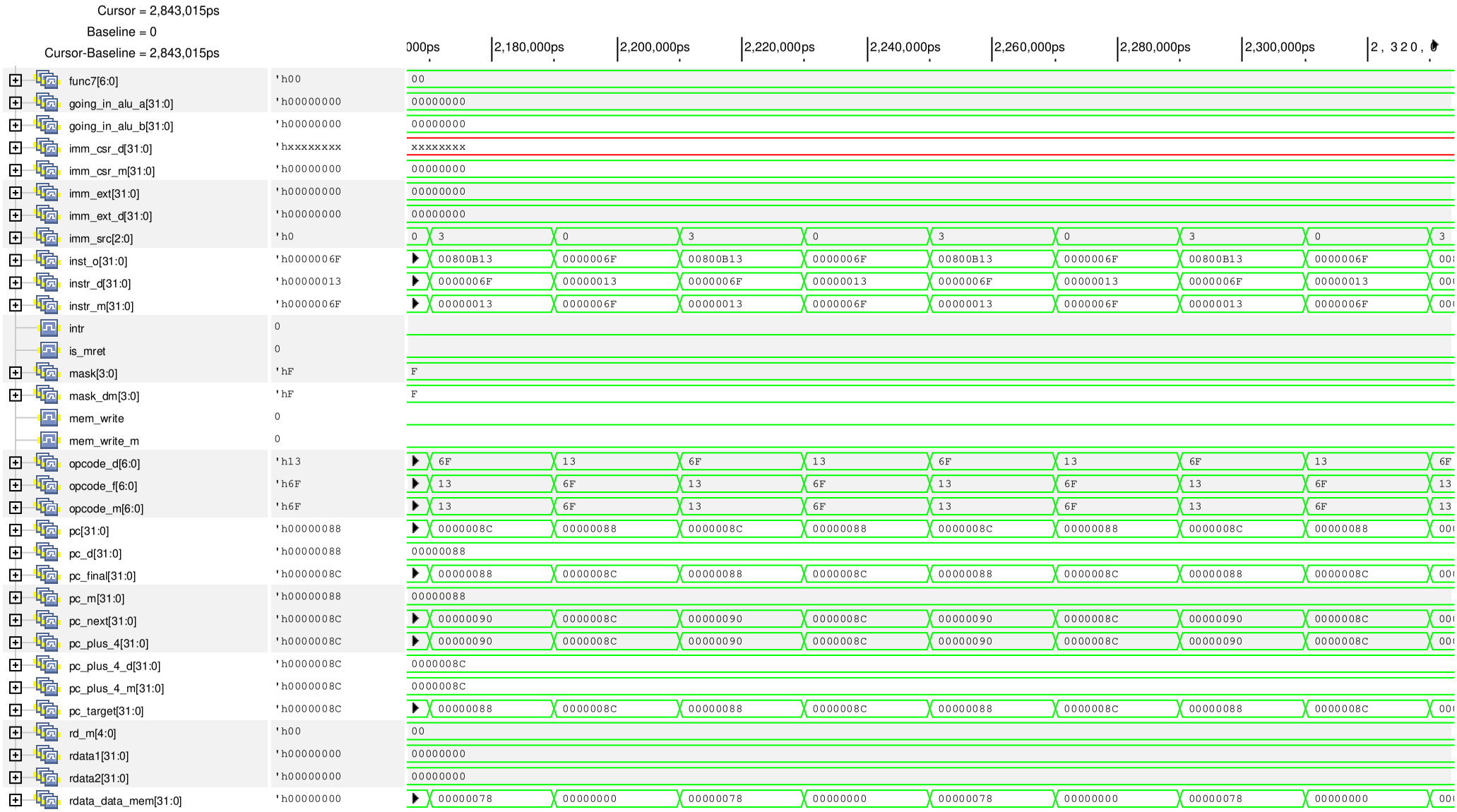

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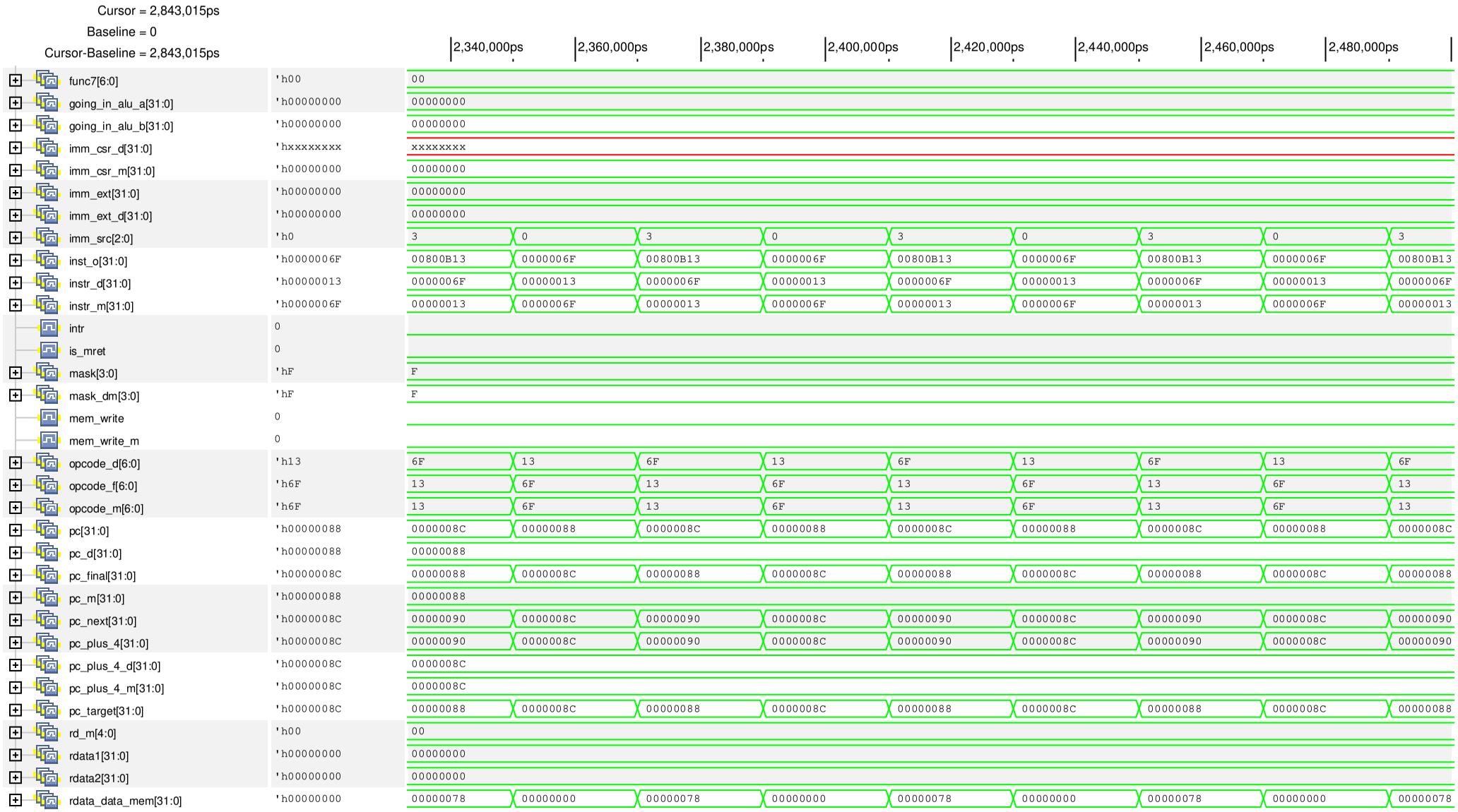

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UET

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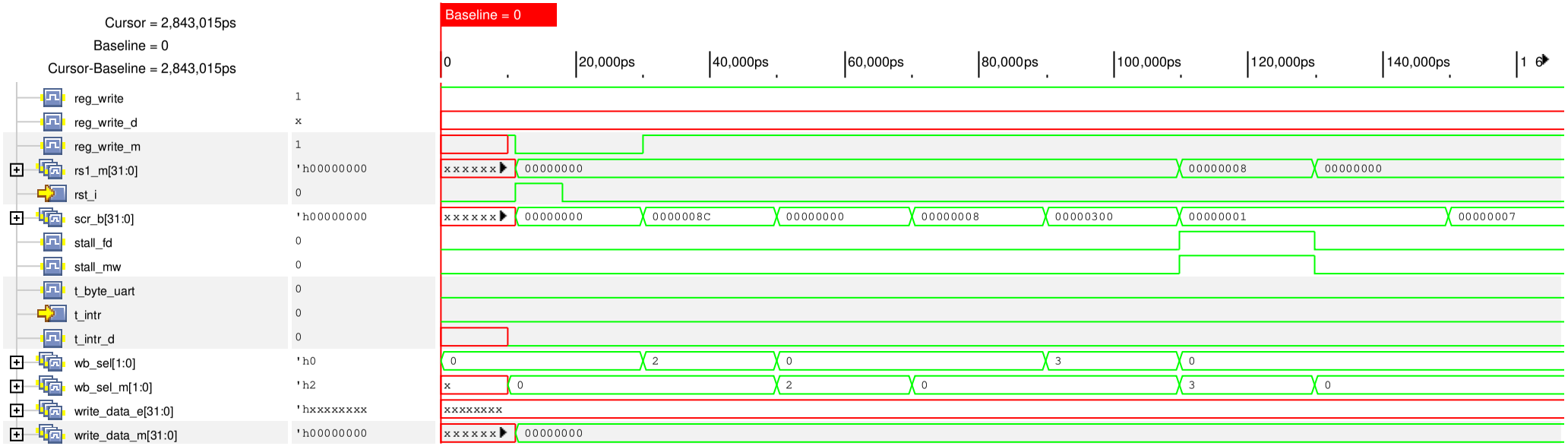

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UET

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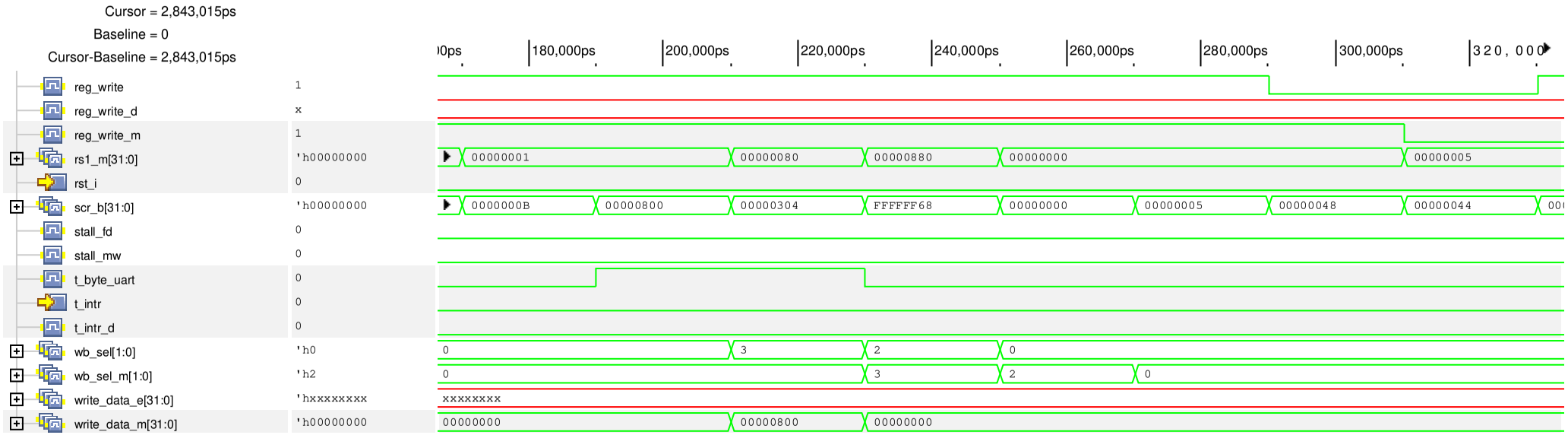

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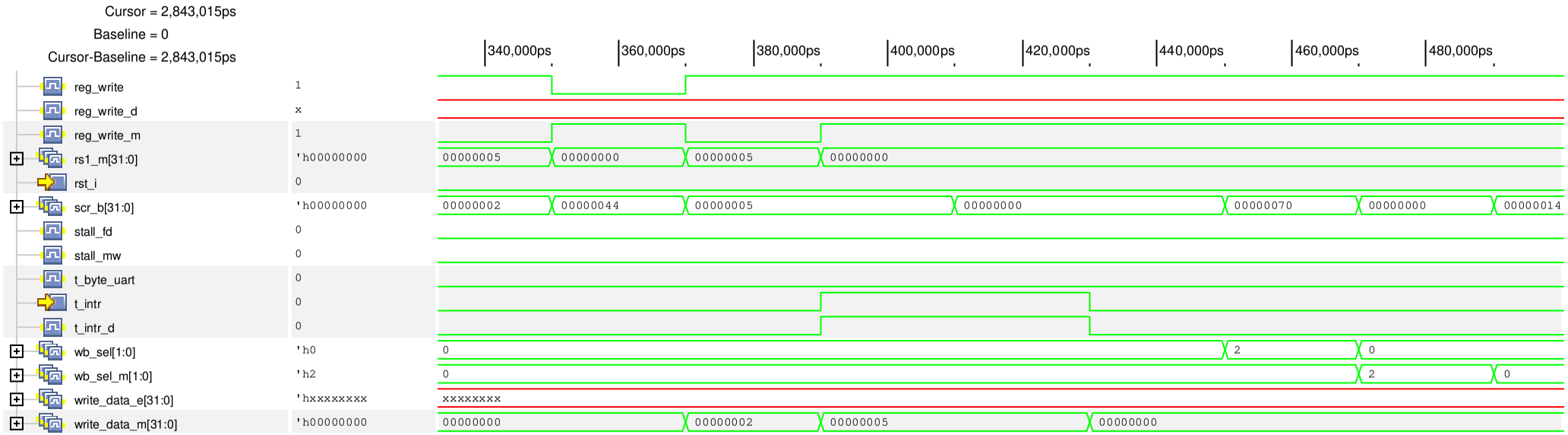

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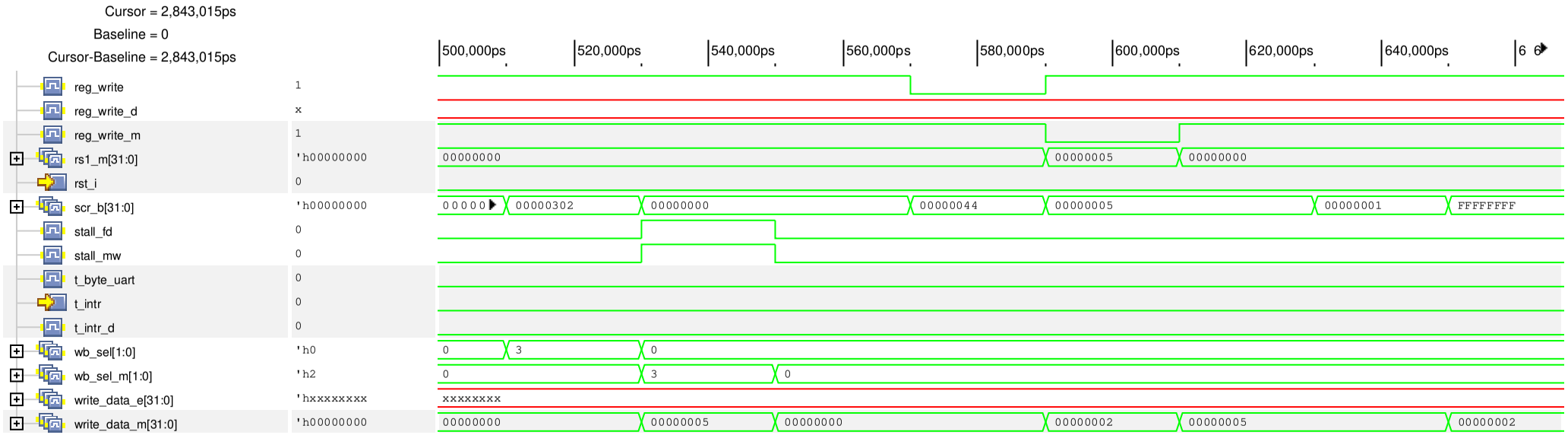

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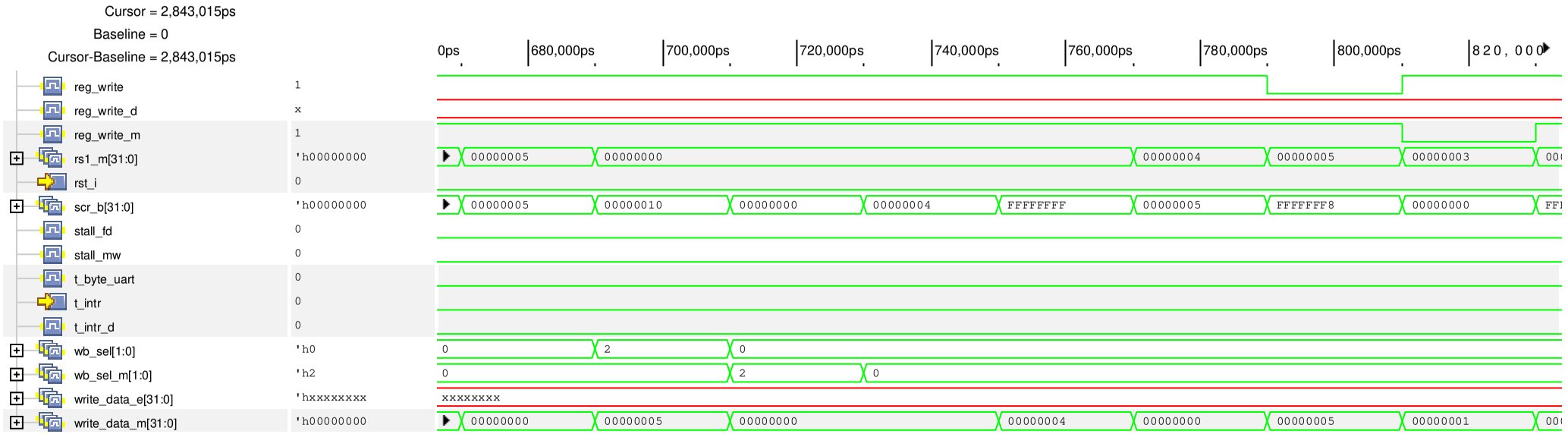

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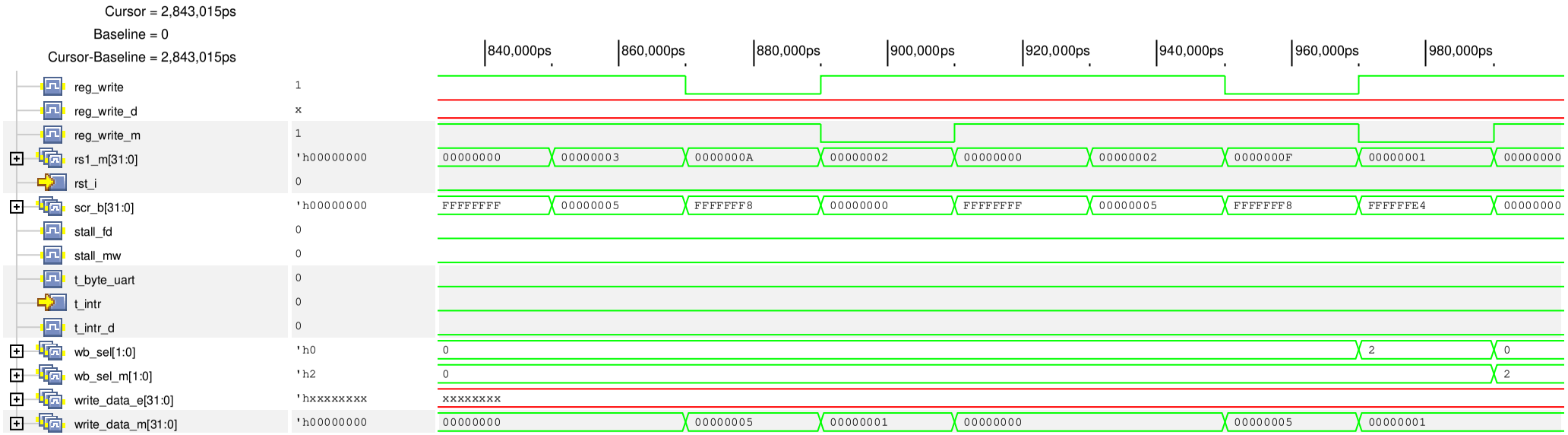

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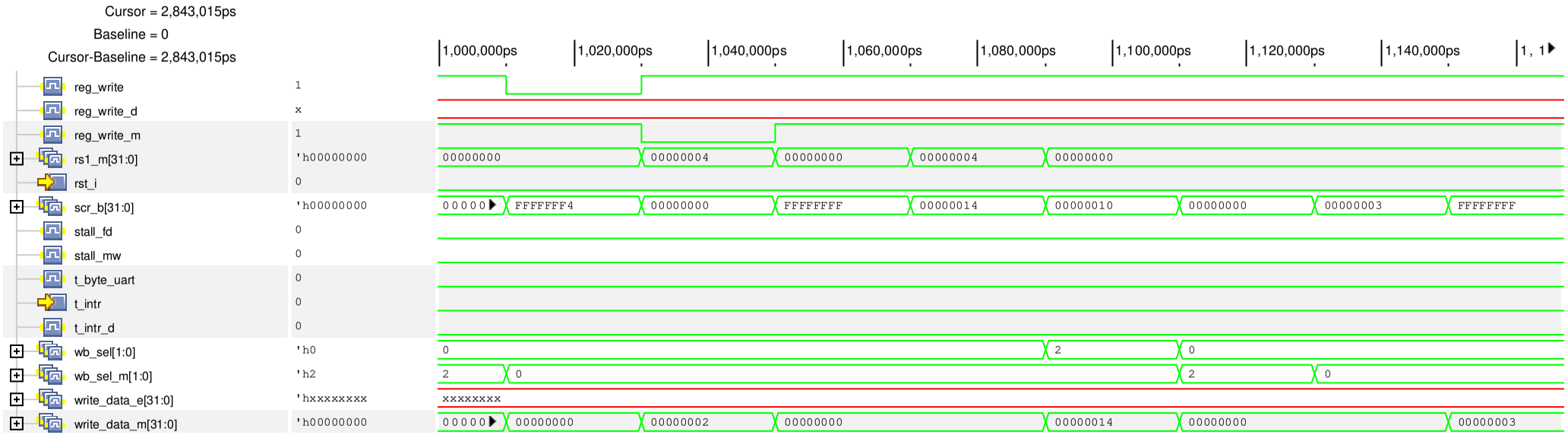

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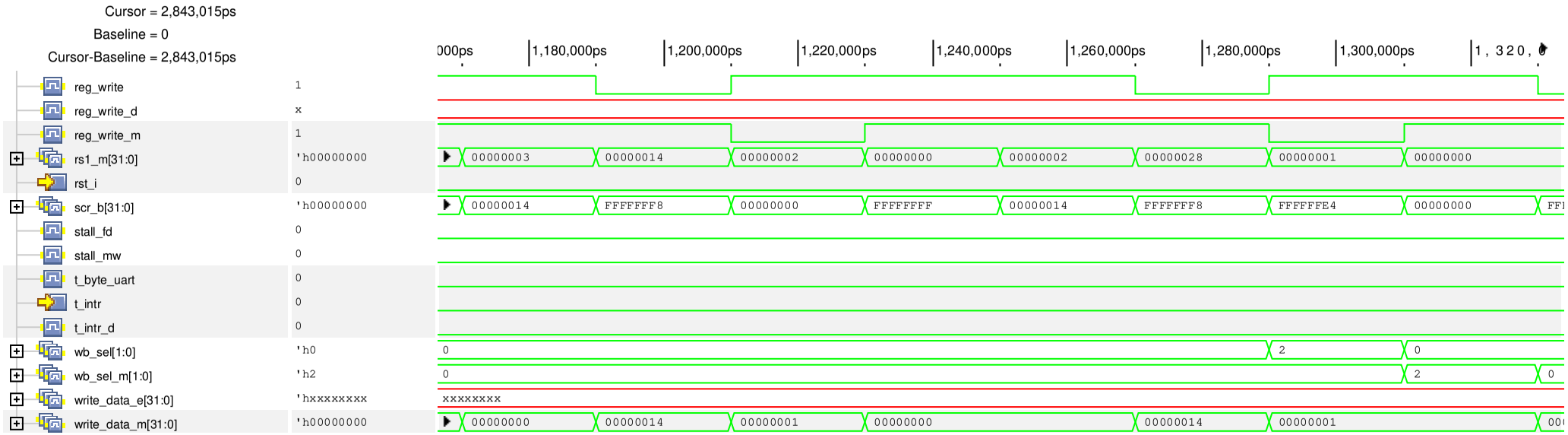

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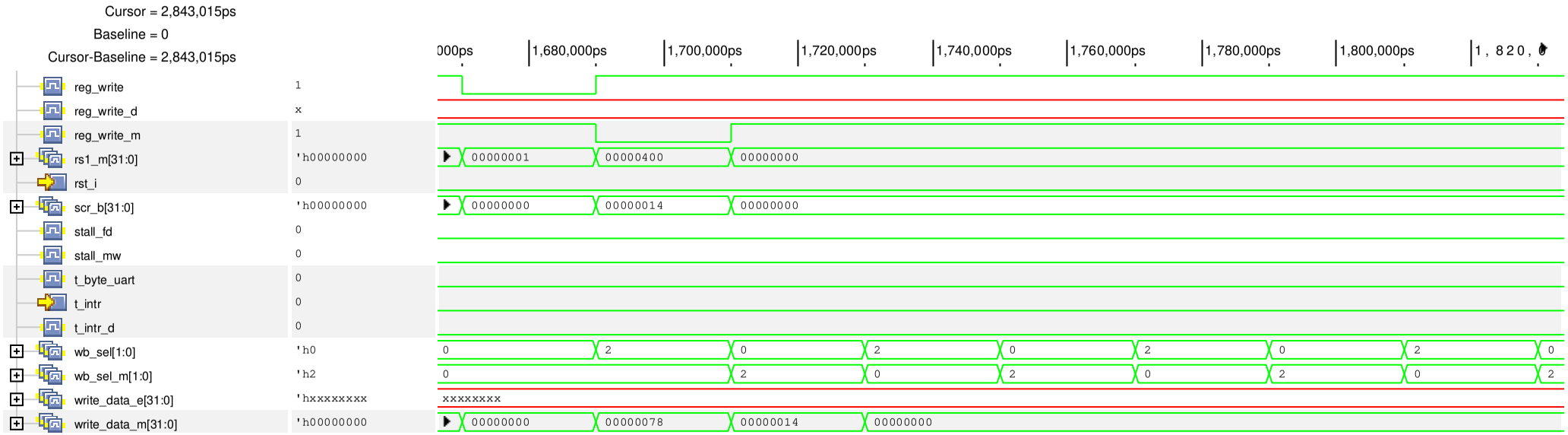

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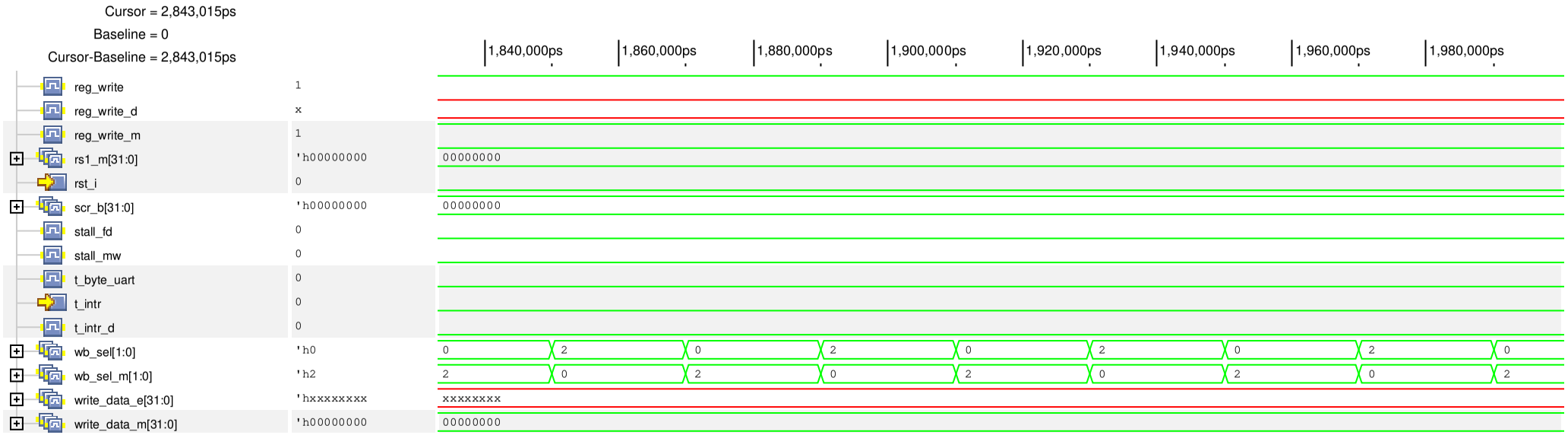

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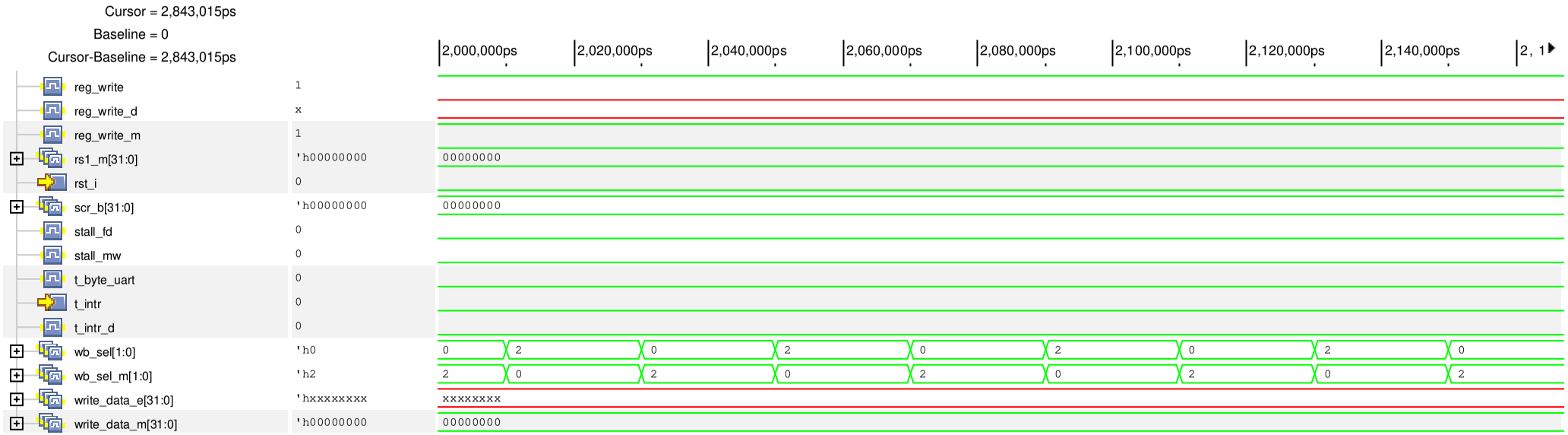

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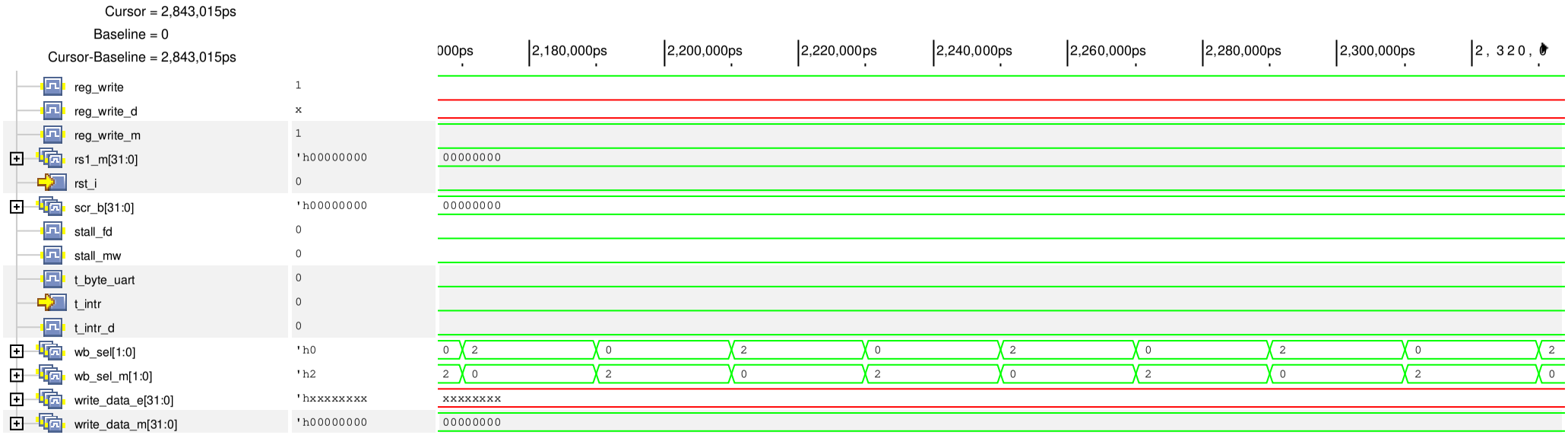

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