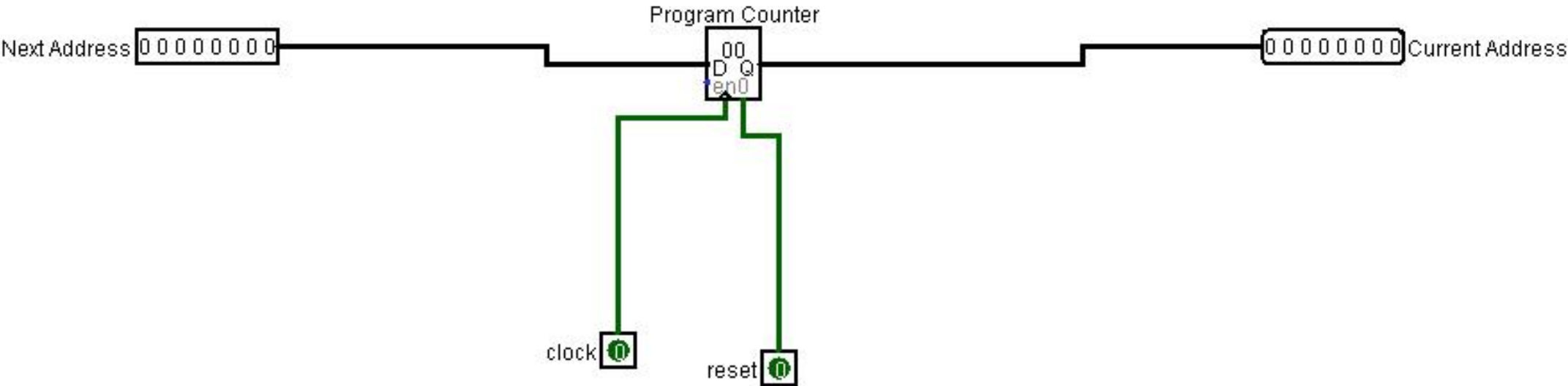
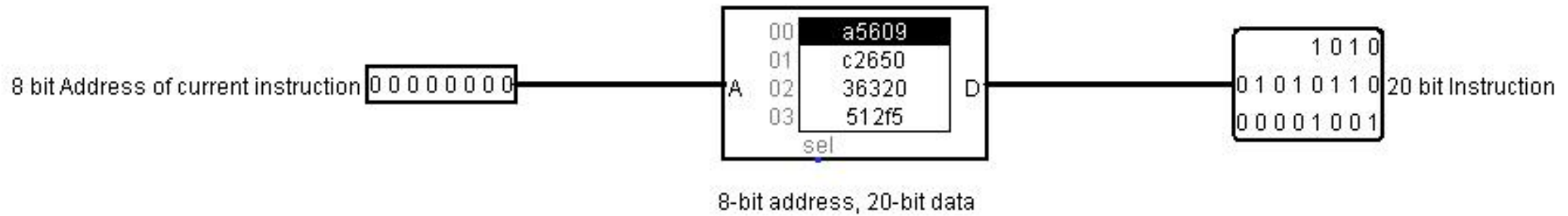


PROGRAM COUNTER

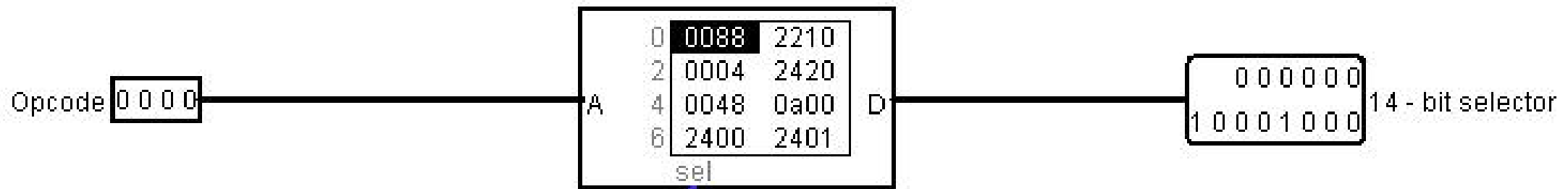


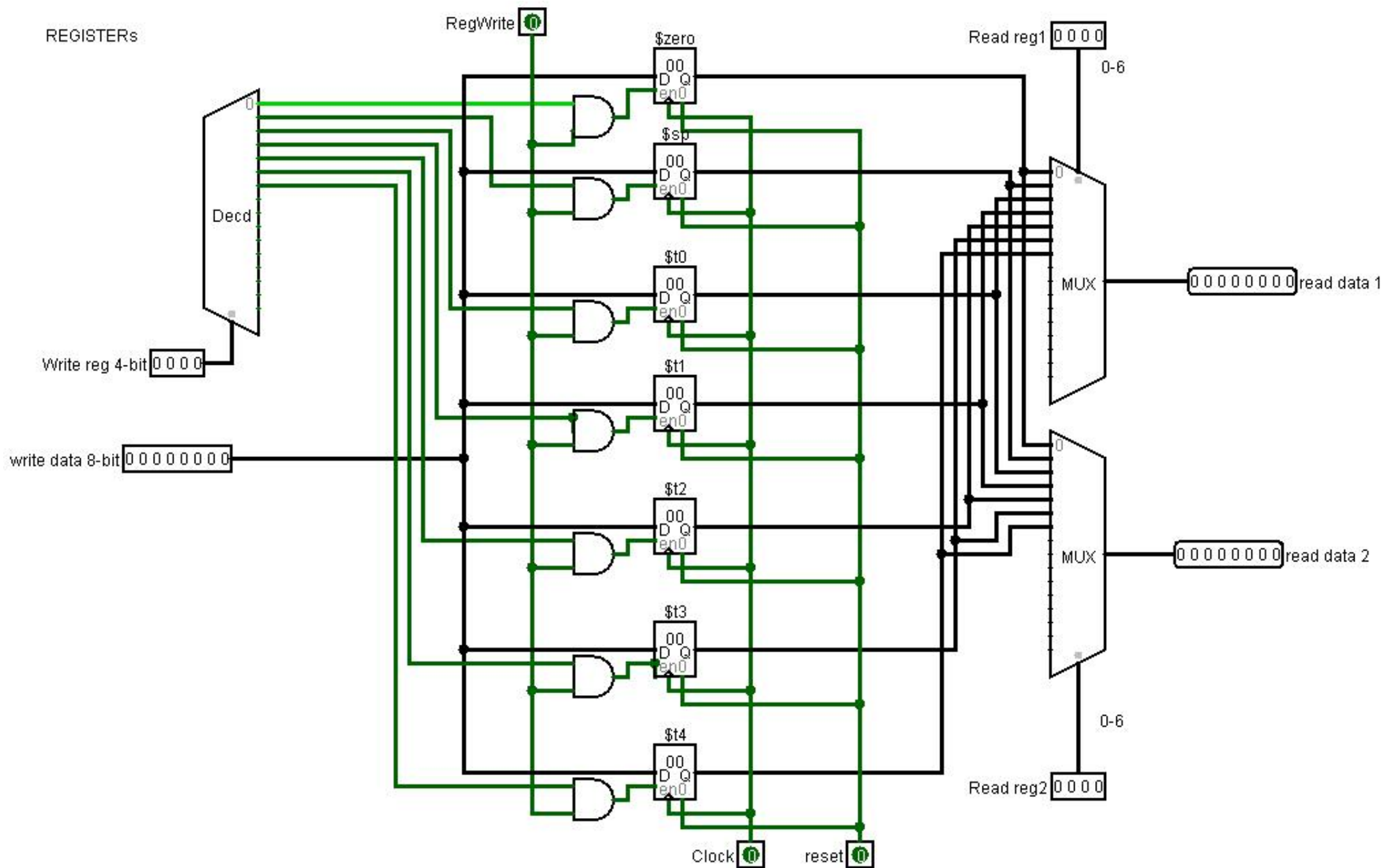
INSTRUCTION MEMORY

Instruction Memory(ROM) loaded from logisim.txt

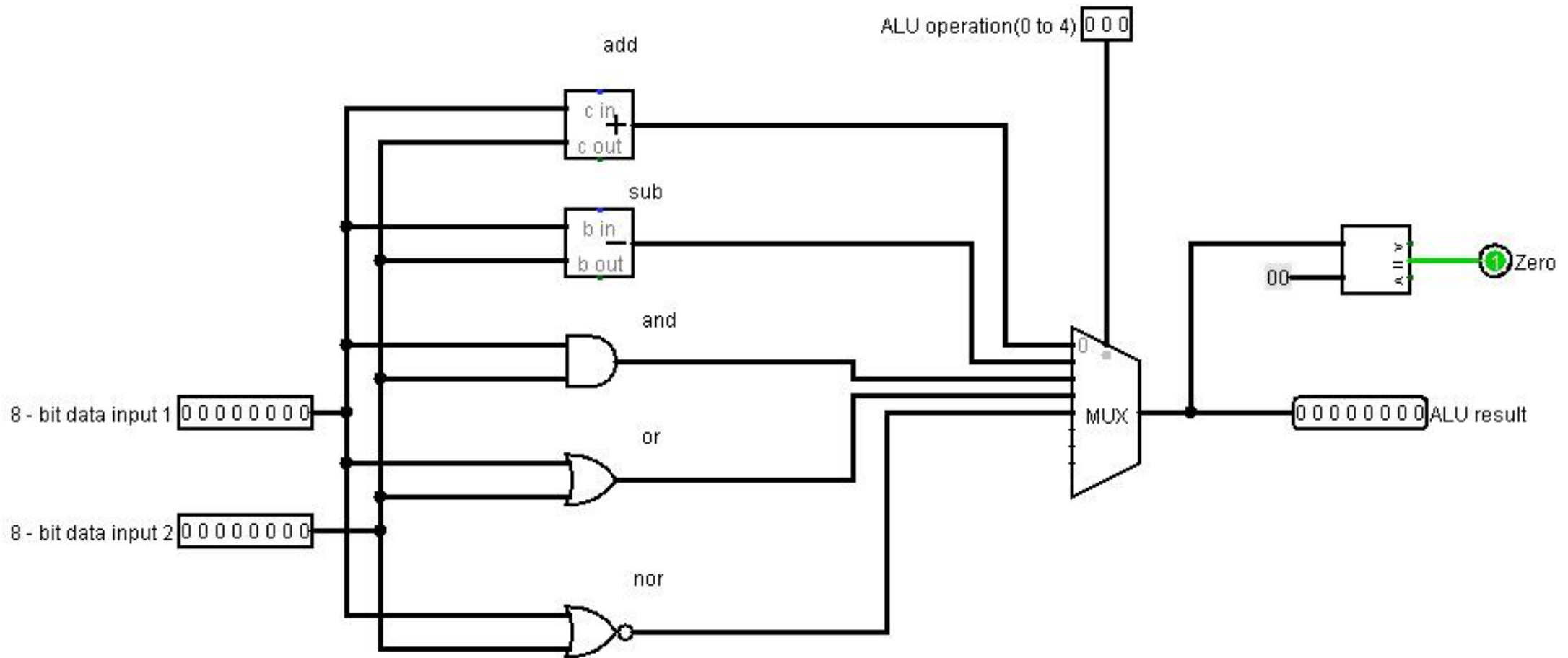


CONTROL

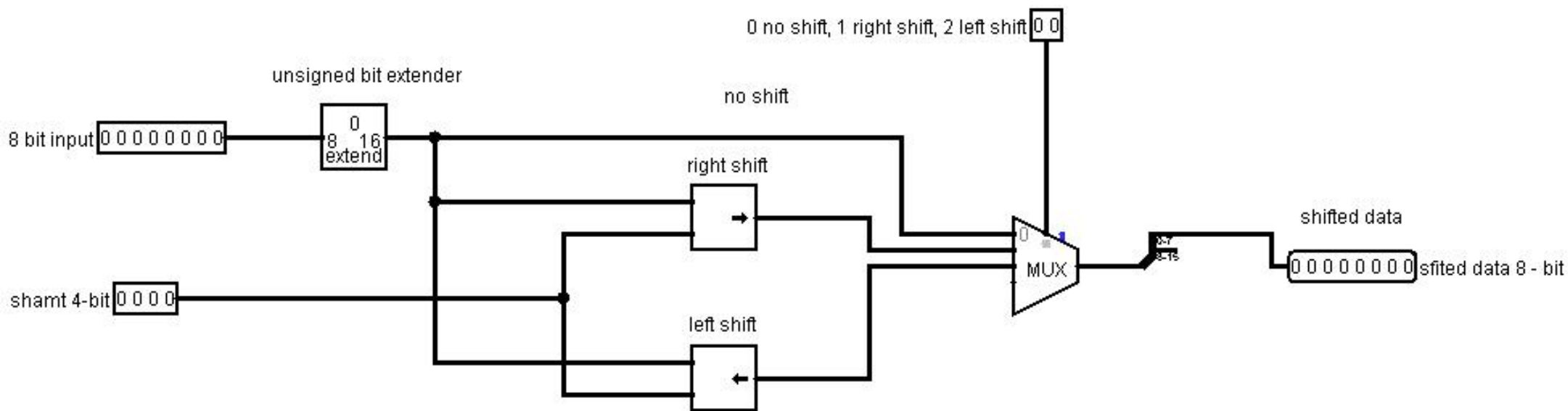




ALU 8-bit

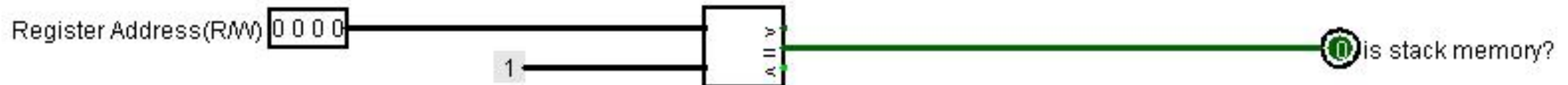


SHIFTER



isSTACK

check if register value is 1 as we defined \$sp at address 0001



DATA & STACK MEMORY

