**Pulse width Calculations**

To generate a PWM at **20ms**, Calculation for ***TPM2->MOD*** is shown below. This is for the Module.

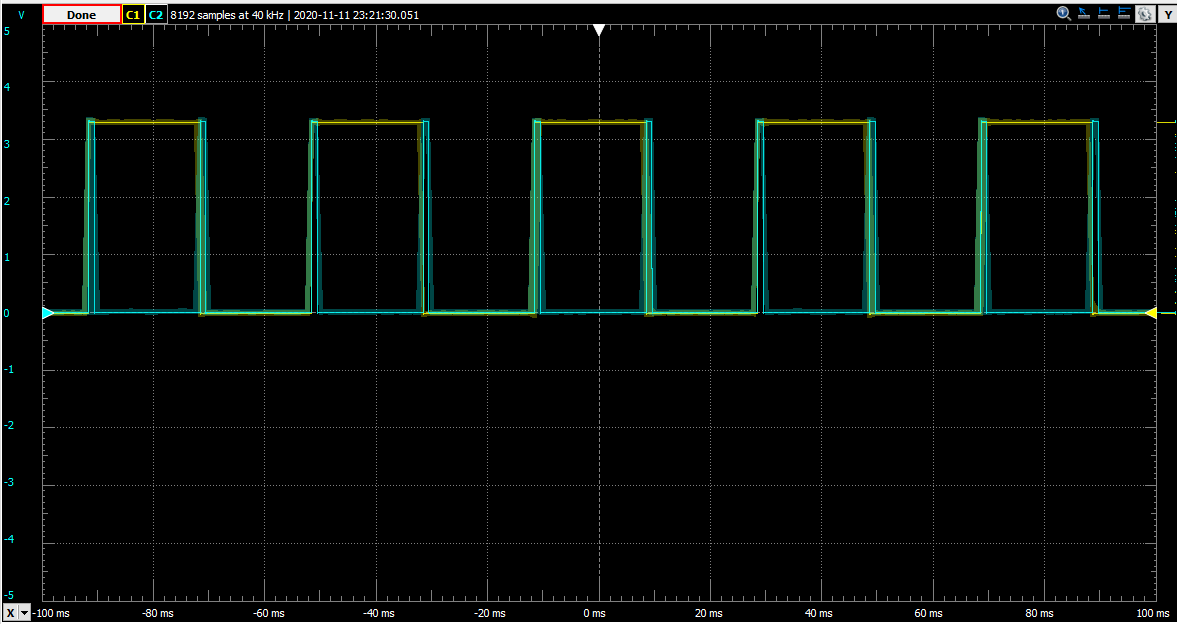
For the channel, calculation for ***TPM2->CONTROLS[0].CnV*** for **1ms** is shown below

For the channel, calculation for ***TPM2->CONTROLS[0].CnV*** for **1.5ms** is shown below

For the channel, calculation for ***TPM2->CONTROLS[0].CnV*** for **2ms** is shown below

**OUTPUTS MEASURED WITH SCOPE**

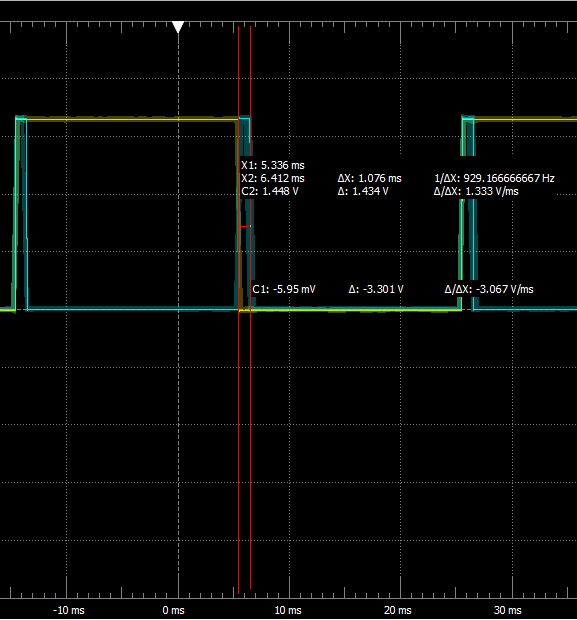
**For pulse width of 1ms:**



***Output for PWM at 20ms with pulse width of 1ms***

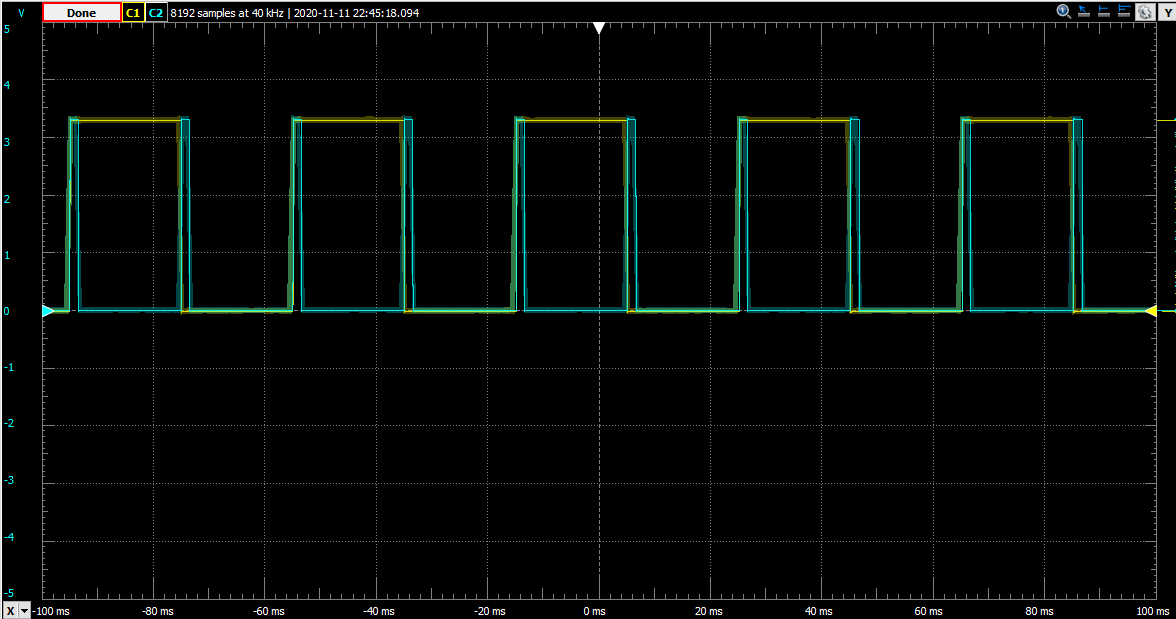


***Proof that PWM is at 20ms***

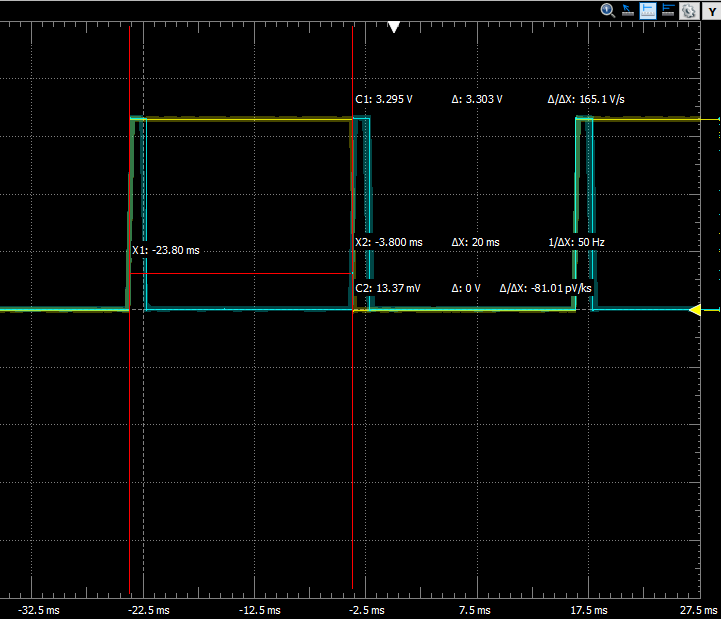


***Proof that Pulse width is 1ms (Δx = 1.076ms)***

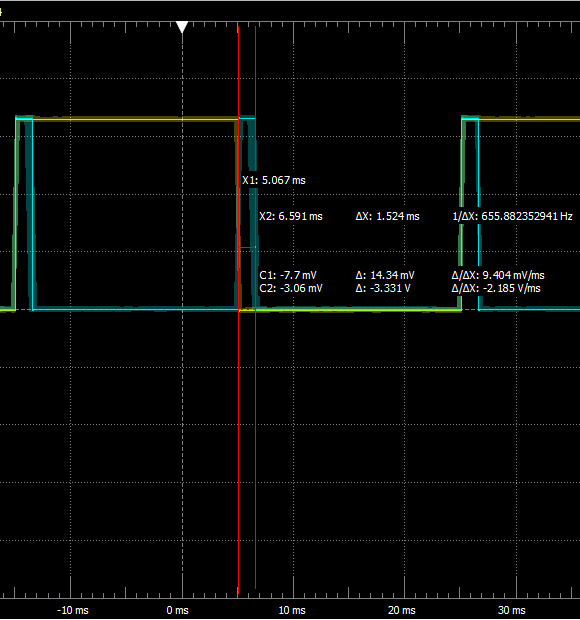
**For pulse width of 1.5ms:**



***Output for PWM at 20ms with pulse width of 1.5ms***

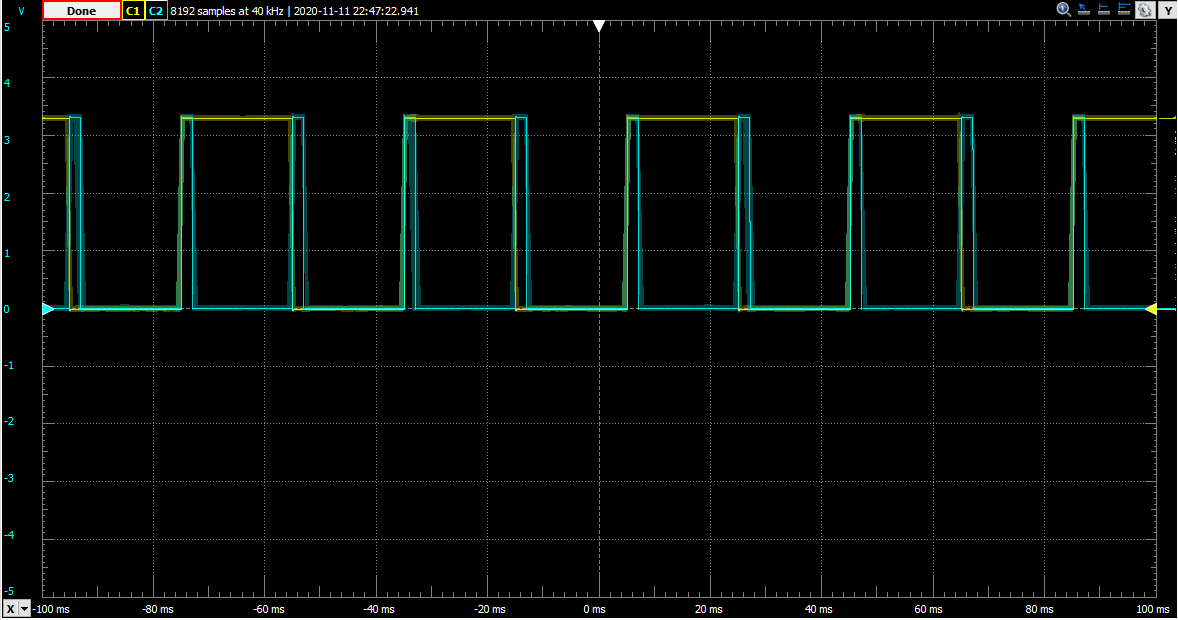


***Proof that PWM is at 20ms***

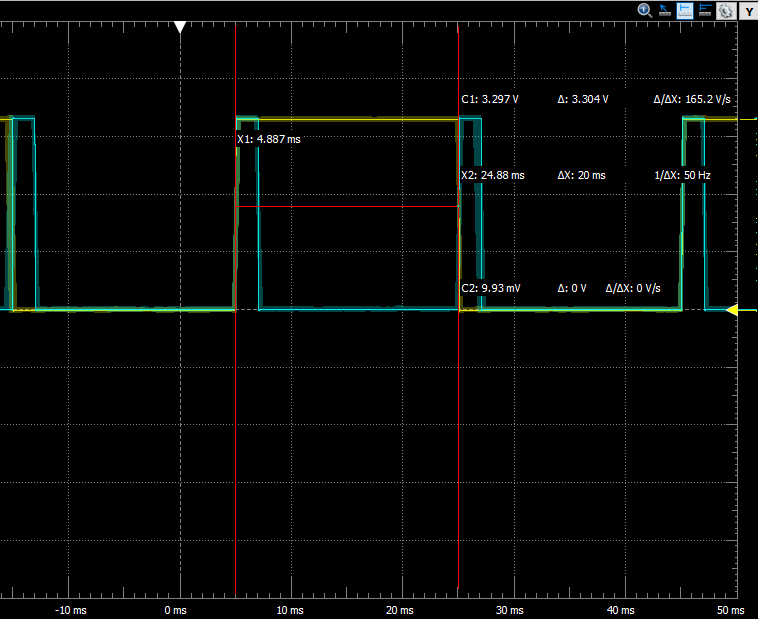


***Proof that Pulse width is 1.5ms (Δx = 1.524ms)***

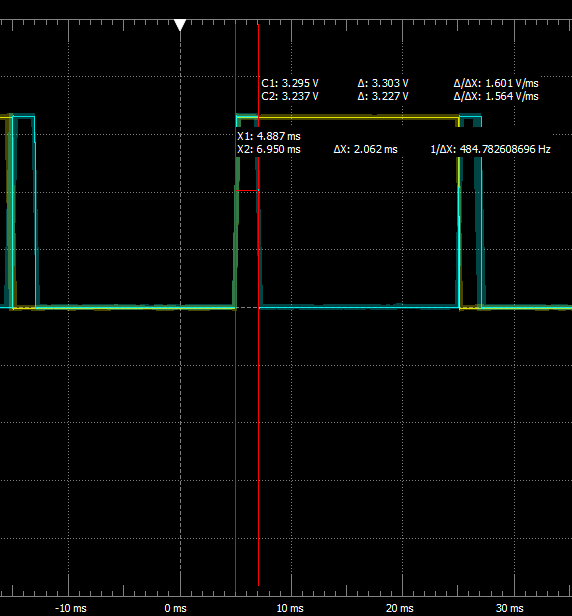
**For pulse width of 2ms:**



***Output for PWM at 20ms with pulse width of 2ms***



***Proof that PWM is at 20ms***



***Proof that Pulse width is 2ms (Δx = 2.062ms)***