1

A

Prove if $L_1 \in P$ then $\bar{L_1} \in P$

Proof

If $L_1 \in P$ then $\exists k$ s.t. $L \in TIME(n^k)$, that is, L_1 is decided by a deterministic machine in polynomial time.

Since $L_1 \in TIME(n^k)$, then the max of $t_m(n)$ of $x \in L_1$ and $t_m(n)$ of $x \notin L_1$ is n^k. This means a TM M decides L_1 in polynomial time.

We can create a 3 tape machine M' that flips the output of M on x by running M on tape 2 and keeping track of the state on tape 3. Once M finishes M' accepts if

As discussed, running M will be $O(n^k)$ time. Reading the final state of M would be $O(n)$ time (where n is length of tape 3). So final runtime is $O(n^k)$, thus $M' \in t_m(n^k)$.

We know M' decides $\bar{L_1}$ because for $x \in \bar{L_1}$, M rejects so M' accepts and for $x \notin \bar{L_1}$, M accepts so M' rejects.

Thus since M' decides $\bar{L_1}$ and $M' \in t_m(n^k)$ then $\bar{L_1} \in TIME(n^k)$ so $\bar{L_1} \in P$

B

Explain why the techinque you used in part a fails to prove $L_1 \in NP \rightarrow \bar{L_1} \in NP$

The proof would need to prove there exists a verifier for $\bar{L_1}$ which is not present in part A. The techinque used in part A determined the runtime of our machine whereas this proof for NP would need to show there is a verifier $V \in P$ for $\bar{L_1}$

C

Prove $(L_1 \in P \wedge L_2 \in P) \rightarrow L_1 \circ L_2 \in P$

Idea

If $L_1 \in P \wedge L_2 \in P$ then $\exists M_1, M_2$ that run in polynomial time that decide L_1, L_2.

Lets create a TM M_3 to decide $L_1 \circ L_2$ and prove that it decides the concatenation in polynomial time.

M_3 will be a 4 tape machine. Tape 1 contains the input string. Tape 2 is working space for M_1/M_2 and Tape 3 will contain the state of the current working machine. Tape 4 contains the length of the working substring (by storing the length as a number of 0's).

M_3 will count the number of 0's o tape 4 and copy that many characters from the start of Tape 1 onto Tape 2. We then simulate M_1 on this string, storing the state on Tape 3. If M_1 accepts, then we clear Tapes 2 and 3 then count the number of 0's on Tape 4 and skip that many characters on Tape 1 then copy remaining string to Tape 2. Then simulate M_2 on Tape 2 and store the state on Tape 3. If M_2 accepts then M_3 will accept. Otherwiseif M_1 or M_2 reject then we add a 0 to Tape 4 and repeat the above steps. If at any point the number of 0's exceed the length of the input string then M_3 rejects.

Proof M_3 decides $L_1 \circ L_2$

If $w \in L_1 \circ L_2$ then $\exists x, y | w = xy \wedge x \in L_1 \wedge y \in L_2$.

> M_3 iterates through w running M_1 on every possible x in w. If M_1 accepts x, then M_3 runs M_2 on the remaining string (y) and will accept if M_2 accepts. Since we know there is an x, y pair that are in their respective languages, we know M_3 will accept on this pair because it tests every possible x, y combination.

If $w \notin L_1 \circ L_2$ then $\nexists x, y | w = xy \wedge x \in L_1 \wedge y \in L_2$.

> M_3 iterates through w running M_1 on every possible x in w. Since there is no x, y pair that satisfy the concatenation, M_1 or M_2 will reject on x, y for each iteration. Eventually once we have exhausted each x, y pair, M_3 will reject once the number of 0's exceed the length of the input, that is M_3 rejects if we try to copy an x where $|x| > |w|$. Thus, M_3 rejects if $w \notin L_1 \circ L_2$.

Therefore M_3 decides $L_1 \circ L_2$.

Proof $t_{M_3}(n) \in O(n^{k+1})$

For both $w \in L_1 \circ L_2 \wedge w \notin L_1 \circ L_2$, M_3 makes (at most) the following steps at each iteration:

- Copy x onto Tape 2 -> $O(n)$
- Run M_1 on x -> $O(n^k)$
- Read M_1 final state -> $O(1)$
- Clear Tape 2 and 3 -> $O(n)$
- Copy y onto Tape 2 -> $O(n)$
- Run M_2 on y -> $O(n^k)$
- Read M_2 final state -> $O(1)$
- Add another 0 to tape 4 -> $O(n)$

As we can see each of these steps are polynomial time and we can find the runtime of a single iteration as $O(n + n^k + 1 + n + n + n^k + 1 + n) = O(n^k)$. We do at most $n + 1$ iterations of this so $t_{M_3}(n) = O((n + 1)n^k) = O(n^k + 1)$ which is polynomial time.

Therefore $L_1 \circ L_2 \in P$ because we created a machine that decides the language in polynomial time.

D

Prove $(L_1 \in NP \wedge L_2 \in NP) \rightarrow L_1 \circ L_2 \in NP$

We can create a machine M that nondeterministically splits our input string w into x, y where $w = xy$ and accepts if x is accepted by a NTM that decides L_1 in polynomial time and y is accepted by a NTM that decides L_2 in polynomial time.

Guess is the split of w such that $w = xy \wedge x \in L_1 \wedge y \in L_2$ and it also includes the certificates for V_1, V_2 so that way they can correctly verify x and y respectively. Our verifier V can take this guess and run the verifiers of L_1, L_2 on x, y. Since $L_1, L_2 \in NP$ their verifiers must be in P, so the total runtime of V would be the runtime of running the verifiers of L_1, L_2 sequentially, which is $O(n^k) + O(n^k) = O(n^k)$. Therefore our verifier V runs in $O(n^k)$ time so $V \in P$.

Therefore since $\exists V \in P, L_1 \circ L_2 \in NP$.

2

A

Prove that $SUBGRAPHISOMORPHISM \in NP$ (show $\exists V \in P$)

Our machine M that decides $SUBGRAPHISOMORPHISM$ nondeterministically creates a function f that maps each vertex in H to a vertex in G (no overlap). M accepts if each vertex in H is able to match to a unique vertex in G and each edge in H matches an edge found in G.

Guess is the function that maps each vertex in H to a unique vertex in G. V will check to make sure the mapped verticies exist in G and that each vertex in H maps to a unique vertex in G. This process takes is $O(n^2)$ because for each vertex v we check if $f(v) \in GV$ and that $f(v)$ is unique. This is accomplished by adding a tape that stores each output of f and makes sure that for each new output it is not already on our tape.

Verifying edges is a similar process. We map an edge in H to an edge in G by appling the f to the verticies of the edge. That is given an edge e with verticies u, v we look for an edge in G such that $f(u), f(v)$ are the verticies of an edge in G. Similarly we store this output on a tape and ensure the outputed edge is unique. Doing both of these for each edge is $O(n)$ so total runtime for edges is $O(n^2$.

Therefore the total runtime of V is $O(n^2) + O(n^2) = O(n^2)$ so $V \in P$.

B

Prove $HAMILTONIAN \leq_p SUBGRAPHISOMORPHISM$

Idea

Our machine creates a function f which maps our input $G \rightarrow G, H$ and inputs this into $SUBGRAPHISOMORPHISM$ and accepts/rejects accordingly. H is a graph such that $|GV| = |HV|$ and the edges are $(v_1, v_2), (v_2, v_3), ...(v_{|HV|-1}, v_{|HV|}), (v_{|HV|}, v_1)$. This means H is a graph representation of an arbitrary hamiltonian circuit for a graph with \$|GV| verticies. If $SUBGRAPHISOMORPHISM$ accepts, then this means that there is a one to one function that maps each vertex in H to a vertex in G and all edges in H exist in G. In other words, there is a subgraph of G that is a hamiltonian circuit. Therefore if ISO acceptts, HAM accepts, otherwise reject.

Proof:

If $G \in HAM \rightarrow f(G) \in ISO$

> If $G \in HAM$ then there is a hamiltonian circuit for G. This means that there is a subgraph that is a hamiltonian circuit of G (remove any edges not used in the circuit). This results in a subgraph of G with $|GV|$ verticies and $|GV|$ edges that connect these verticies in a perfect loop. Those are the same specifications of our graph H, so ISO will map verticies in H such that they correspond to verticies in G to create a hamiltonian circuit. Thus $f(G) \in ISO$ because H can be mapped to a valid subgraph of G.

If $G \notin HAM \rightarrow f(G) \notin ISO$

> If $G \notin HAM$ then there is not a hamiltonian circuit for G. This means that there is not a subgraph that is a hamiltonian circuit of G (remove any edges not used in the circuit). So ISO cannot map verticies in H to verticies in G to create valid circuit. Thus $f(G) \notin ISO$ because H cannot be mapped to a valid subgraph of G.

If $f(G) \in ISO \rightarrow f(G) \in HAM$

> If $f(G) \in ISO$ then we can map a hamiltonian circuit (H) onto a subgraph of G. This subgraph of G has the same number of verticies but less edges. Since H is a valid circuit of G, then G has a hamiltonian circuit, therefore $G \in HAM$.

If $f(G) \notin ISO \rightarrow f(G) \notin HAM$

> If $f(G) \notin ISO$ then we cannot map a hamiltonian circuit (H) onto a subgraph of G. This subgraph of G has the same number of verticies but less edges. Since there is no valid mapping of H onto G, there is no valid hamiltonian circuit in G. Then G has no valid hamiltonian circuit, therefore $G \notin HAM$.

Therefore $G \in HAM \leftrightarrow f(G) \in ISO$. Since there is a function f that maps HAM to ISO then $HAM \leq_p ISO$.

3

Prove MAJORITY-SAT is NP-complete

Prove $MAJORITY - SAT \in NP$

Our guess would simply be the assignments of the variables (ie true or false). We can verify if this assignment is true by simply counting the number of true literals in a given clause and ensuring that the number is greater than half the number of literals in a clause. We do this by having a count tape to count every literal in a clause and for each true literal cross of 2 0's in the count. If we have all crossed off 0's then that clause is true. If any clause does not have all crossed 0's then we reject. Otherwise accept.

This is $O(n)$ on our 2 tape machine because counting number of literals in a clause is $O(n)$, verifying there is a majority true is $O(n)$. Therefore $V \in P$ so $MAJORITY - SAT \in NP$

Prove $3 - SAT \leq_p MAJORITY - SAT$

We can map each clause in $3-SAT$ which consists of literals a, b, c to a new clause in MAJORITY-SAT which consists of literals a, b, c, x, y, where x, y are new literals not in our $3 - SAT$ problem.

Since x, y are new literals, they can be T/F unlike a, b, c which are T iff they are T in $3 - SAT$.

Proof:

If $F \in 3 - SAT$ then $f(F) \in MAJORITY - SAT$

> If $F \in 3 - SAT$ then $\exists$ assignments where each clause is true.
>
> This means for all clauses in F, at least one literal is true.
>
> Then our CNF for $MAJORITY - SAT$ ($f(F)$) has at least one literal from all clauses in F true.
>
> This means $f(F) \in MAJORITY - SAT$ because all clauses in F have at least one true literal, so each clause in $MAJORITY - SAT$ have at least one true literal besides x, y. If x, y are true then that clause will have majority true because we have one true literal from F and x, y are true.
>
> Therefore, if $F \in 3 - SAT$ then $f(F) \in MAJORITY - SAT$

If $F \notin 3 - SAT$ then $f(F) \notin MAJORITY - SAT$

If $F \notin 3-SAT$ then there exists at least one clause in F that is false. That means this clause has all false for the literals.

For $f(F)$ that means that this clause will have 3 false literals from F and x, y which can be true or false. Even if both x, y are true, we will not have a majority true for this clause. Meaning $f(F) \notin MAJORITY-SAT$

Therefore if $F \notin 3-SAT$ then $f(F) \notin MAJORITY-SAT$

If $f(F) \in MAJORITY-SAT$ then $F \in 3-SAT$

If $f(F) \in MAJORITY-SAT$ then the majority of literals in each clause is true. Since each clause contains two literals that are always true, one of the remaining 3 literals must also be true. The remaining 3 literals in a given clause represent the literals of a clause in F, since one of these literals must be true for each clause, this is the assignment that would satisfy $F \in 3-SAT$. Thus, if $f(F) \in MAJORITY-SAT$ then $F \in 3-SAT$.

If $f(F) \notin MAJORITY-SAT$ then $F \notin 3-SAT$

If $f(F) \notin MAJORITY-SAT$ then there is one clause where the majority of literals are false. Since each clause contains two literals that are always true, the remaining 3 literals must be false. The remaining 3 literals in a given clause represent the literals of a clause in F, since all of these literals are false, this is the assignment would not satisfy $3-SAT$. Thus, if $f(F) \notin MAJORITY-SAT$ then $F \notin 3-SAT$.

As such, our clause in $3-SAT$ is true iff our clause in $MAJORITY-SAT$ is true. So $3-SAT \leq_p MAJORITY-SAT$.

4

Prove the 0-1 integer programming problem is NP-complete

Prove $INT \in NP$

Our certificate for this problem is the 0,1 assignments for the variables. We can create a verifier V that checks if the assignment results in each inequality being true. For each inequality, we check if the present variables are 0's or 1's then add up the left side accordingly. Then we check if the left side is less than or equal to the right side. If it is we move onto the next inequality, else reject. Thus if all inequalities hold true then we accept.

The runtime for this is $O(n^2)$ because adding the left side of the equation is $O(n)$ then seeing which side is greater is also $O(n)$ and we need to do this for m clauses. So this is $O(nm) = O(n^2)$.

Since our verifier $V \in P, INT \in NP$.

Prove $SAT \leq_p INT$

For our function, each literal in SAT matches to one x variable in INT and each clause in SAT matches to one linear inequality in INT.

Our function f makes a set of linear equations mapping each variable in F to the variabls in our system of linear inequalities. Then all $b_1 ... b_m$ are equal to -1. For coeffecients, $a_{i,j} = -1$ if l_j is a literal in the ith clause, and $a_{i,j} = 0$ otherwise. For each negation of a literal l_j in a clause (i), we increase b_i by 1 and flip the sign of $a_{i,j}$ (make $a_{i,j} = 1$)

Proof:

If $F \in SAT$ then $f(F) \in INT$

> If $F \in SAT$ then there is an assignment to the literals where each clause is true.
>
> If each clause is true, then at least one literal is true. This carries over to our system of inequalities $f(F)$. If all literals in a clause are simply the variable, this means for our inequality to be true at least one x_i is equal to 1, so the sum is at most $-1 * 1 \leq -1$ so this inequality is true. If the clause contains negations of a variable, then the inequality will still hold because we incremented b_j by 1 so that way the inequality accepts when that variable is 0 (if the negated variable is true then the inequality requires one other non negated variable to be 1 in order for the inequality to hold, this way we do not accept when we should not). Thus if $F \in SAT$ the same assignment that makes F true can be used to make $f(F) \in INT$ true.

If $F \notin SAT$ then $f(F) \notin INT$

> If $F \notin SAT$ then there is no assignment to the literals where each clause is true. This means at least one clause is false in F, that is all literals are false for this clause. In $f(F)$, there must also exist an inequality that is false. If there are no negations of variables in our clause c, then the inequality would contain all $x_i = 0$ so $0 + 0 + ... + 0 \leq -1$. For each

negation, b_c is incremented by 1, so if there are y negations we get the inequality $y + 0 + 0 + ... + 0 \leq y - 1$ which is false. Thus if $F \notin SAT$ then $f(F) \notin INT$

If $f(F) \in INT$ then $F \in SAT$

If $f(F) \in INT$ then there exists an assignment to the variables where each inequality is true. That means each inequality has one literal that is true, either a positive coeffecient has an $x_i = 0$ or a negative coeffecient has an $x_i = 1$. This means our F has an assignment (the same one as $f(F)$) where at least one literal is true in each clause. This means that $F \in SAT$

If $f(F) \notin INT$ then $F \notin SAT$

If $f(F) \notin INT$ then there exists an inequality that is false. That means this inequality has all literals that are false, either a positive coeffecient has an $x_i = 1$ or a negative coeffecient has an $x_i = 0$. This results in the left side being one greater than our b. This means our F has a clause (the same one as $f(F)$) where all literals are false. This means that $F \notin SAT$

Therefore, $F \in SAT \leftrightarrow f(F) \in INT$ so $SAT \leq_p INT$.