

Aiden Racelis

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EDUCATION

Intended Computer Science (B.S.) w/ Cybersecurity Minor College of Engineering, Virginia Tech, Blacksburg, VA	Expected in 12/26 GPA: 3.41/4
High School Diploma Governor Livingston High School, Berkeley Heights, NJ	GPA: 4.4/4.5
Career Objective First-generation undergraduate freshman, eager to secure an internship/ design team to apply strong organizational, problem-solving, and leadership abilities, contributing to future research and development at a company in computer science.	

TECHNICAL SKILLS

- **Programming Languages:** Java, Python, MATLAB
- **Software:** Figma(Beginner level)
- **Editing Tools:** Adobe Photoshop, Adobe Illustrator

LEADERSHIP

Virginia Tech Student Engineers' Council (SEC) E-Week Committee Member	April 2024-Present Blacksburg, VA
<ul style="list-style-type: none">• Responsible for improving the impact of E-Week for fellow Hokies and inclusion to the Virginia Tech Student Body.• Under mentoring of planning events and connecting with high-profile professionals.	
Party Maniacs Party Host/Runner	June 2020-August 2023 Springfield, NJ
<ul style="list-style-type: none">• Organized over 100 different events involving 20+ children and customers• Motivated and led multiple assistants over the years in running birthday parties, leading many to be promoted• Honored as a top employee of the company	

PROJECTS

HackVT Hackathon Virginia Tech, Blacksburg, VA	June 2024-Present
<ul style="list-style-type: none">• Working alongside a scrum team of 4 seamlessly to develop a Hiking App.• Learning how to use Figma to create schemas and graphs, following structures of SQL.• Learning and using RESTful API	

AWARDS AND HONORS

- Dean's List.....Spring 2024
- United States House of Representatives Congressional Nomination.....December 2022
- American Computer Science League Top 25 Teams Internationally..... June 2022