Time Report Project Week 3
Group 2

Finnished tasks during the week:

- Design specification version 1.0

- Project plan version 1.0

Ongoing tasks:

- Design specification version 2.0

- Project plan 2.0

Planned tasks for the coming week:

- System design presentation

- Design specification version 2.0

- Project plan version 2.0

Individual worked hours:

Student	Worked last week	Worked so far	Tasks done this week
Kebba Jeng	5	10	Address suggestions from supervisor (new block diagram). Preparation for presentation.
Hannes Fröberg	5	10	Decode block, settings and further work on design spec going top down.
Ghady Al Haddad	5	10	Document format, and further work on Decode block, settings design.
Martin Bildhjerd	5	10	General info for the signal generator block. Some ports have been added, although it should be noted that there still are ports that need to be placed.
Aidin Jamshidi	5	10	Worked on VGA block, UI block in design specification. Further work on GitLab project. Some pre-work for presentation.

How do this follow the time plan (in the project plan document)?

Everything is progressing according to schedule.