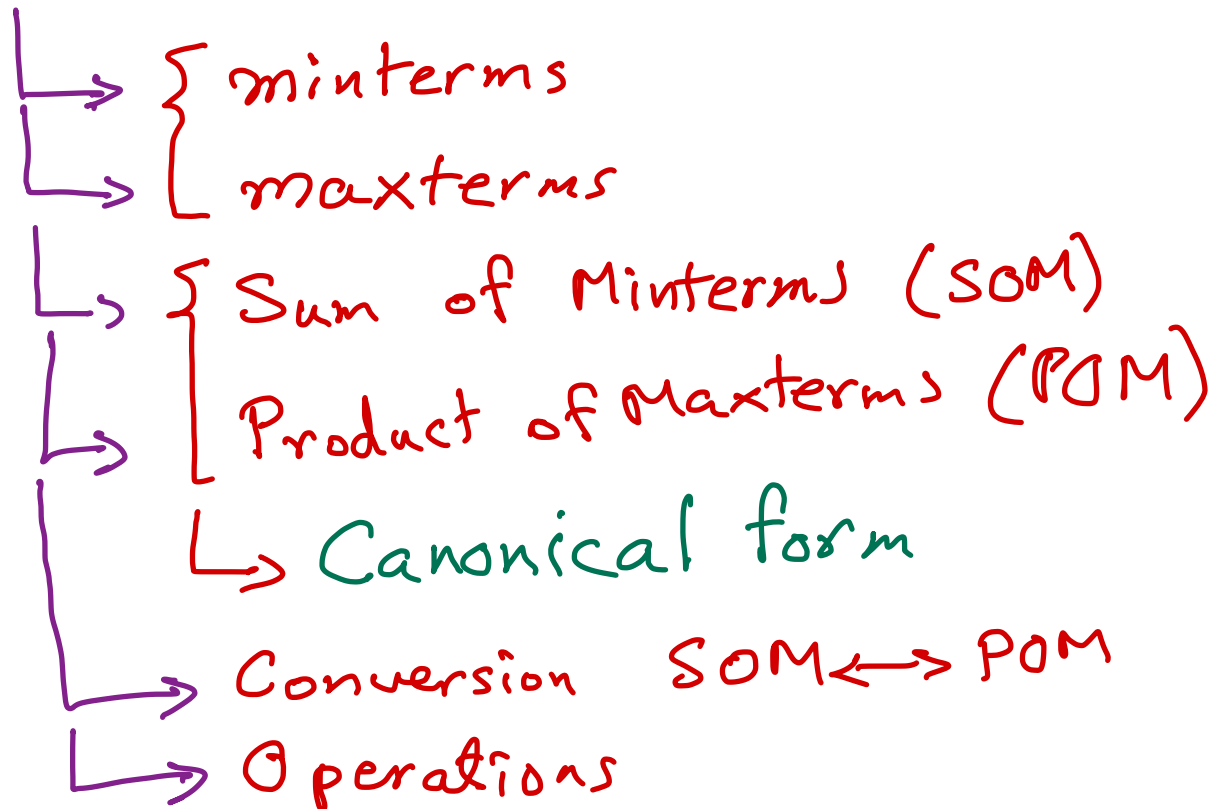


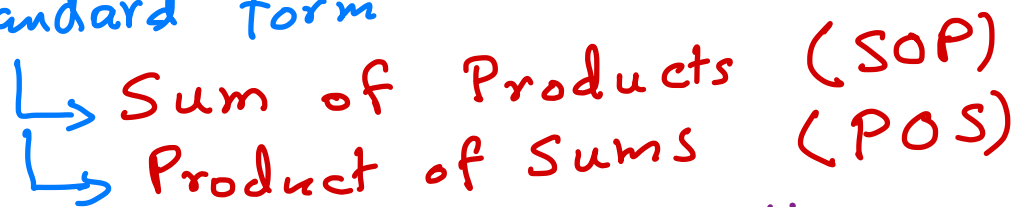
# Standard & Canonical Forms

In this lesson, we learn

\* Canonical form



\* Standard form



\* Two-Level Implementation

\* Propagation Delay & Critical path

# Minterms

\* Consider: set of functions of 3 vars. (A, B, C)

\* Minterm: Product term s.t. all literals

show up the product in true or complement form.

Ex 1

ABC is a minterm

$A\bar{B}\bar{C}$  is a minterm

AB is NOT a minterm  
{Missing C}

\* Minterm Numbering:

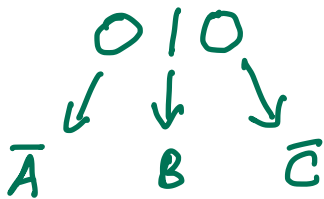
Question: How many minterms can we have in 3 variable system?

$m_i$  is minterm #  $i$

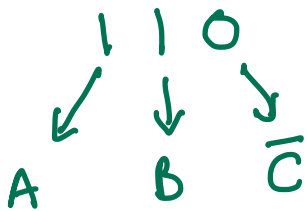
$\Rightarrow$  Binary representation of  $i$   
tells us the algebraic form  
of  $m_i$

Ex 2

$$m_{\textcircled{2}} = \bar{A} B \bar{C}$$



$$m_{\textcircled{6}} = A B \bar{C}$$



Assuming functions of (A, B, C)

MSB of index  
↓  
LSB of index

**Note:** minterms are also Boolean functions.

Ex 3 Derive the truth table of  $m_3$ ,  $m_5$ , and  $m_7$ , assuming functions of  $(A, B, C)$ .

011  
ABC  
101  
ABC

110  
ABC

A	B	C	$m_3$	$m_5$	$m_6$
0	0	0	0	0	0
0	0	1	0	0	0
0	1	0	0	0	0
0	1	1	1	0	0
1	0	0	0	0	0
1	0	1	0	1	0
1	1	0	0	0	1
1	1	1	0	0	0

**Observation?**

\* A minterm will be 1 only once in the truth table (TT)

\*  $m_i$  will be 1 at Row  $i$  of TT and 0 otherwise

## Sum of Minterms

\* All Boolean functions can be written in sum-of-minterms (SOM) form

Ex 4

X	Y	F	$m_1$	$m_2$
0	0	0	0	0
0	1	1	1	0
1	0	1	0	1
1	1	0	0	0

Diagram illustrating the mapping of minterms  $m_1$  and  $m_2$  from the truth table rows where  $F=1$  to their respective minterm columns. Green arrows show the mapping from the first and third rows of the truth table to the corresponding minterm columns.

$$\Rightarrow F(x, y) = m_1 + m_2 = \overline{x}y + x\overline{y}$$

Diagram showing the expansion of the minterms into their algebraic forms:  $m_1 = \overline{x}y$  and  $m_2 = x\overline{y}$ .

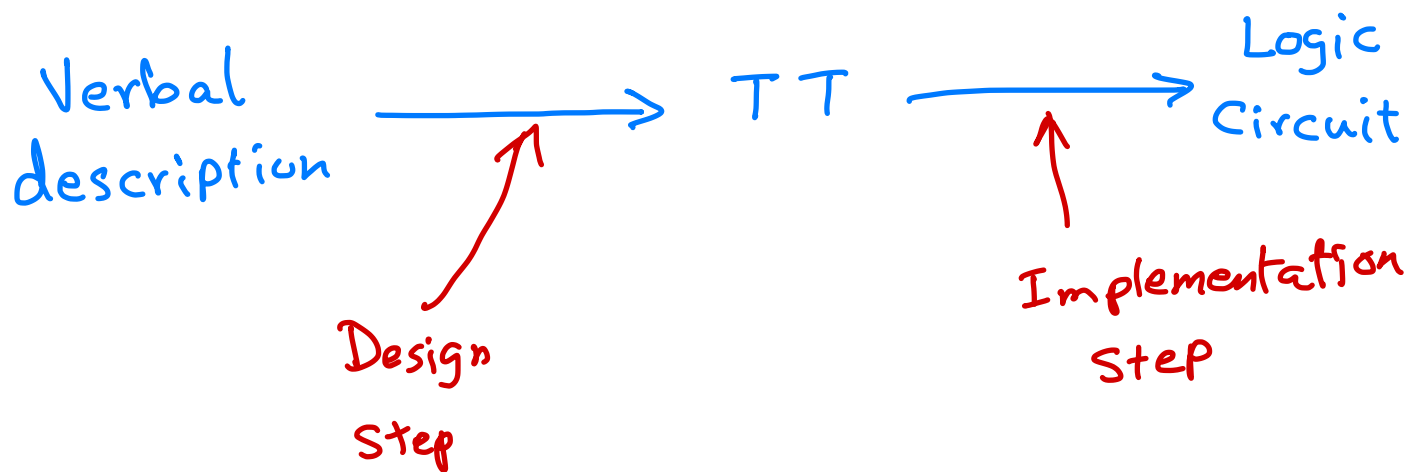
Notation

$$\rightarrow F(x, y) = \sum m(1, 2)$$

Algebraic  
SOM form

\* What did we just do?

we went from TT to  
gate implementation



Ex 5 Express  $G(a,b,c)$  given in TT below  
in SOM canonical form using  $\Sigma$  notation

a	b	c	G	
0	0	0	1	$m_0$
0	0	1	0	
0	1	0	1	$m_2$
0	1	1	0	
1	0	0	0	
1	0	1	1	$m_5$
1	1	0	0	
1	1	1	0	

$$\Rightarrow G(a,b,c) = \sum m(0,2,5)$$

if the question says express  $G$  algebraically  
in SOM form

$$G(a,b,c) = \bar{a}\bar{b}\bar{c} + \bar{a}b\bar{c} + a\bar{b}c$$

# Maxterms

\* Consider: set of functions of  $\geq 3$  vars.  $(A, B, C)$

\* Maxterm: Sum term s.t. all literals

Show up the sum is true or complement form.

Ex 6

$A + B + \bar{C} \rightarrow \text{maxterm}$

$\bar{A} + \bar{B} + \bar{C} \rightarrow \text{maxterm}$

$A + C \rightarrow \text{Not maxterm (B missing)}$

\* Similarly, we number Maxterms

\* Maxterm  $i$ , denoted  $M_i$

\*  $M_i = 0$  at row  $i$  of truth table

otherwise 1 (complement of  $m_i$ )

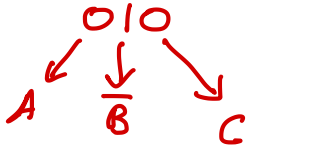
\*  $M_i = \overline{m_i}$

Ex 7  $M_3 = \overline{m_3} = (\bar{A} B C)' = A + \bar{B} + \bar{C}$

\* We can also compute  $M_i$  directly from  $i$

Ex 8

$$M_2 = A + \bar{B} + C$$



\* Functions can be represented in Product-of-Maxterms (POM) canonical form

Ex 9

A	B	C	F	$M_1$	$M_4$	$M_7$
0	0	0	1	1	1	1
0	0	1	0	0	1	1
0	1	0	1	1	1	1
0	1	1	1	1	1	1
1	0	0	0	1	0	1
1	0	1	1	1	1	1
1	1	0	1	1	1	1
1	1	1	0	1	1	0

$$F(A, B, C) = M_1 \cdot M_4 \cdot M_7 = \prod M(1, 4, 7)$$

\* Algebraically

$$F(A, B, C) = (A + B + \bar{C}) (\bar{A} + B + C) (\bar{A} + \bar{B} + \bar{C})$$



\* Use DeMorgan's law to convert between the two canonical forms.

Ex 10

$$G(A, B, C) = \sum m(1, 3, 4)$$

Express  $G$  in POM using  $\Pi$  notation

$$G(A, B, C) = m_1 + m_3 + m_4$$

$$\overline{G}(A, B, C) = (m_1 + m_3 + m_4)'$$

$$\overline{\overline{G}} = G = M_1 \cdot M_3 \cdot M_4$$

$$= \Pi M(1, 3, 4)$$

This tells us that the 0s of  $\overline{G}$

are at Rows 1, 3, 4

$\Rightarrow$  The 0s of  $G$  are in the other rows

$$\Rightarrow G(A, B, C) = \Pi M(0, 2, 5, 6, 7)$$

## Shortcut

To find  $G$  in the other canonical form, just list the indices not appearing in the given canonical form.

### Ex 11

$$F(A, B, C, D) = \prod M(0, 1, 2, 3, 4, 5, 6, 8, 10, 11, 12, 13, 14)$$

\* Express  $F$  in SOM

$$\Rightarrow F(A, B, C, D) = \sum m(7, 9, 15)$$

\* Express  $\overline{F}$  in POM and SOM.

$$\overline{F}(A, B, C, D) = \prod M(7, 9, 15)$$

$$\overline{F}(A, B, C, D) = \sum m(0, 1, 2, 3, 4, 5, 6, 8, 10, 11, 12, 13, 14)$$

# Operations on Canonical Forms

\* ANDing two SOMs  
 $\Rightarrow$  take intersection of indices

\* ORing two SOMs  
 $\Rightarrow$  Union indices

\* ANDing two POMs  
 $\Rightarrow$  Union

\* ORing two POMs  
 $\Rightarrow$  intersection

Ex 12

Let

$$F(A, B, C) = \Sigma m(0, 1, 3, 5)$$

$$G(A, B, C) = \Pi M(1, 2, 3, 4)$$

$$\begin{aligned} \bullet \quad F \cdot G &= (\Sigma m(0, 1, 3, 5)) \cdot (\Pi M(1, 2, 3, 4)) \\ &= \Pi M(2, 4, 6, 7) \cdot \Pi M(1, 2, 3, 4) \\ &= \Pi M(1, 2, 3, 4, 6, 7) \end{aligned}$$

Let  $F(A, B, C) = \sum m(0, 1, 3, 5)$   
 $G(A, B, C) = \prod M(1, 2, 3, 4)$

- $F + G = (\sum m(0, 1, 3, 5)) + (\sum m(0, 5, 6, 7))$   
 $= \sum m(0, 1, 3, 5, 6, 7)$

- How about  $F + G$  in POM?

$$F + G = \prod M(2, 4)$$

# Standard Forms

\* A product term is an ANDing of one or more literals

Ex

$AB$   
 $ABC$   
 $\bar{A}B\bar{C}$

→ Product terms

A minterm is a special case of a product term

\* Similarly, we define sum terms as the ORing of one or more literals

Ex

$\bar{A} + B$   
 $A + B + C$   
 $\bar{A} + \bar{C}$

→ All are sum terms

A maxterm is a special case sum term.

\* We can express Boolean functions as sum-of-products (SOP) or product-of-sums (POS) form.

\* These two forms are called the standard forms

Ex Indicate which of the following functions is in SOP or POS standard form.

•  $ab + cd$

SOP

•  $(a+b)(\bar{c}+a)$

POS

•  $ab + (a+b)(c+\bar{d})$

Not in standard form

•  $a + cd$

SOP

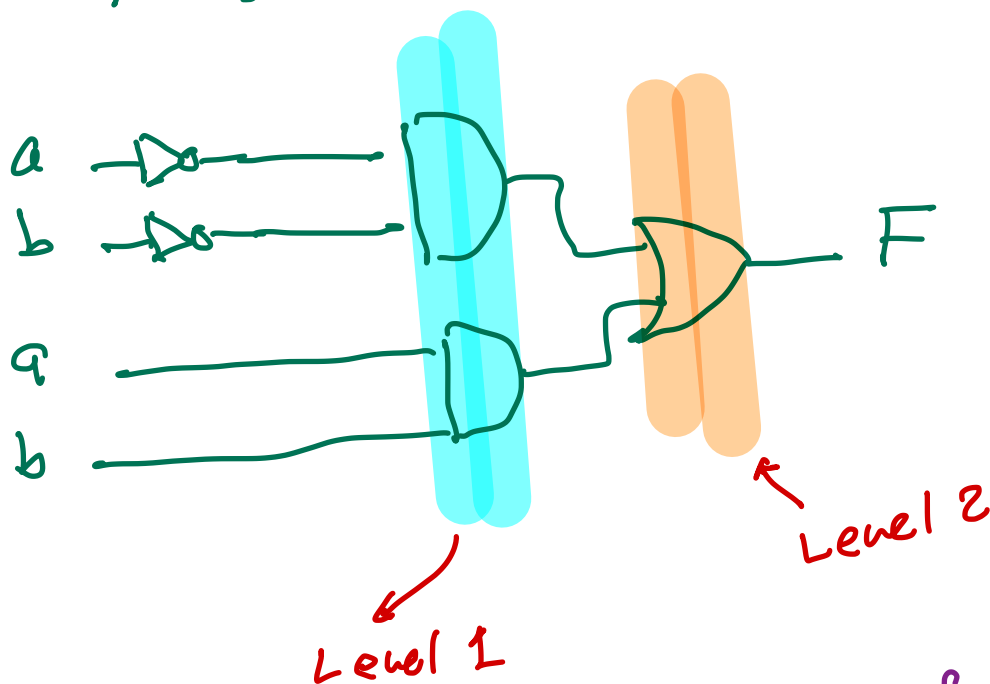
•  $a(b+c)$

POS

# Two-Level Implementations

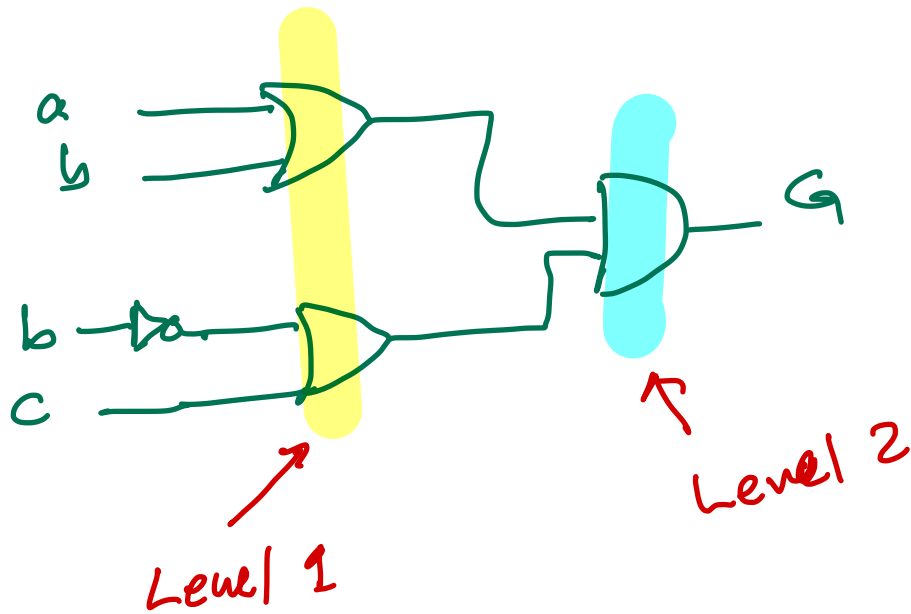
- \* Both standard and canonical forms result in 2-level logic circuits
- \* SOP/SOM: One level of AND gates followed by one OR gate  
→ total two levels

Ex  $F(a, b) = \sum m(0, 3) = \bar{a}\bar{b} + ab$



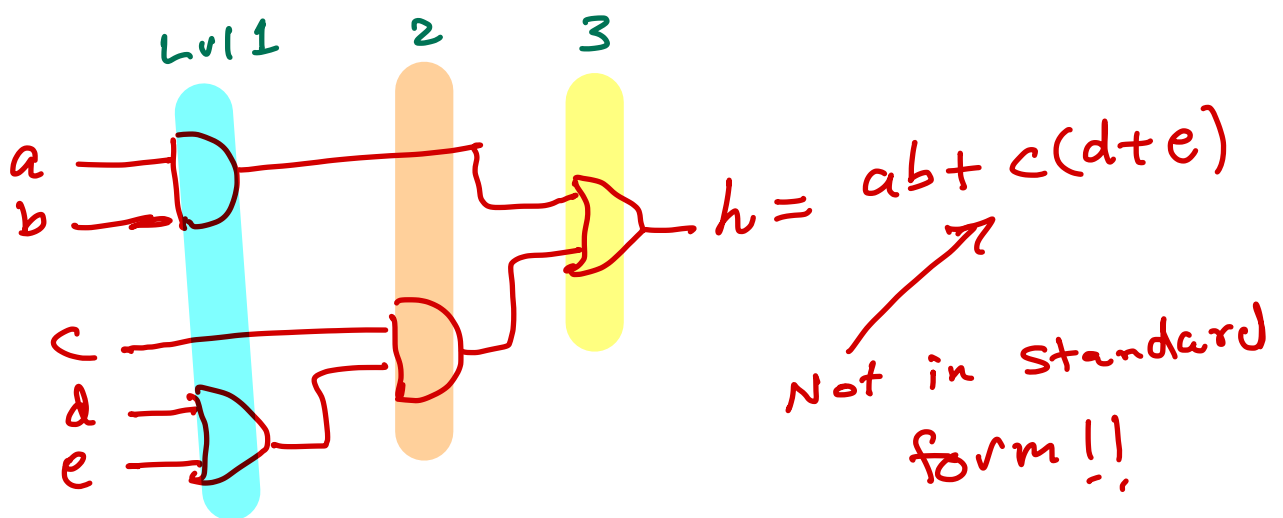
For obvious reasons, SOP/SOM forms are also referred to as AND-OR implementations.

Ex  
 $G = (a+b)(\bar{b}+c) \leftarrow \text{POS}$



Similarly, POS/POM forms are OR-AND implementations

\* if your implementation has  $> 2$  levels, it is not in standard form

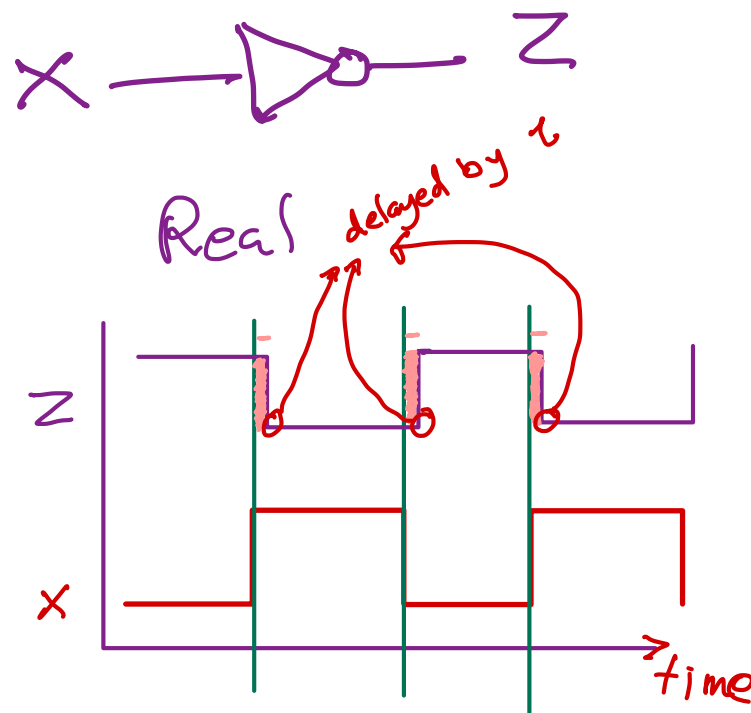
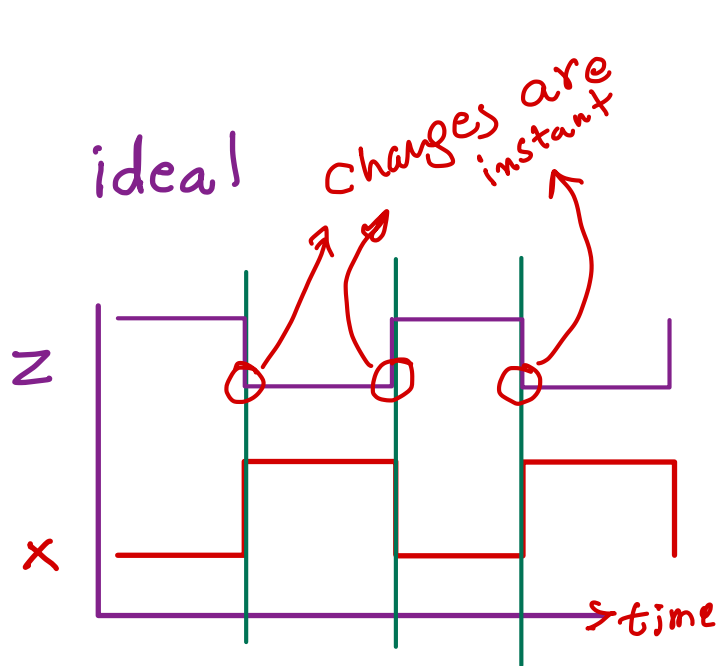




# Propagation Delay

\* So far, we ignored any delay from input to output

\* In practice, there is a delay, and the changes in output due to changes in input are not instantaneous



\* This delay in output change due to input change is "propagation delay"

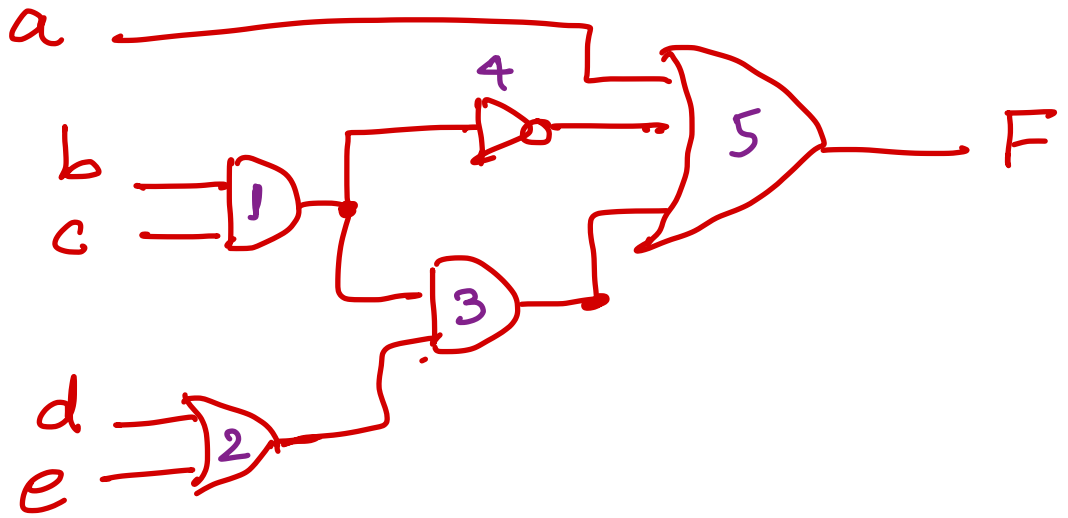
\* These plots are referred to as timing diagram or waveform.

## PROCEDURE FOR FINDING THE LONGEST PROPAGATION DELAY IN LOGIC CIRCUITS

1. Every gate has its own propagation delay (Given)
2. Start from inputs, compute the delay at output of each gate = delay of the gate + max. delay of its inputs
3. The maximum propagation delay from inputs to outputs is called the

**CRITICAL PATH**

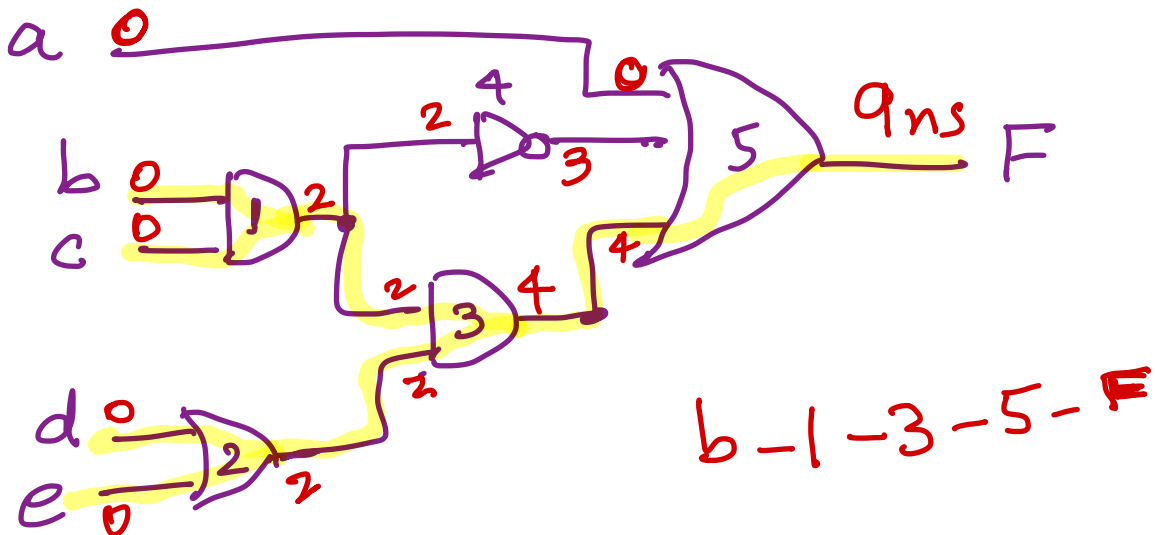
Ex



Assume the gates propagation delays are

Gate	Prop. delay
2-input AND/OR	2ns
Inverter	1ns
3-input OR	5ns

Find the delay at the output of every gate, and determine the critical path of this circuit.



## Exam Questions

3. Let  $F(A, B, C) = \bar{A}B + A\bar{C}$ . Express  $F(A, B, C)$  as a sum of minterms using the  $\Sigma$  notation. (2 Point)

\* We need to convert every product to a minterm

\* Use algebraic manipulation

• AND identity + Complement properties

$$\bar{A}B = \bar{A}B \cdot 1 = \bar{A}B(C + \bar{C}) = \bar{A}BC + \bar{A}B\bar{C}$$

$\begin{array}{cc} 011 \downarrow 3 & 010 \downarrow 2 \end{array}$

$$A\bar{C} = A(B + \bar{B})\bar{C} = AB\bar{C} + A\bar{B}\bar{C}$$

$\begin{array}{cc} 100 \downarrow 4 & 110 \downarrow 6 \end{array}$

$$\Rightarrow F(A, B, C) = \Sigma m(2, 3, 4, 6)$$

Shortcut

$$\begin{array}{l} \bar{A}B - \begin{array}{l} \nearrow 010 \rightarrow 2 \\ \searrow 011 \rightarrow 3 \end{array} \\ A\bar{C} - \begin{array}{l} \nearrow 100 \rightarrow 4 \\ \searrow 110 \rightarrow 6 \end{array} \end{array}$$

$$\Rightarrow F(A, B, C) = \Sigma m(2, 3, 4, 6)$$

6. Let  $G = AB + \bar{C}D$ . Express  $G$  in a product of sums standard form.

(3 Points)

Using dist. law

$$\begin{aligned} G &= (AB + \bar{C})(AB + D) \\ &= (A + \bar{C})(B + \bar{C})(A + D)(B + D) \end{aligned}$$

OR you can use duality to solve  
this problem

## Question 3:

(14 points)

- a) (1 point) The function F, where  $F(A,B,C,D) = \sum(2,3,6)$ , can be expressed algebraically in canonical form as:

- a.  $A'BC' + A'BC + ABC'$
- b.  $A'B'C + A'CD'$
- c.  $(A + B + C)(A + B + C')(A + B' + C)(A' + B + C')(A' + B' + C')$
- d. Answers (a) and (c)
- e. None of the Above.

- b) (1 point) Refer to the following statements:

*Statement 1:* All canonical forms for representing a function are standard forms.

*Statement 2:* All standard forms for representing a function are canonical forms.

*Statement 3:* The canonical forms and the standard forms are unique for each function

Which of these statements is/are correct?

- a. All statements.
- b. Statement 1 only.
- c. Statement 2 only.
- d. Statement 3 only.
- e. None.

- c) (5 points) Given  $G(x,y,z) = x'y + xz + yz$ .

(i) (2 points) Derive the truth table for function  $G(x,y,z)$ .

(ii) (1 point) List all the Minterms of function  $G(x,y,z)$  using the  $\sum$  notation.

(iii) (2 points) Write function  $G(x,y,z)$  as a product of Maxterms using algebraic form.

d) (4 points) Given the Boolean functions  $F(x,y,z)$  and  $G(x,y,z)$  as:

$$F(x,y,z) = \sum(0,2,4,5)$$

$$G(x,y,z) = (x + y + z') (x + y' + z) (x + y' + z') (x' + y + z') :$$

(i) (2 points) List the minterms of  $(F \cdot G')$  using the  $\sum$  notation.

(ii) (2 points) List the maxterms of  $(F' + G)$  using the  $\prod$  notation.

e) (3 points) Given the following implementation of function F. Calculate the propagation delay of F and determine the critical path. Assume the delay of each gate is equal to the number of inputs (i.e. the delay of an inverter is 1ns, the delay of a 2-input AND/OR gate is 2ns)

