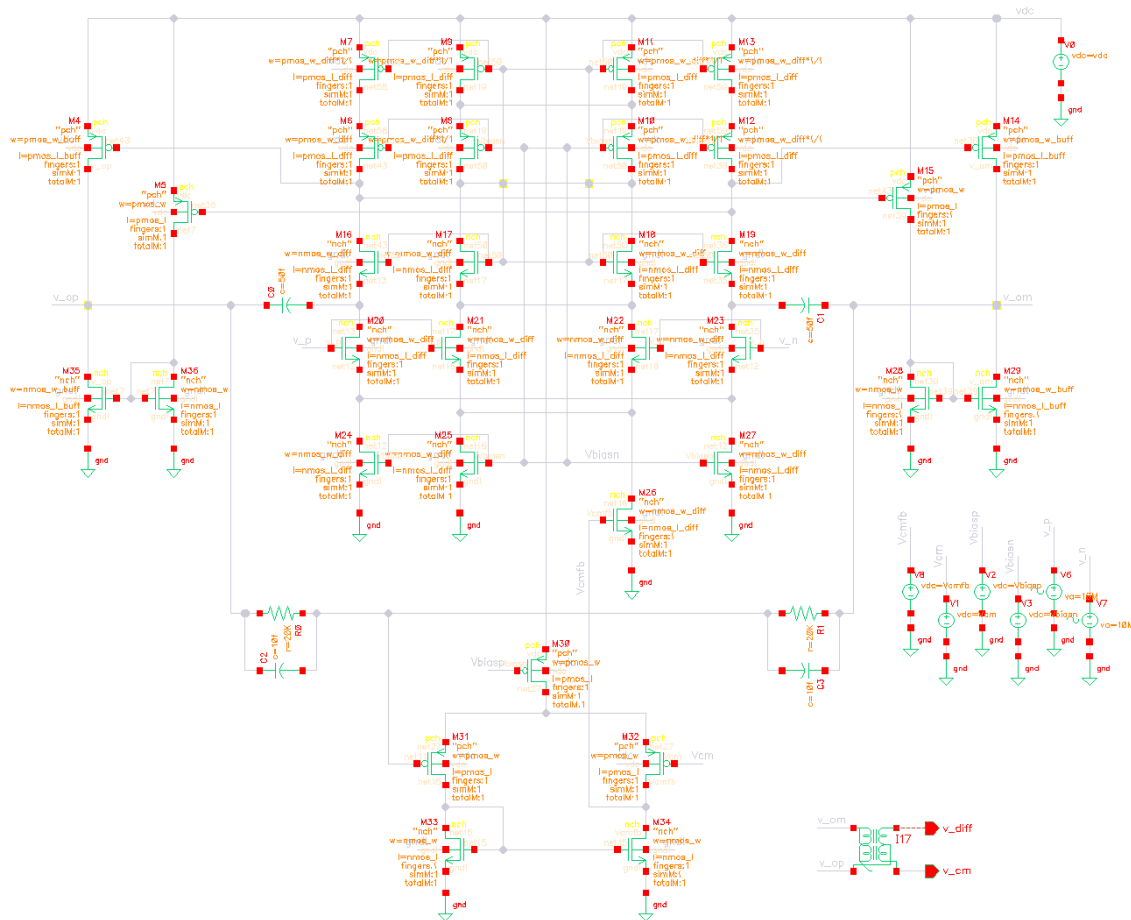
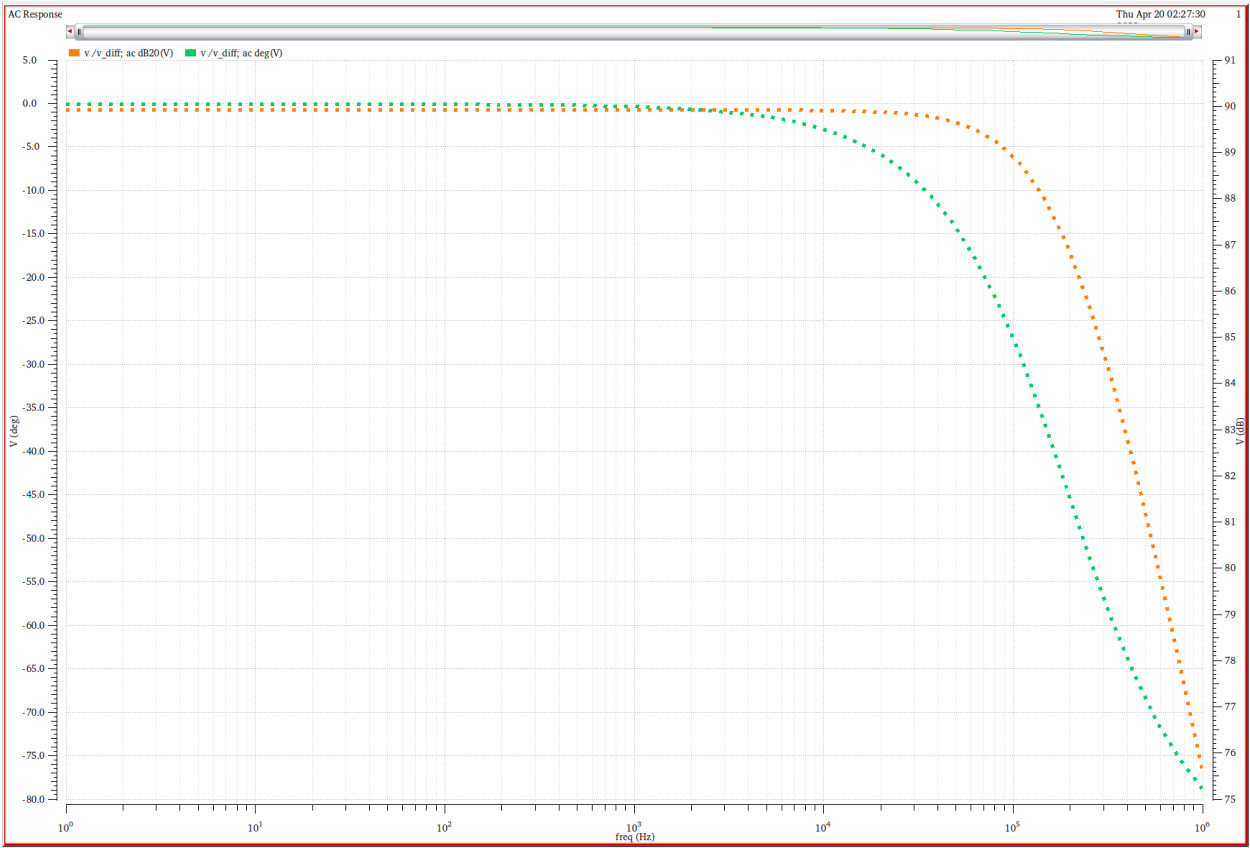
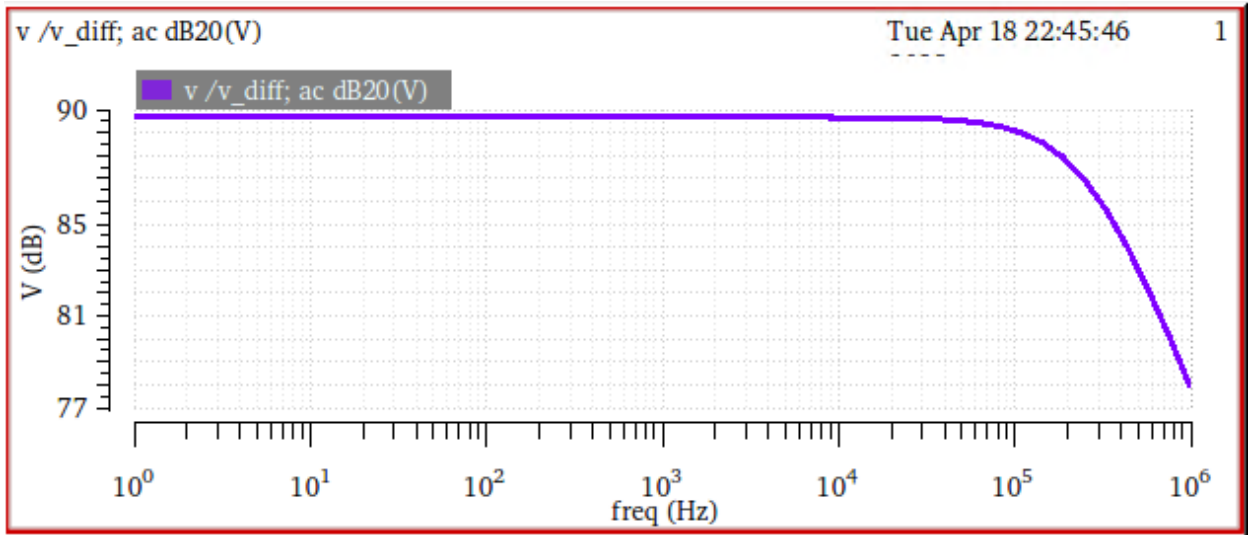


ELEG 587V Homework #5 (50 pts)

For all problems, you are to choose all the transistor properties that are required to meet the desired specification for the circuit as well as draw the circuit. Please box or underline all answers. Design both circuits within the MUSE 65nm technology within Cadence Virtuoso.

1. Design a fully-differential op amp with CMFB. Provide a bode plot for the design with two small-signal transient simulations (your choice). The gain should be at least 90 dB.





**Transient response:**

