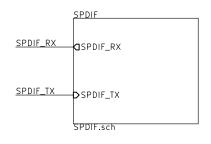
Open Hardware DSP Platform — www.ohdsp.org ADAU145x DSP — ADAU1452, ADAU1451, ADAU1450 all supported Revision 2.0

| | ADAU1452 | |
|---|---|--|
| IN_LRCLK0 | | DACQUE LECLEON OUT_LRCLKO |
| IN_BCLK0 | —DADCIN_LRCLKO —DADCIN_BCLKO | DACOUT_BCLKO |
| IN_SDATA0 | DADCIN_BCLKU DADCIN_SDATA0 | DACOUT_SDATAO OUT_SDATAO |
| IN LRCLK1 | | DACOUI_SDATAOD |
| | —DADCIN_LRCLK1 | DACOUI_LRCLKID |
| IN_BCLK1 | —DADCIN_BCLK1 | DACOUT_BCLK1D OUT_BCLK1 |
| IN_SDATA1 | —DADCIN_SDATA1 | DACOUT_SDATA1 D UUI_SDATA1 |
| IN_LRCLK2 | DADCIN_LRCLK2 | DACOUT_LRCLK2D OUT_LRCLK2 |
| IN_BCLK2 | ADCIN_BCLK2 | DACOUT_BCLK2D OUT_BCLK2 |
| IN_SDATA2 | ADCIN_SDATA2 | DACOUT_SDATA2 OUT_SDATA2 |
| IN_LRCLK3 | | OUT LDCLK7 |
| IN_BCLK3 | -DADCIN_LRCLK3 | DACUUI_LRCLKJU OUT BCLVZ |
| IN SDATA3 | -DADCIN_BCLK3_ | DACOUI_DULKO |
| | DADCIN_SDATA3 | DACOUI_SDATASD |
| SPDIF_RX | -DADAU_SPDIF_RX | ADAU_SPDIF_TXD SPDIF_TX |
| AUXADC1 | DAUXADC_IN1 | 12C SCI 12C_SCL |
| AUXADC2 | -DAUXADC_IN1 -DAUXADC_IN2 | I2C_SDA I2C_SDA |
| AUXADC3 | AUXADC_IN3 | IZC_SDAY |
| AUXADC4 | DAUXADC_INS | SPI_MISUD CDI_CCIA |
| AUXADC5 | DAUXADC_IN4 | STI_SCLIVE CDI MOCI |
| AUXADC6 | DAUXADC_INS | 3F1_MU31V |
| MAN_RESET | MAN_RST_IN | SPI_SS SPI_SS |
| GLB_RESET | GLB_RESET | MCLKD MCLK |
| | OCD_KESET | MCLK2D MCLK2 |
| SPI_M_MUTE | SPI_M_MUTE | CDI M CI K |
| SPI_M_RST | SPI_M_RST | SPI_M_CLND CDI M MOCI |
| SPI_M_CS | SPI_M_CS | SPI_M_MUSID CDI M MICO |
| | | |
| | | SPI_M_MISO |
| | ADAU1452.sch | SPI_M_MISO |
| | | SFI_M_MISU |
| IN_LRCLK0 | ADAU1452.sch Connectors | DAG LEGING OUT_LRCLKO |
| IN_BCLK0 | ADAU1452.sch Connectors QADC_LRCLKO | DAC_LRCLKO OUT_LRCLKO |
| | ADAU1452.sch Connectors ———————————————————————————————————— | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO |
| IN_BCLK0 IN_SDATA0 | ADAU1452.sch Connectors GADC_LRCLKO ADC_BCLKO ADC_SDATAO | DAC_LRCLKO OUT_LRCLKO DAC_BCLKO OUT_SCLKO DAC_SDATAO |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATA0 GADC_LRCLK1 | DAC_LRCLKO OUT_LRCLKO DAC_BCLKO OUT_SCLKO DAC_SDATAO DAC_LRCLK1 OUT_SCLK1 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 | ADAU1452.sch Connectors QADC_LRCLKO QADC_SDATAO QADC_SDATAO QADC_LRCLK1 QADC_BCLK1 | DAC_LRCLKO DAC_BCLKO DAC_SDATAO DAC_LRCLK1 DAC_BCLK1 DAC_BCLK1 DAC_BCLK1 DAC_BCLK1 DAC_BCLK1 |
| IN_BCLKO IN_SDATAO IN_LRCLK1 IN_BCLK1 IN_SDATA1 | ADAU1452.sch Connectors QADC_LRCLKO QADC_SDATAO QADC_LRCLK1 QADC_BCLK1 QADC_SDATA1 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_SCLKO ODAC_SDATAO DAC_LRCLK1 OUT_LRCLK1 DAC_BCLK1 OUT_LRCLK1 DAC_SDATA1 OUT_SDATA1 |
| IN_BCLKO IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 | ADAU1452.sch Connectors — ADAC_LRCLKO — ADAC_BCLKO — ADAC_SDATAO — ADAC_LRCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_LRCLK2 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_SCLKO DAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SCLK1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_LRCLK1 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 | ADAU1452.sch Connectors QADC_LRCLKO QADC_SDATAO QADC_SDATAO QADC_LRCLK1 QADC_BCLK1 | DAC_LRCLKO OUT_LRCLKO DAC_SDATAO DAC_BCLK1 OUT_SDATAO DAC_BCLK1 OUT_BCLK1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK1 OUT_BCLK1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_LRCLK2 DAC_BCLK2 OUT_LRCLK2 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_BCLK2 IN_SDATA2 | ADAU1452.sch Connectors — ADAC_LRCLKO — ADAC_BCLKO — ADAC_SDATAO — ADAC_LRCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_BCLK1 — ADAC_LRCLK2 | DAC_LRCLKO OUT_LRCLKO ODAC_SDATAO DAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 DAC_SDATA1 OUT_LRCLK1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_LRCLK2 DAC_SDATA2 OUT_LRCLK2 OUT_SDATA2 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 | ADAU1452.sch Connectors QADC_LRCLKO ADC_BCLKO ADC_SDATAO QADC_LRCLK1 ADC_BCLK1 ADC_BCLK1 ADC_SDATA1 QADC_LRCLK2 ADC_BCLK2 ADC_BCLK2 ADC_SDATA2 | DAC_LRCLKO OUT_LRCLKO DAC_SDATAO OUT_SDATAO DAC_BCLK1 OUT_SCLK1 DAC_BCLK1 OUT_SCLK1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_SDATA1 DAC_BCLK2 OUT_SCLK2 DAC_BCLK2 OUT_SCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_LRCLK3 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATA0 QADC_LRCLK1 QADC_SDATA1 QADC_SDATA1 QADC_LRCLK2 QADC_LRCLK2 QADC_SDATA2 QADC_LRCLK3 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_SCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_BCLK1 OUT_SDATA1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_LRCLK2 DAC_BCLK2 OUT_SDATA2 DAC_RCLK3 OUT_SDATA2 DAC_RCLK3 OUT_SDATA2 DAC_RCLK3 OUT_SCLK3 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATAO QADC_LRCLK1 QADC_BCLK1 QADC_SDATA1 QADC_LRCLK2 QADC_BCLK2 QADC_BCLK2 QADC_BCLK3 QADC_BCLK3 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_SCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_LRCLK1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_BCLK2 DAC_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_SCLK3 DAC_BCLK3 OUT_BCLK3 DAC_BCLK3 OUT_BCLK3 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATA0 QADC_LRCLK1 QADC_SDATA1 QADC_SDATA1 QADC_LRCLK2 QADC_LRCLK2 QADC_SDATA2 QADC_LRCLK3 | DAC_LRCLKO OUT_LRCLKO DAC_SDATAO OUT_SCLKO DAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 DAC_BCLK1 OUT_SDATA1 DAC_RCLK2 OUT_SDATA1 DAC_BCLK2 OUT_LRCLK2 DAC_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA2 DAC_LRCLK3 OUT_SCLK3 DAC_SDATA3 OUT_SCLK3 DAC_SDATA3 OUT_SCLK3 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 IN_SDATA3 | ADAU1452.sch Connectors — ADAULHCLKO — ADAU_LRCLKO — ADAU_SDATAO — ADAU_LRCLK1 — ADAU_SDATA1 — ADAU_SDATA1 — ADAU_SDATA1 — ADAU_LRCLK2 — ADAU_SDATA2 — ADAU_SDATA2 — ADAU_SDATA3 — ADAU_SDATA3 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_SDATAO DAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_BCLK1 OUT_SDATA1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_BCLK2 DAC_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA3 DAC_BCLK3 OUT_SDATA3 DAC_BCLK3 OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 I2C_SCL |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATAO GADC_LRCLK1 GADC_BCLK1 GADC_SDATA1 GADC_LRCLK2 GADC_LRCLK2 GADC_BCLK2 GADC_SDATA2 GADC_SDATA3 GADC_SDATA3 GADC_SDATA3 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_SDATA1 DAC_LRCLK2 OUT_LRCLK2 OUT_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA2 DAC_LRCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_SDATA2 DAC_SDATA2 OUT_SDATA3 DAC_SDATA3 OUT_SDATA3 I2C_SCL I2C_SCL I2C_SDA |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 IN_BCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 AUXADC1 AUXADC2 | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATAO GADC_SDATAO GADC_SDATA1 GADC_SDATA1 GADC_LRCLK2 GADC_SDATA2 GADC_LRCLK3 GADC_SDATA3 GADC_LRCLK3 GADC_SDATA3 GADC_SDATA3 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 DAC_BCLK1 OUT_SDATA1 DAC_SDATA1 OUT_SDATA1 DAC_SCLK2 OUT_BCLK2 DAC_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA2 DAC_BCLK3 OUT_BCLK3 DAC_BCLK3 OUT_BCLK3 DAC_BCLK3 OUT_BCLK3 DAC_SDATA3 OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL SDATA3 OUT_SDATA3 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_BCLK1 IN_BCLK2 IN_BCLK2 IN_SDATA1 IN_LRCLK2 IN_BCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 AUXADC1 AUXADC2 AUXADC3 | ADAU1452.sch Connectors — ADAU1452.sch Connectors — ADAU_LRCLK0 — ADAU_BCLK0 — ADAU_SDATA0 — ADAU_SDATA1 — ADAU_SDATA1 — ADAU_SDATA1 — ADAU_SDATA1 — ADAU_SDATA2 — ADAU_SDATA2 — ADAU_SDATA3 — ADAU_SDATA3 — ADAU_SDATA3 | DAC_LRCLKO OUT_LRCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_BCLK1 OUT_SDATA1 DAC_BCLK1 OUT_SDATA1 DAC_BCLK2 OUT_SDATA1 DAC_BCLK2 OUT_SDATA2 DAC_BCLK2 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA3 DAC_SDATA3 OUT_SDATA3 DAC_SDATA3 OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 I2C_SCL OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 PAC_SDATA3 OUT_SDATA3 I2C_SCL OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_BCLK1 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK3 IN_BCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 AUXADC2 AUXADC2 AUXADC3 AUXADC4 | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATAO QADC_LRCLK1 QADC_SDATA1 QADC_SDATA1 QADC_BCLK2 QADC_BCLK2 QADC_BCLK3 QADC_SDATA2 QADC_SDATA3 QADC_SDATA3 QAUSADCIN_1 QAUXADCIN_1 QAUXADCIN_2 QAUXADCIN_3 QAUXADCIN_3 | DAC_LRCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_BCLK1 OUT_SDATA1 DAC_BCLK1 OUT_SDATA1 DAC_SDATA1 OUT_SDATA1 DAC_BCLK2 OUT_BCLK2 DAC_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_SDATA2 DAC_BCLK3 OUT_SDATA3 DAC_BCLK3 OUT_SDATA3 DAC_SDATA3 OUT_SDATA3 DAC_SDATAS OUT_SDATAS SPI_MISO SPI_SSL |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 AUXADC2 AUXADC2 AUXADC3 AUXADC4 AUXADC5 | ADAU1452.sch Connectors QADC_LRCLKO ADAC_BCLKO ADAC_SDATAO QADC_LRCLK1 QADC_SDATA1 QADC_LRCLK2 QADC_BCLK2 QADC_BCLK2 QADC_BCLK3 QADC_BCLK3 QADC_SDATA2 QADC_LRCLK3 QADC_SDATA3 QAUXADCIN_1 QAUXADCIN_1 QAUXADCIN_2 QAUXADCIN_5 QAUXADCIN_5 QAUXADCIN_5 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK3 OUT_BCLK |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_BCLK1 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK3 IN_BCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 AUXADC2 AUXADC2 AUXADC3 AUXADC4 | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATAO QADC_LRCLK1 QADC_SDATA1 QADC_SDATA1 QADC_BCLK2 QADC_BCLK2 QADC_BCLK3 QADC_SDATA2 QADC_SDATA3 QADC_SDATA3 QAUSADCIN_1 QAUXADCIN_1 QAUXADCIN_2 QAUXADCIN_3 QAUXADCIN_3 | DAC_LRCLKO OUT_LRCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_SDATAO OUT_SDATAI DAC_SDATAI OUT_SDATAI DAC_SDATAI OUT_SDATAI DAC_SCLK2 OUT_LRCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_BCLK2 DAC_SDATA2 OUT_SDATA2 DAC_LRCLK3 OUT_BCLK3 DAC_SDATA3 OUT_SDATA3 DAC_SDATA4 OUT_SDATA4 DA |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_BCLK3 IN_SDATA3 AUXADC1 AUXADC2 AUXADC2 AUXADC3 AUXADC4 AUXADC5 | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATAO GADC_BCLK1 GADC_BCLK1 GADC_BCLK1 GADC_BCLK2 GADC_BCLK2 GADC_BCLK2 GADC_BCLK3 GADC_BCLK3 GADC_BCLK3 GADC_BCLK3 GADC_BCLK3 GADC_BCLK3 GADC_BCLK3 GADC_BCLK3 GAUXADCIN_1 GAUXADCIN_1 GAUXADCIN_1 GAUXADCIN_2 GAUXADCIN_4 GAUXADCIN_6 | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK3 OUT_BCLK |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_BCLK1 IN_BCLK2 IN_BCLK2 IN_BCLK2 IN_BCLK3 IN_SDATA1 IN_LRCLK3 IN_SDATA3 AUXADC1 AUXADC1 AUXADC2 AUXADC3 AUXADC4 AUXADC5 AUXADC6 | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATAO GADC_BCLK1 GADC_BCLK1 GADC_BCLK1 GADC_BCLK2 GADC_BCLK2 GADC_SDATA2 GADC_LRCLK3 GADC_SDATA2 GADC_BCLK3 GADC_SDATA3 GAUSADCIN_1 GAUXADCIN_2 GAUXADCIN_2 GAUXADCIN_2 GAUXADCIN_5 GAUXADCIN_6 GAUXADCIN_6 GAUXADCIN_6 | DAC_LRCLKO OUT_BCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 DAC_BCLK1 OUT_BCLK1 DAC_SDATA1 OUT_SDATA1 DAC_BCLK1 OUT_BCLK1 DAC_BCLK2 OUT_BCLK2 DAC_BCLK2 OUT_BCLK2 DAC_SDATA2 OUT_SDATA3 DAC_BCLK3 OUT_BCLK3 DAC_BCLK3 OUT_BCLK3 DAC_BCLK3 OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SCLK3 DAC_SDATA3 OUT_SDATA3 I2C_SCL OUT_SDATA3 I2C_SCL OUT_SDATA3 SPL_SCL SCL I2C_SDA SPL_SCL SPL |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_BCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_SDATA2 IN_LRCLK3 IN_SDATA3 AUXADC1 AUXADC2 AUXADC3 AUXADC4 AUXADC5 AUXADC6 MAN_RESET SPI_M_MUTE | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATAO GADC_SDATAI GADC_SDATA1 GADC_LRCLK1 GADC_BCLK2 GADC_BCLK2 GADC_BCLK2 GADC_BCLK3 GADC_BCLK3 GADC_SDATA3 GAUXADCIN_1 GAUXADCIN_1 GAUXADCIN_2 GAUXADCIN_3 GAUXADCIN_3 GAUXADCIN_5 GAUXADCIN_6 GAUXADCIN_6 GUSB_RST DM_MUTE | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK4 OUT_BCLK4 OUT_BCLK5 OUT_BCLK |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_SDATA3 AUXADC1 AUXADC1 AUXADC2 AUXADC3 AUXADC4 AUXADC5 AUXADC6 MAN_RESET SPI_M_MUTE GLB_RESET | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATAO QADC_LRCLK1 QADC_SDATA1 QADC_SDATA1 QADC_LRCLK2 QADC_BCLK2 QADC_BCLK2 QADC_BCLK3 QADC_BCLK3 QADC_SDATA3 QAUXADCIN_1 QAUXADCIN_2 QAUXADCIN_2 QAUXADCIN_5 QAUXADCIN_6 QUSB_RST DM_MUTE DM_RST | DAC_LRCLKO OUT_LRCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_BCLK1 OUT_SDATA1 DAC_BCLK1 OUT_SDATA1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_BCLK2 DAC_BCLK2 OUT_SCLK2 DAC_SDATA2 OUT_LRCLK2 DAC_SDATA2 OUT_SDATA3 DAC_BCLK3 OUT_SDATA3 DAC_SCLK3 OUT_SCLK3 DAC_SDATA3 OUT_SCLK3 DAC_SCLK3 OUT_SCLK3 DAC_SCLK3 OUT_SCLK3 DAC_SDATA3 OUT_SCLK3 DAC_SCLK3 OUT_SCLK3 OUT_SCLK |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_BCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_SDATA2 IN_LRCLK3 IN_SDATA3 AUXADC1 AUXADC2 AUXADC3 AUXADC4 AUXADC5 AUXADC6 MAN_RESET SPI_M_MUTE | ADAU1452.sch Connectors GADC_LRCLKO GADC_BCLKO GADC_SDATAO GADC_SDATAI GADC_SDATA1 GADC_LRCLK1 GADC_BCLK2 GADC_BCLK2 GADC_BCLK2 GADC_BCLK3 GADC_BCLK3 GADC_SDATA3 GAUXADCIN_1 GAUXADCIN_1 GAUXADCIN_2 GAUXADCIN_3 GAUXADCIN_3 GAUXADCIN_5 GAUXADCIN_6 GAUXADCIN_6 GUSB_RST DM_MUTE | DAC_LRCLKO OUT_LRCLKO ODAC_BCLKO OUT_BCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK1 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK2 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK3 OUT_BCLK4 OUT_BCLK4 OUT_BCLK5 OUT_BCLK |
| IN_BCLK0 IN_SDATA0 IN_LRCLK1 IN_BCLK1 IN_SDATA1 IN_LRCLK2 IN_BCLK2 IN_SDATA2 IN_LRCLK3 IN_SDATA3 AUXADC1 AUXADC1 AUXADC2 AUXADC3 AUXADC4 AUXADC5 AUXADC6 MAN_RESET SPI_M_MUTE GLB_RESET | ADAU1452.sch Connectors QADC_LRCLKO QADC_BCLKO QADC_SDATAO QADC_LRCLK1 QADC_SDATA1 QADC_SDATA1 QADC_LRCLK2 QADC_BCLK2 QADC_BCLK2 QADC_BCLK3 QADC_BCLK3 QADC_SDATA3 QAUXADCIN_1 QAUXADCIN_2 QAUXADCIN_2 QAUXADCIN_5 QAUXADCIN_6 QUSB_RST DM_MUTE DM_RST | DAC_LRCLKO OUT_LRCLKO ODAC_SDATAO OUT_SDATAO DAC_LRCLK1 OUT_SDATAO DAC_BCLK1 OUT_SDATA1 DAC_BCLK1 OUT_SDATA1 DAC_SDATA1 OUT_SDATA1 DAC_LRCLK2 OUT_BCLK2 DAC_BCLK2 OUT_SCLK2 DAC_SDATA2 OUT_LRCLK2 DAC_SDATA2 OUT_SDATA3 DAC_BCLK3 OUT_SDATA3 DAC_SCLK3 OUT_SCLK3 DAC_SDATA3 OUT_SCLK3 DAC_SCLK3 OUT_SCLK3 DAC_SCLK3 OUT_SCLK3 DAC_SDATA3 OUT_SCLK3 DAC_SCLK3 OUT_SCLK3 OUT_SCLK |





Notes:

All digital I/O is 3V3. Use outside this voltage can cause damage.

See bill of materials for detailed parts information.

Trace impedance on SPI/I2C, MCLK, and I2S is designed for approx 89ohm. 26AWG ribbon cable used with Ground-Signal-Ground is approx 89ohm.

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Sheet: /

File: DSP-ADAU1452.sch

Title: ADAU145x DSP - ADAU1452, ADAU1451, ADAU1450 supported

 Size: A4
 Date: 2016-05-25
 Rev: 2.0

 KiCad E.D.A. kicad 4.0.2-stable
 Id: 1/5

