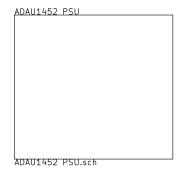
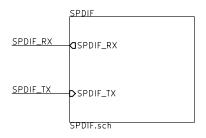
OHDSP ADAU1452 DSP

Note: All digital I/O is 3V3. Use outside this voltage can cause damage.

	ADAU1452		
IN_LRCLK0	->ADCIN_LRCLK0	DACOUT_LRCLKOD	OUT_LRCLKO
IN_BCLK0	->ADCIN_BCLK0	DACOUT_BCLKOD	OUT_BCLKO
IN_SDATA0	->ADCIN_SDATA0	DACOUT_SDATAOD	OUT_SDATAO
IN_LRCLK1	→ADCIN_LRCLK1	DACOUT_LRCLK1D	OUT_LRCLK1
IN_BCLK1	→ADCIN_BCLK1	DACOUT_BCLK1D	OUT_BCLK1
IN_SDATA1	-DADCIN_SDATA1	DACOUT_SDATA1D	OUT_SDATA1
IN_LRCLK2	→ADCIN_LRCLK2	DACOUT_LRCLK2D	OUT_LRCLK2
IN_BCLK2	→ADCIN_BCLK2	DACOUT_BCLK2D	OUT_BCLK2
IN_SDATA2	-DADCIN_SDATA2	DACOUT_SDATA2D	OUT_SDATA2
IN_LRCLK3	DADCIN_LRCLK3 DADCIN_BCLK3 DADCIN_SDATA3	DACOUT_LRCLK3D	OUT_LRCLK3
IN_BCLK3		DACOUT_BCLK3D	OUT_BCLK3
IN_SDATA3		DACOUT_SDATA3D	OUT_SDATA3
SPDIF_RX	DADAU_SPDIF_RX	ADAU_SPDIF_TXD	SPDIF_TX
AUXADC1	DAUXADC_IN1 DAUXADC_IN2	12C_SCL ◇	I2C_SCL
AUXADC2		12C_SDA ◇	I2C_SDA
AUXADC3 AUXADC4 AUXADC5 AUXADC6 MAN_RESET	-DAUXADC_IN3 -DAUXADC_IN4 -DAUXADC_IN5 -DAUXADC_IN6 -DMAN_RST_IN	SPI_MISOD SPI_SCLK SPI_MOSIO SPI_SSO	SPI_MISO SPI_SCLK SPI_MOSI SPI_SS
GLB_RESET	GLB_RESET	MCLKD MCLK2D	MCLK2
	ADAU1452.sch	MCLKZD	

	Connectors		
IN_LRCLKO	DADC_LRCLKO DADC_BCLKO DADC_SDATAO	DAC_LRCLKO C	OUT_LRCLKO
IN_BCLKO		DAC_BCLKO C	OUT_BCLKO
IN_SDATAO		DAC_SDATAO C	OUT_SDATAO
IN_LRCLK1	DADC_LRCLK1 DADC_BCLK1 DADC_SDATA1	DAC_LRCLK1	OUT_LRCLK1
IN_BCLK1		DAC_BCLK1	OUT_BCLK1
IN_SDATA1		DAC_SDATA1	OUT_SDATA1
IN_LRCLK2	□ADC_LRCLK2	DAC_LRCLK2	OUT_LRCLK2
IN_BCLK2	□ADC_BCLK2	DAC_BCLK2	OUT_BCLK2
IN_SDATA2	□ADC_SDATA2	DAC_SDATA2	OUT_SDATA2
IN_LRCLK3	□ADC_LRCLK3	DAC_LRCLK3 <	OUT_LRCLK3
IN_BCLK3	□ADC_BCLK3	DAC_BCLK3 <	OUT_BCLK3
IN_SDATA3	□ADC_SDATA3	DAC_SDATA3 <	OUT_SDATA3
AUXADC1	AUXADCIN_1	12C_SCL ◇	I2C_SCL
AUXADC2		12C_SDA ◇	I2C_SDA
AUXADC3 AUXADC4 AUXADC5 AUXADC6	☐AUXADCIN_2 ☐AUXADCIN_3 ☐AUXADCIN_4 ☐AUXADCIN_5 ☐AUXADCIN_6	SPI_MISO C SPI_SCLKD SPI_SSD SPI_MOSID	SPI_MISO SPI_SCLK SPI_SS SPI_MOSI
MAN_RESET	QUSB_RST Connectors.sch	MCLK2_IN	MCLK2





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File: DSP-ADAU1452-StandAlone.sch

Title: OHDSP DSP-ADAU1452-StandAlone						
	Size: A4	Date: 18	Jan 2016		Rev: 1.0	
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