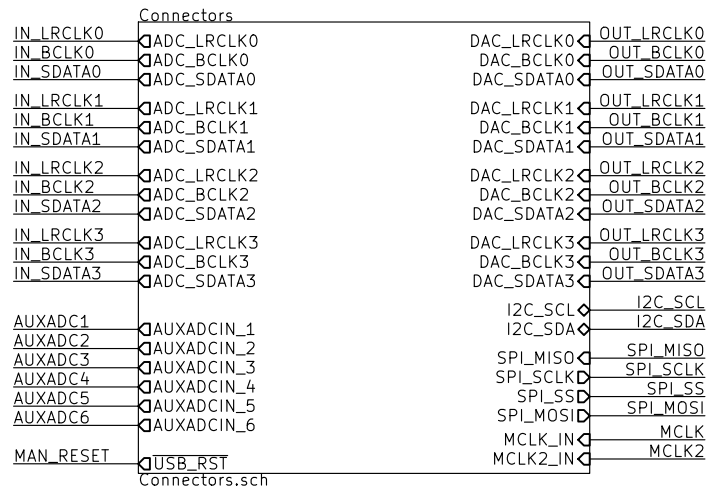
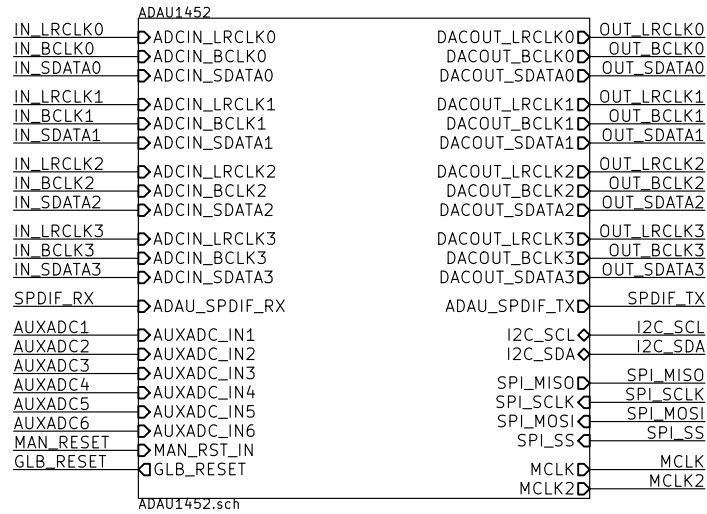


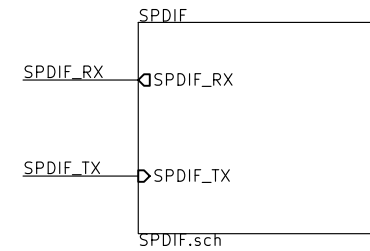
OHDSP ADAU1452 DSP

Note:
All digital I/O is 3V3. Use outside this voltage can cause damage.



ADAU1452 PSU

ADAU1452 PSU.sch



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Sheet: /

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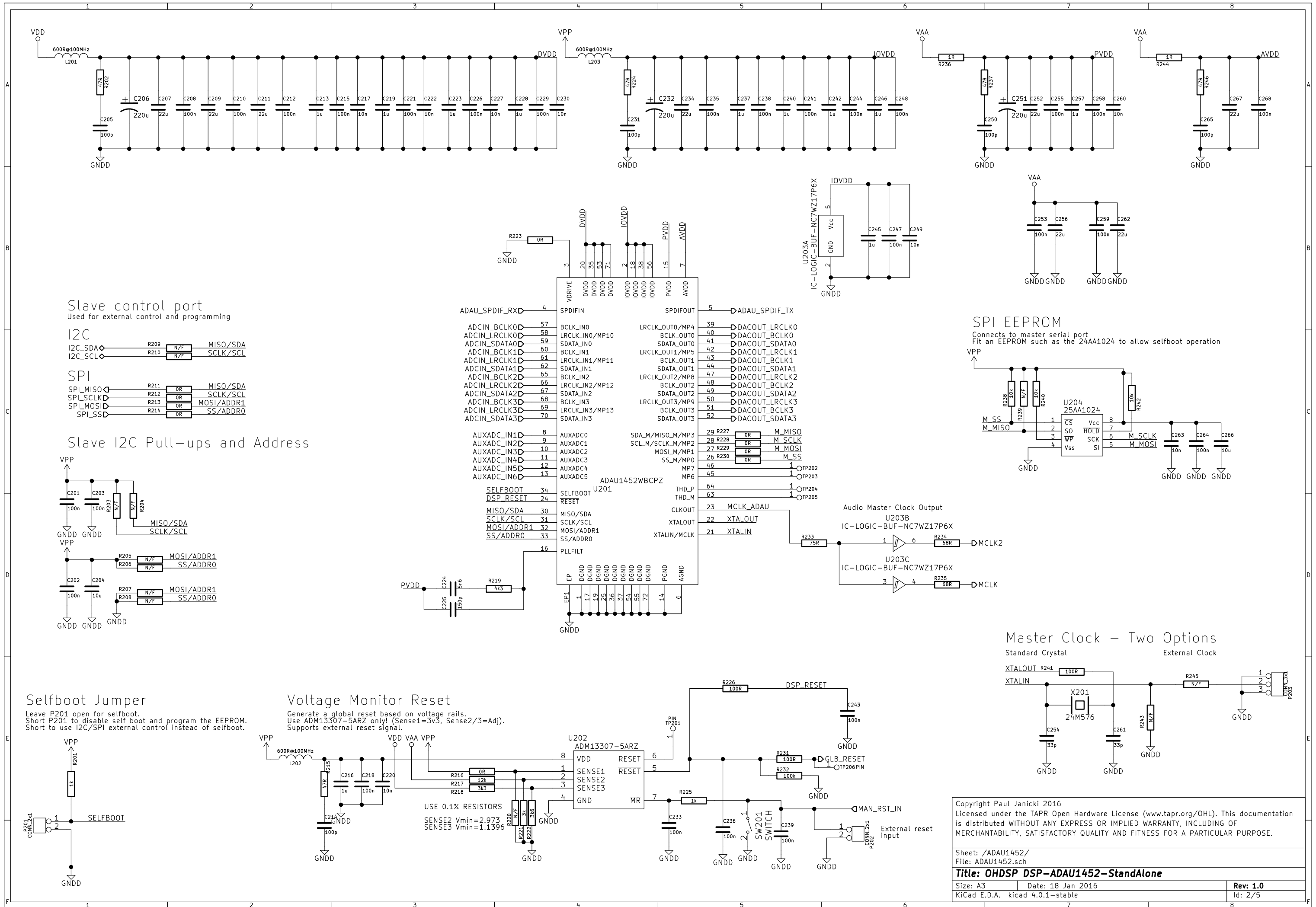
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Size: A4 Date: 18 Jan 2016

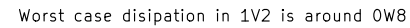
KiCad E.D.A. kicad 4.0.1-stable

Rev: 1.0

Id: 1/5



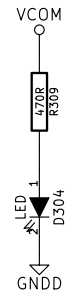
First section generates 3v8, then two LDOs generate 3v3 and 1v2



VPP
↑



5V Input



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File: ADAU1452 PSU.sch

Title: OHDSP DSP-ADAU1452-StandAlone

Size: A4	Date: 18 Jan 2016
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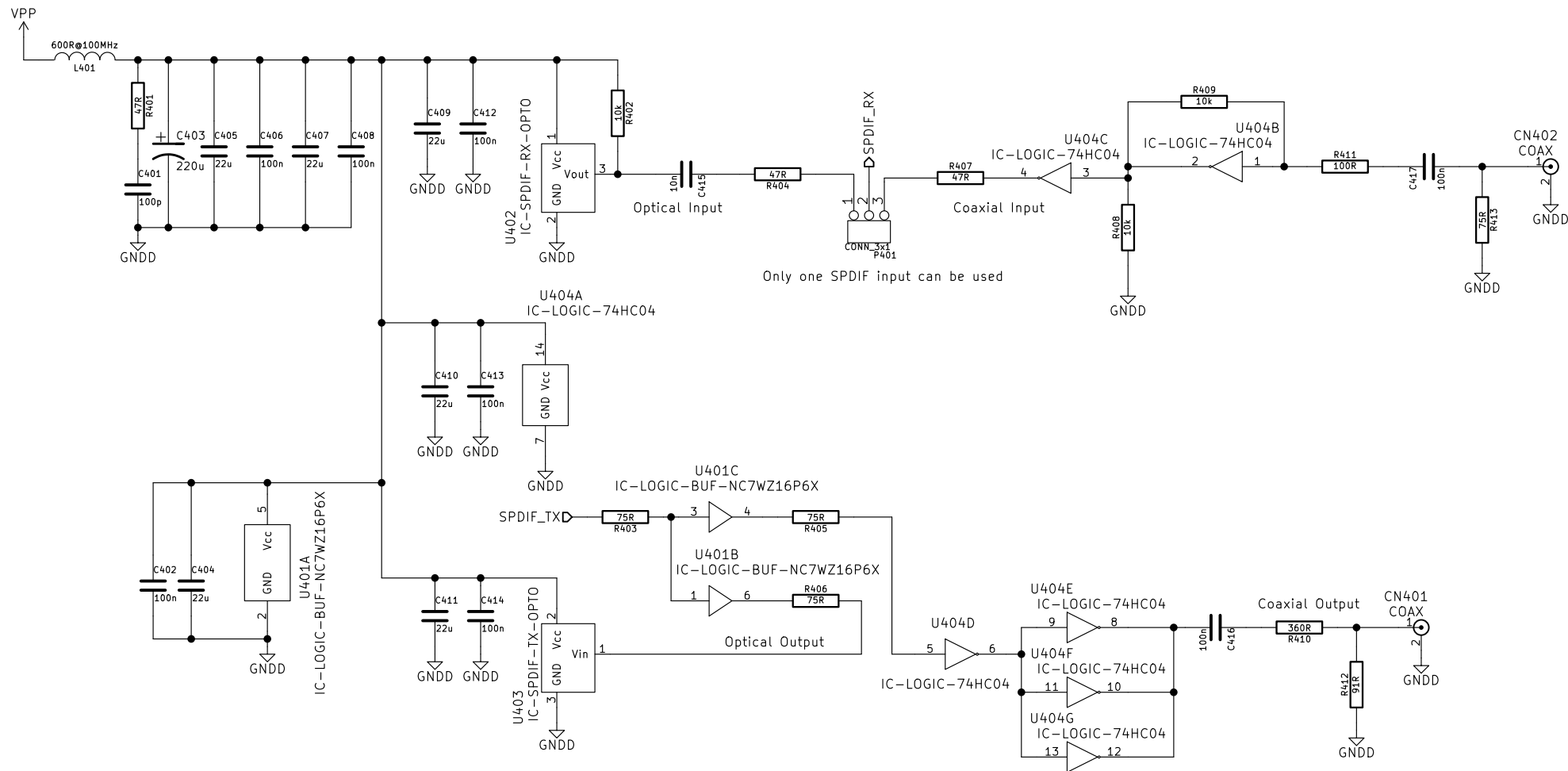
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Rev: 1.0

Id: 3/5

SPDIF Optical Input and Output

Useful information for SPDIF conversion:
http://whitefiles.org/b1_s/1_free_guides/fg1mt/pgs/h13f.htm



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Sheet: /SPDIF/
 File: SPDIF.sch

Title: OHDSP DSP-ADAU1452-StandAlone

Size: A4 Date: 18 Jan 2016

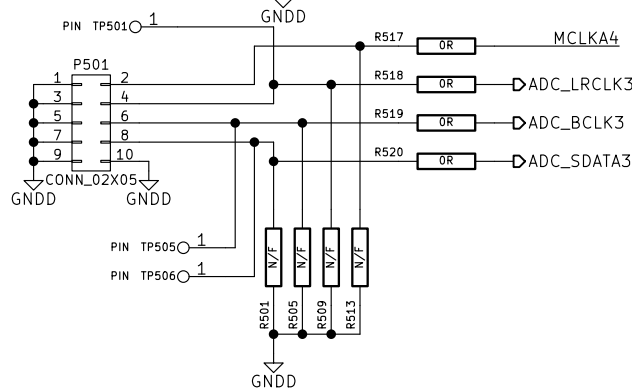
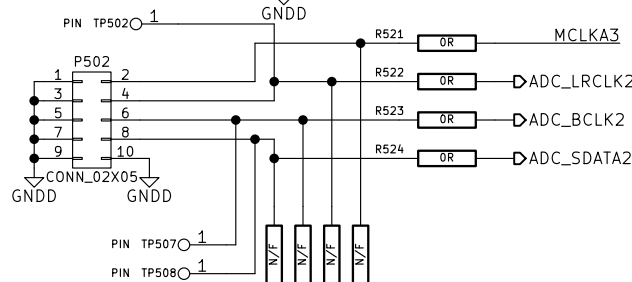
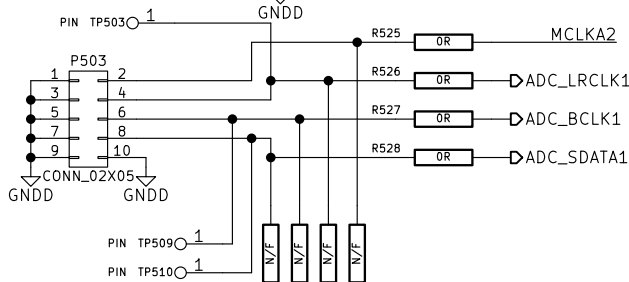
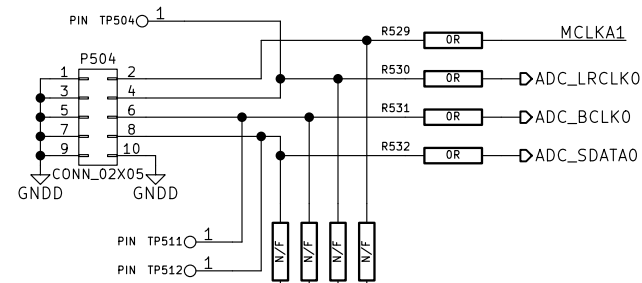
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Rev: 1.0

Id: 4/5

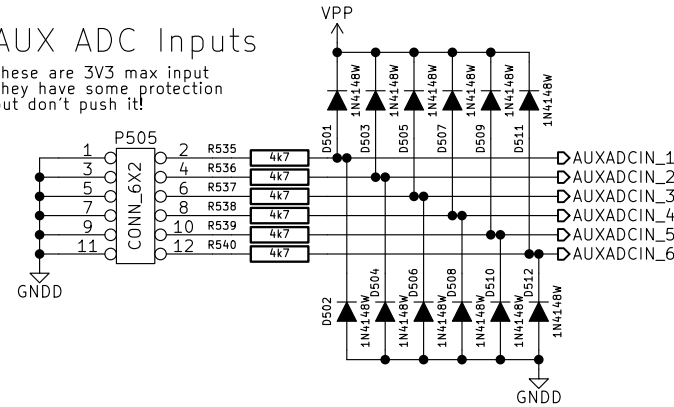
I2S Inputs

See ADAU1452 datasheet for details
Inputs are highly flexible



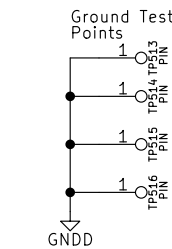
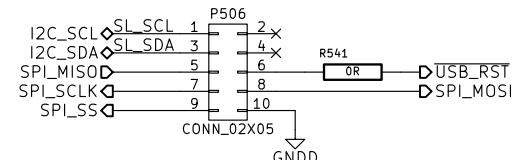
AUX ADC Inputs

These are 3V3 max input
They have some protection
but don't push it!

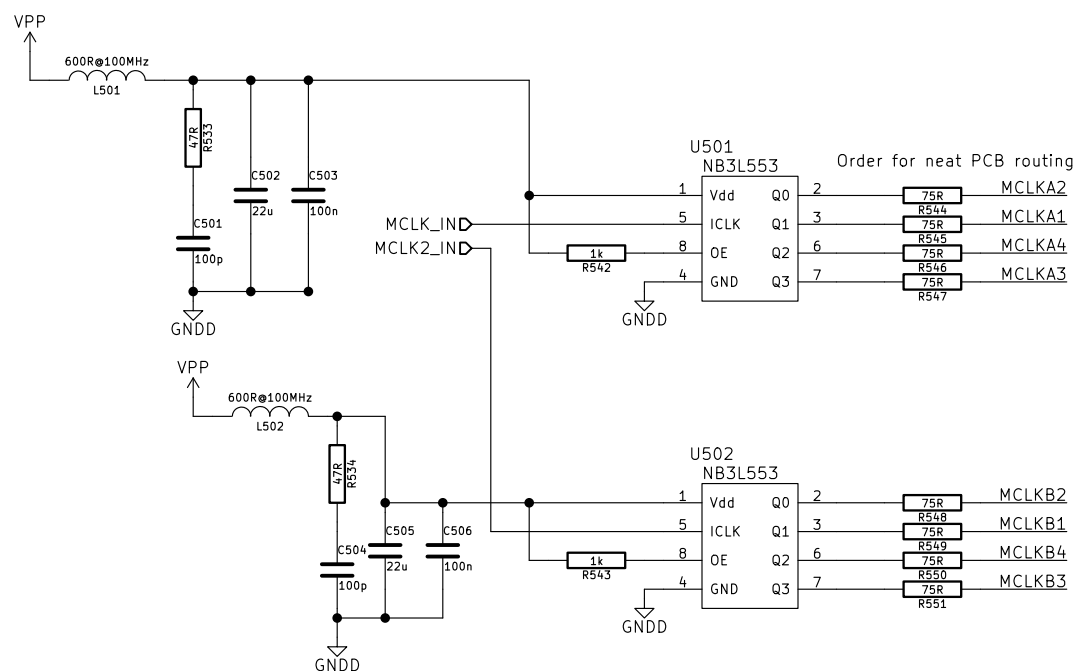


ADI EVAL-ADUSB2Z USB/I2C

These go to the slave port
These are 3V3 lines only!



Master Clock Fan Out



I2S Outputs

See ADAU1452 datasheet for details
Outputs are highly flexible

