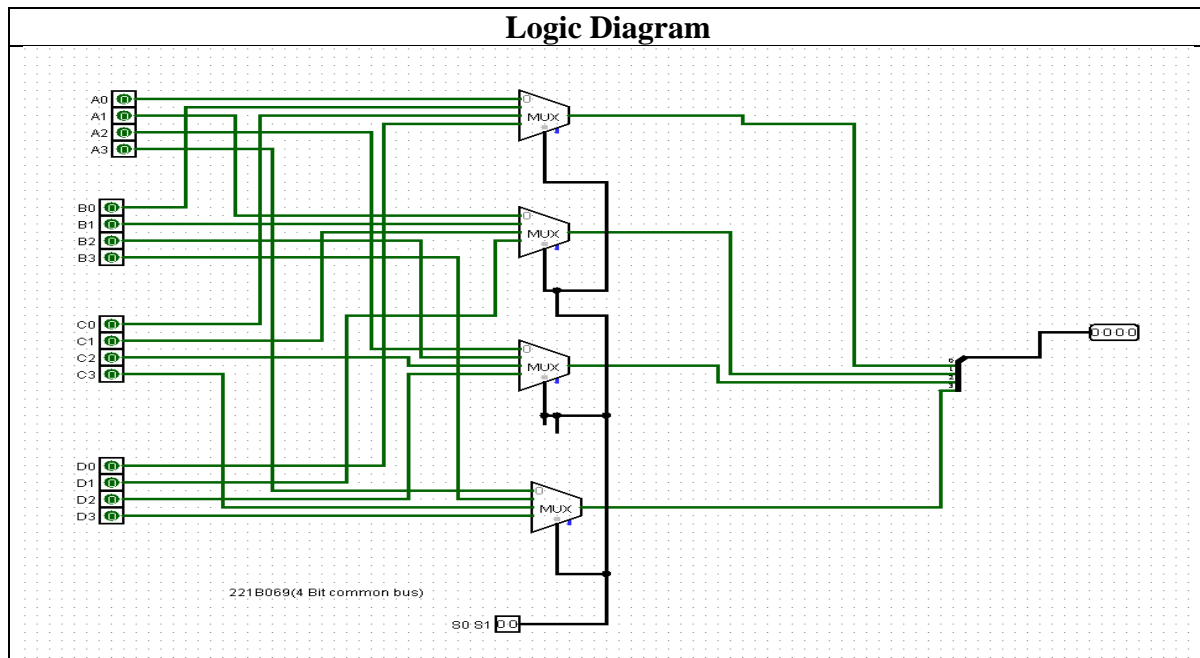


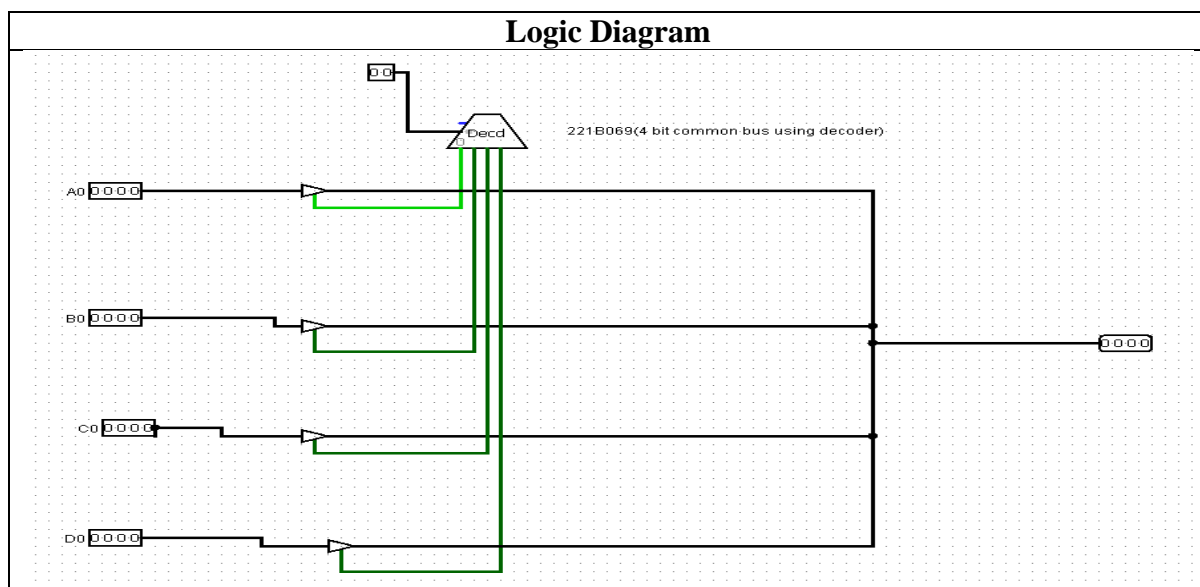
Experiment # 5

Aim: Design of common bus, logic unit and shift unit.

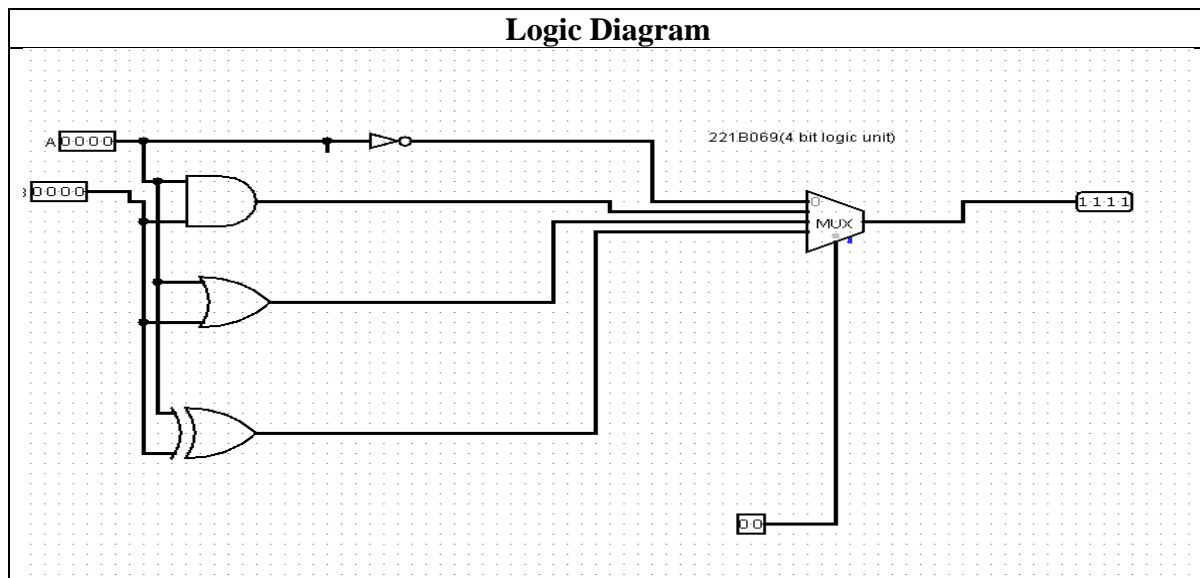
Exercise#1: Design 4-bit common bus system using in-built block of multiplexer which shares data to the output from four sources as given in the table. Use inputs and outputs as individual bits and selection lines in the form of bus.



Exercise#2: Repeat Exercise#1 using in-built block of tri-state (controlled) buffers and decoder.



Exercise#3: Design 4-bit logic unit using in-built block of multiplexer which performs four logical operations as given in the table. Use all inputs, outputs and selection lines in the form of bus.



Exercise#4: Design 4-bit shift circuit in using in-built block of multiplexer which performs four shift operations as given in the table. Use all inputs, outputs and selection lines in the form of bus.

