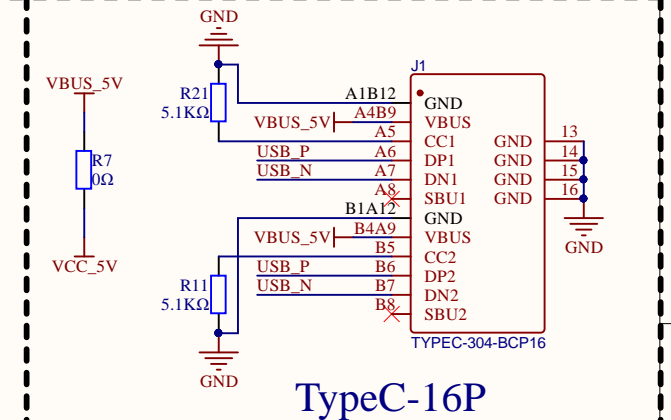
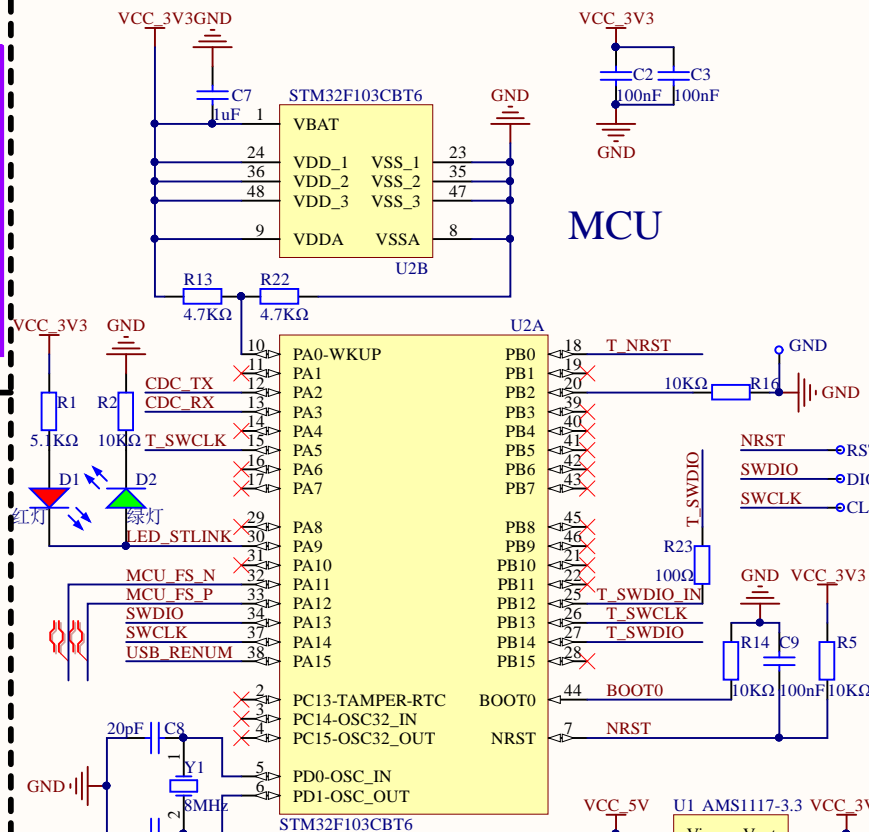


The diagram shows the pinout for a USB connector with four connection points:

- J7:**
  - VBUS\_5V
  - DTR\_SIG
  - RXD
  - TXD
  - GND
- J3:**
  - GND
  - CDC\_TX
  - CDC\_RXD
  - RENUM
  - VCC\_PWR
- J2:**
  - GND
  - T\_SWDIO
  - T\_SWCLK
  - T\_NRST
  - VCC\_PWR
- J1:**
  - USB Connector (TypeC-16P)

Below the J2 and J3 blocks, the following signals are listed:

- J8:**
  - VBUS\_5V
  - T\_NRST
  - T\_SWCLK
  - T\_SWDIO
  - GND



CP2102N

Pin 1: DCD

Pin 2: RI/CLK

Pin 3: GND

Pin 4: CP2102 FS P

Pin 5: CP2102 FS N

Pin 6: D+

Pin 7: D-

Pin 8: VDD

Pin 9: VREGIN

Pin 10: VBUS

Pin 11: RSTB

Pin 12: NC

Pin 13: SUSPENDB

Pin 14: SUSPEND

Pin 15: CHREN

Pin 16: CHR1

Pin 17: GPIO.3/WAKEUP

Pin 18: GPIO.2/RS485

Pin 19: GPIO.1/RXT

Pin 20: GPIO.0/TXT

Pin 21: GPIO.6

Pin 22: GPIO.5

Pin 23: RTS

Pin 24: RXD

Pin 25: TXD

Pin 26: DSR

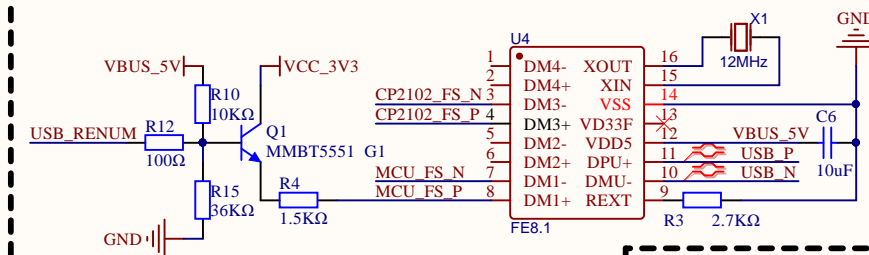
Pin 27: DTR

Pin 28: SIG

U3

CP2102N-A02-GQFN28

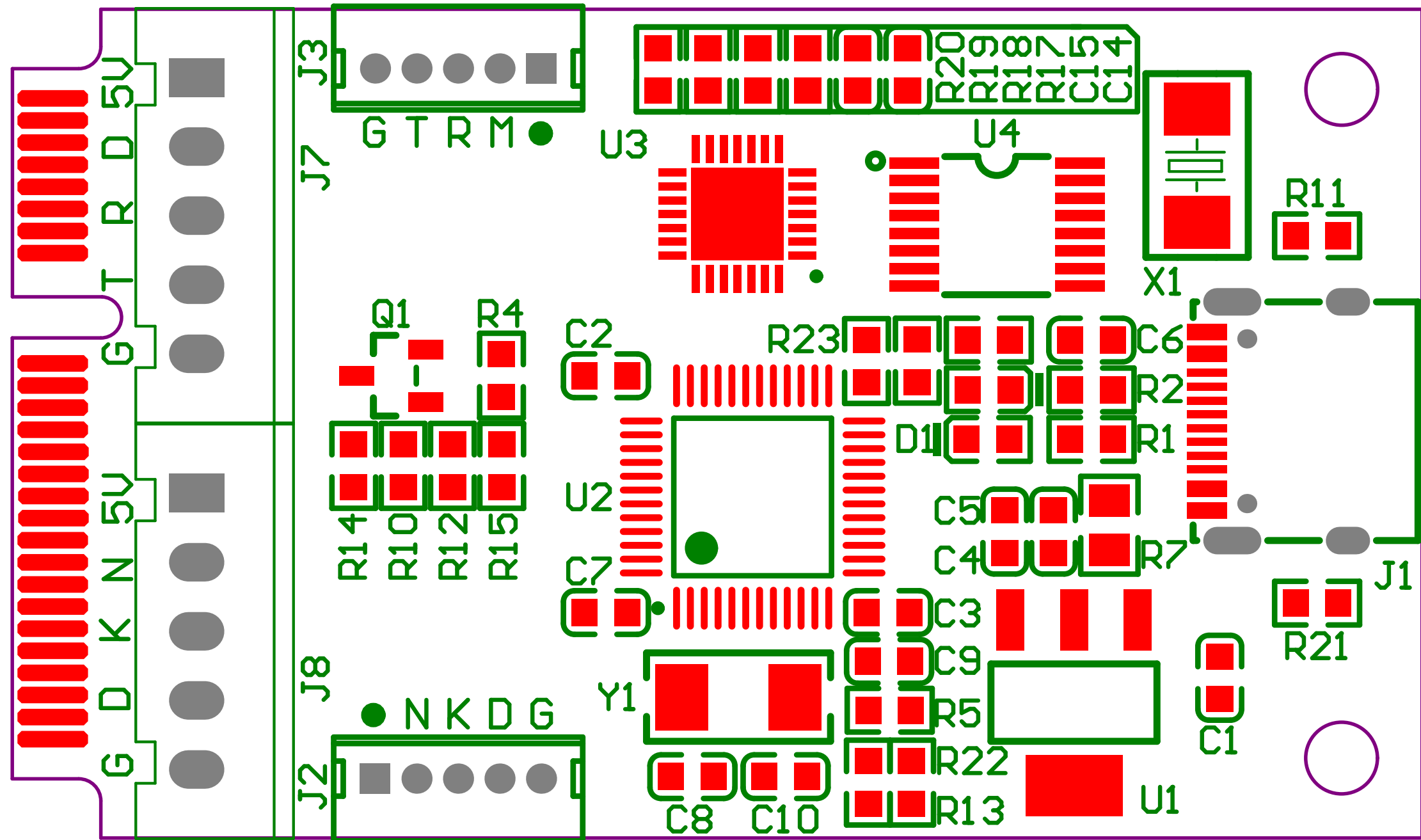
Pin connection diagram for the CP2102N-A02-GQFN28 package. The diagram shows a yellow rectangular chip with pins numbered 1 through 28. Pins 1-7 are on the left, 8-14 on the bottom, 15-21 on the right, and 22-28 on the top. Various pins are connected to external signals: GND, SIG, DTR, TXD, RXD, CTS, RTS, GPIO.4, GPIO.5, GPIO.6, GPIO.0/TXT, GPIO.1/RXT, GPIO.2/RS485, GPIO.3/WAKEUP, CHR0, VBUS, RSTB, NC, SUSPENDB, SUSPEND, CHREN, and CHR1. Pins 2, 3, 4, 5, 6, 7, 11, 12, 13, 14, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, and 28 are marked with a red 'X' and a diagonal line, indicating they are not connected. A dashed line separates the top pins from the bottom pins.



Model Name: Aixcel_Tiny_S	
Size: A4	Number: 01
Date: 2022/11/2	Time:

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*Guass Rd At Pudong*  
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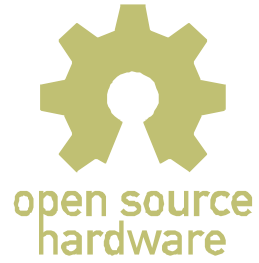
Aixcel



JLCJLCJLCJLC

VBUS_5V
USB_RENUM
RXD
TXD
GND

VBUS_5V
NRST
SWCLK
SWDIO
GND



Aixcel .Ltd

RSTCLKDIOGND

