# Difference between Monoey Mapped IO and IO Mapped

The Micropeocessor cannot do anything by itself therefore, it needs to be linked with memory, extra peripherals or ID denices. This linking is called interfacing.

Memory Mapped IO

- 1). IO dernices are accessed like any other memory location.
- address values.
- 3) The instruction used are LDA, STA e.t.c.
- 4) Cycles involved during operation are Memory Read & Memory Weite
- 5) Any register can communicate with IO de nice.
- 6) 2<sup>16</sup> I/O ports are possible to be used for interfacing.
- 7) During read or write cycle IO/ 14 = 0

IO Mapped IO They cannot be accessed

like any other memory

location.

They are assigned with 8-bit address values.

The instruction used are IN and OUT.

Cycles involved during operation are IO read and IO weite. In

Only accumulator can communicale with ID denices.

 $\vartheta^{8}$  I/O poets are possible to be used for interfacing.

During read or weith cycle IO/M = 1.

- 8) No separate control signal Special control signals are required since we have lot used.

  Of manoxy space.
- 9) Assistanctic and logic operation. Anotheredic and logic operation are performed directly on the are god performed directly on the data.
- Note: The interfacing of the I/O denices in 8085 can be done in two ways.
- → Manay Mapped I/O interfacing
  In this kind of interfacing, we assign a memory address that can be used in some names as we used a Normal Memory location.
- → I/O Mapped I/O tenterfacting

  A kind of interfacing in which we assign a 8- bit address value to the input / output devices which can be accessed using IN and OUT instendions.