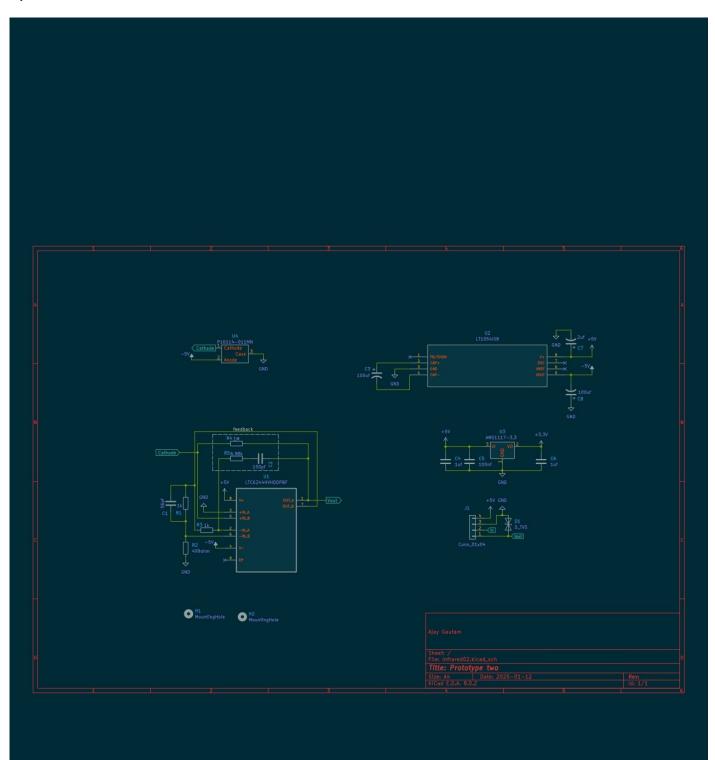
Firmware for Value Reading from Sensor:

```
// Well Documented code for better understanding.
//Referance 1. https://embeddedexplorer.com/esp32-gpio-tutorial/
           2. https://my-esp-idf.readthedocs.io/en/latest/api-
reference/peripherals/gpio.html
https://www.espressif.com/sites/default/files/documentation/esp32 technical reference manu
al en.pdf
     page: 26,32, 63,
// i've used above for making this code i haven't add gpio.h header, i've directly mapped
resisters.
#include <stdint.h>
#include <stdio.h>
#define GPIO_INPUT_PIN 25 // Define GPIO pin number
// Register addresses
#define GPIO ENABLE_REG 0x3FF44020 // GPIO enable register
#define GPIO_OUT_REG 0x3FF44004 // GPIO output register
--- Added for future
#define GPIO_PIN_MUX_REG 0x3FF49024 // Pin multiplexing register --- Added for future
// Function to set a GPIO pin as input
void gpio_set_direction(uint8_t gpio_num, uint8_t mode) {
   if (mode) { // If mode is 1, set as output
       *(volatile uint32_t *)(GPIO_ENABLE_REG) |= (1 << gpio_num);
       *(volatile uint32_t *)(GPIO_ENABLE_REG) &= ~(1 << gpio_num);
    }
// Function to read the level of a GPIO pin
uint8_t gpio_get_level(uint8_t gpio_num) {
    return (*(volatile uint32_t *)(GPIO_IN_REG) >> gpio_num) & 0x01; // & 0x01 is used to
isolate the least significant bit (LSB)
from the GPIO input register, mask all, (to extract a single bit from a multi-bit value)
void main() {
   // Set GPIO_INPUT_PIN as input
   gpio_set_direction(GPIO_INPUT_PIN, 0);
   while (1) {
       // Read the level of the input pin
       uint8_t level = gpio_get_level(GPIO_INPUT_PIN);
       // Print the level to Terminal or serial monitor. same time connect FFT Analyzer
       printf("GPIO %d Level: %d\n", GPIO_INPUT_PIN, level);
       // Add a delay if necessary (e.g., using vTaskDelay)
```

]

Updated Schematic:



Remark:

- 1. Sensor P16114-011MN is used.
- 2. Pcb layout routing is Pending
- 3. Firmware is Well Documented, Added citation for better understanding.

Pending work:

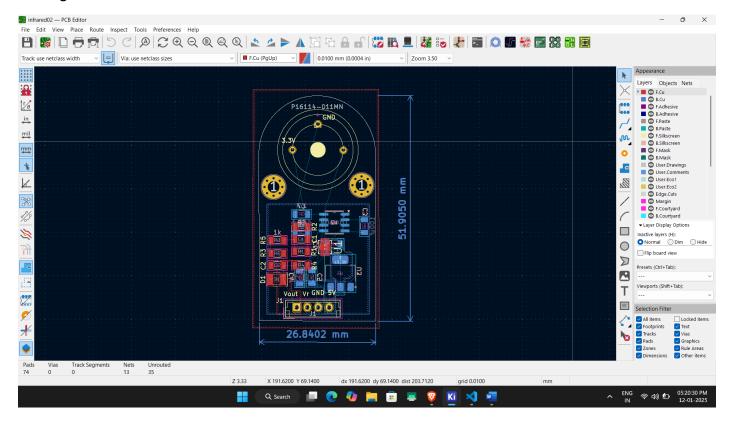


Figure: Pcb layout