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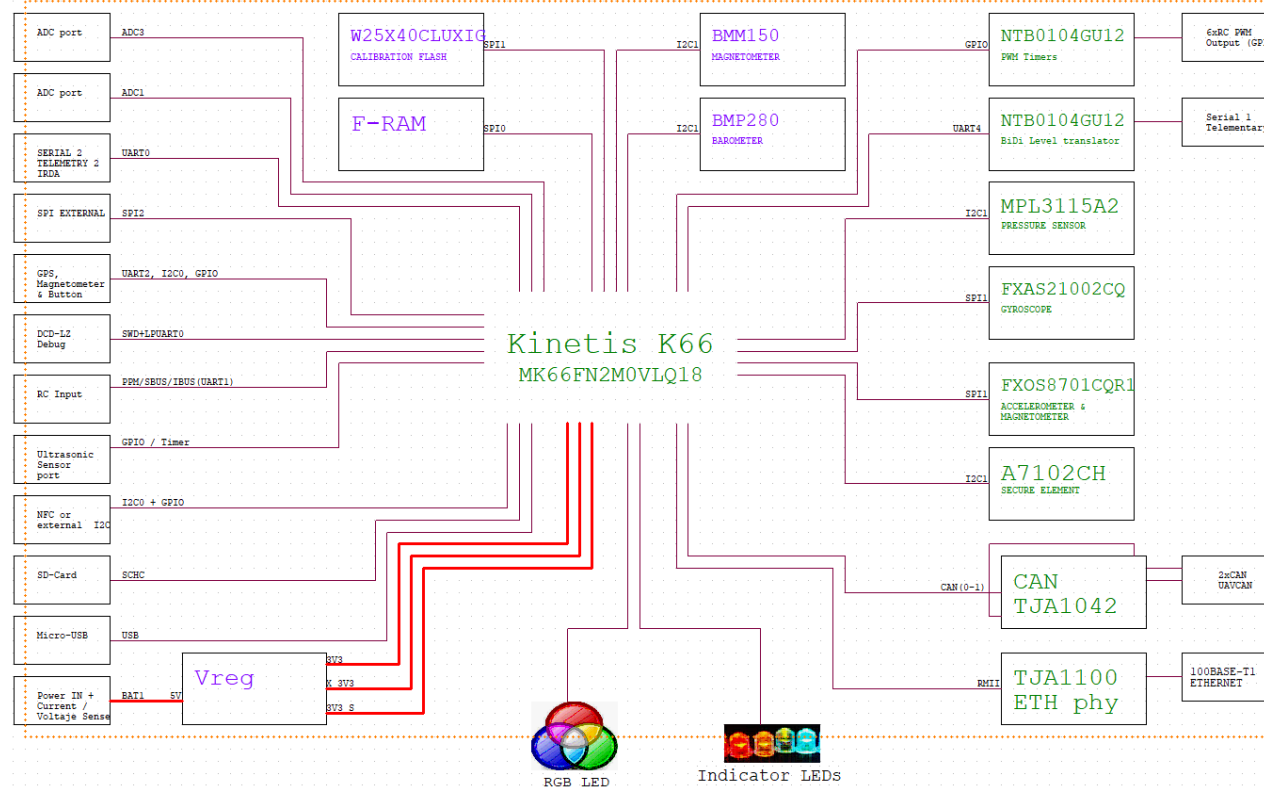
Revisions			
Rev	Description	Date	Approved
X1	First Release	14FEB18	IAIN GALLOWAY
X2	Project Rename & A7102CH 1C	SEP18	IAIN GALLOWAY
B	Reference Designator are sync with NXP_NXPhlite 3.0RC18	23OCT18	IAIN GALLOWAY
BX1		11NOV18	IAIN GALLOWAY
BX2			IAIN GALLOWAY
BX3			IAIN GALLOWAY
BX4			IAIN GALLOWAY
BX5	-Added J28 for external sensors -Added FX4FMU baro sensor	14NOV18	IAIN GALLOWAY
BX6			IAIN GALLOWAY
BX7		23NOV18	IAIN GALLOWAY
BX8	-Added ESD diodes	24NOV18	IAIN GALLOWAY
BX9	- Added MS621FE-FL11E BATTERY - BMM150 SPI to I2C mode	28NOV18	IAIN GALLOWAY
BX10	- Removed duplicate pullup / down resistors from I2C1_SDA_INTERNAL & I2C1_SCL_INTERNAL - On-Board sensors moved to 'SENSORS' page	03DEC18	IAIN GALLOWAY
C	Diagram added/A085 release	18DEC18	IAIN GALLOWAY
C1	BOM updated	21JAN19	IAIN GALLOWAY
CX1	NTB0104GU12 OE pin pull up./ D50 to DNP /J4.1 to GND / Obsolete components updated.	21JAN19	IAIN GALLOWAY
D	Release A085	30 JUL 19	IAIN GALLOWAY

# RDDDRONE-FMUK66

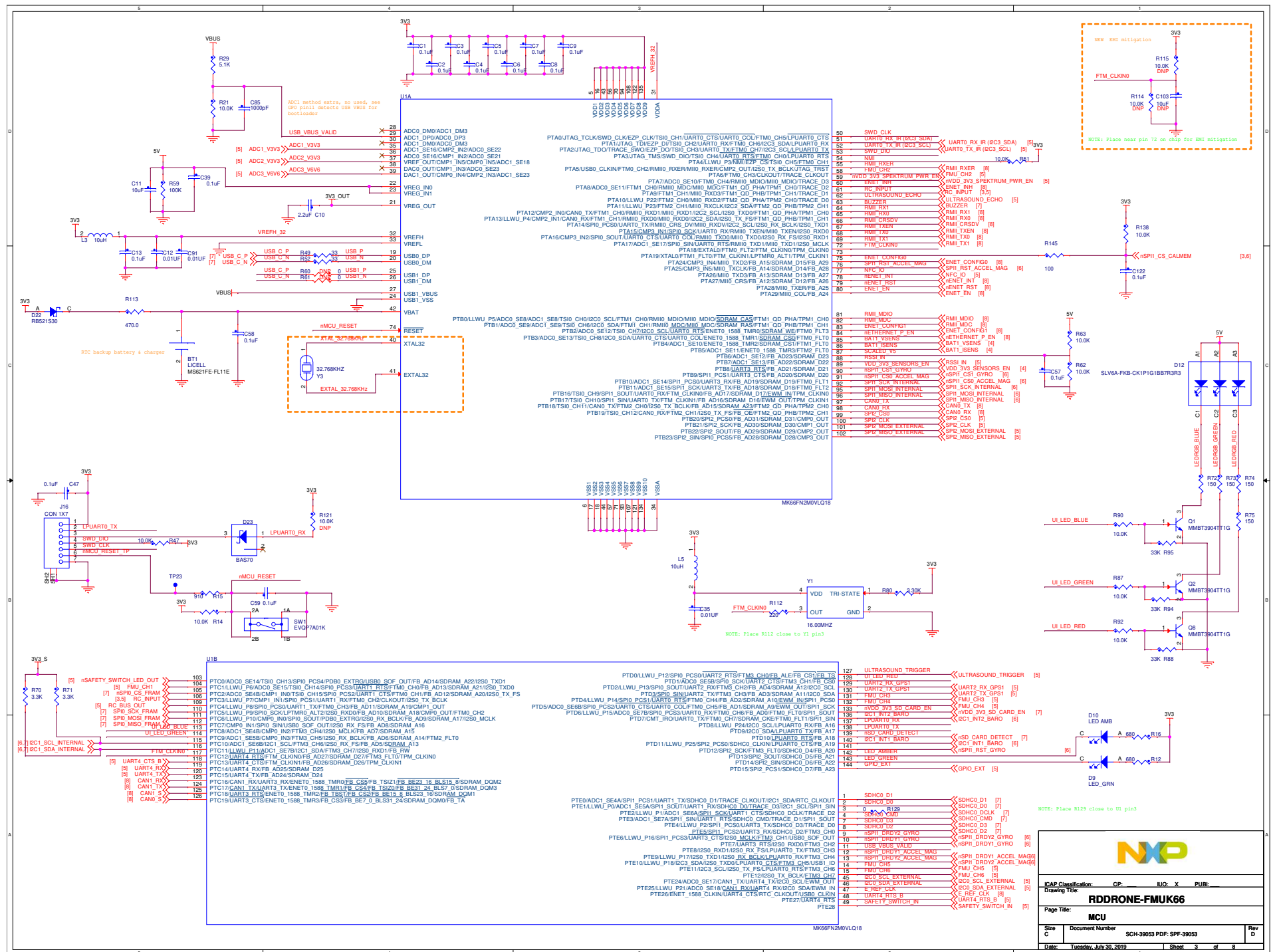
			
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Drawing Title: RDDDRONE-FMUK66			
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REF DES	ASSY OPT	PAGE NAME
R61,R60,R114,R115,C103,R121	DNP	3. MCU
R77	DNP	4. POWER
R45,R128	DNP	5. CONNECTORS
R65,R132,R134,R64,R133,R131	DNP	6. SENSORS
R111,C108,R120	DNP	7. SDCARD & MEMORY

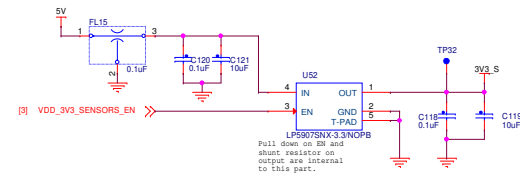
## RDDRONE-FMUK66 MODULAR AND FLEXIBLE DRONE DEVELOPMENT PLATFORM



<b>NXP</b>			
RAP Classification: CP: BUC: X PUB:			
Drawing Title: <b>RDDRONE-FMUK66</b>			
Page Title: <b>BLOCK DIAGRAM</b>			
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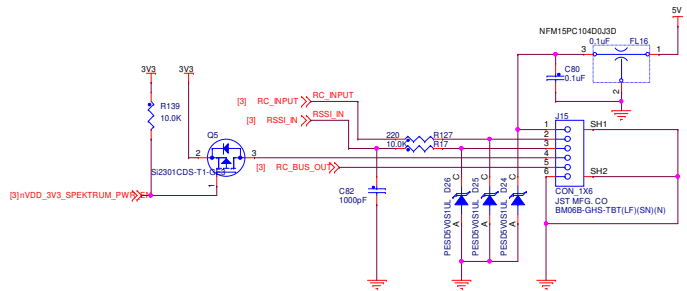


NOTE: Extra Low Noise

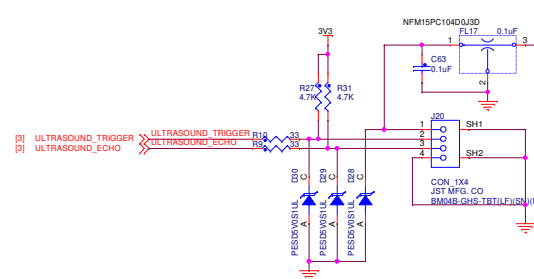


ICAP Classification:		CP:	I/O: X	PUB:
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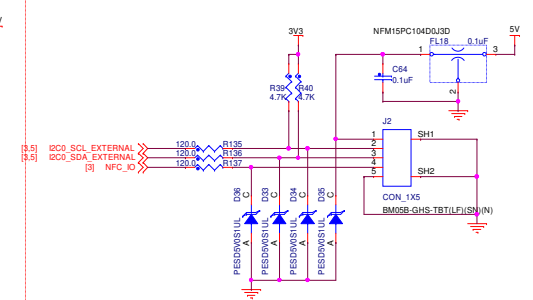
# PPM-RSSI-SBUS-SPEKTRUM SERIAL4/FrSky



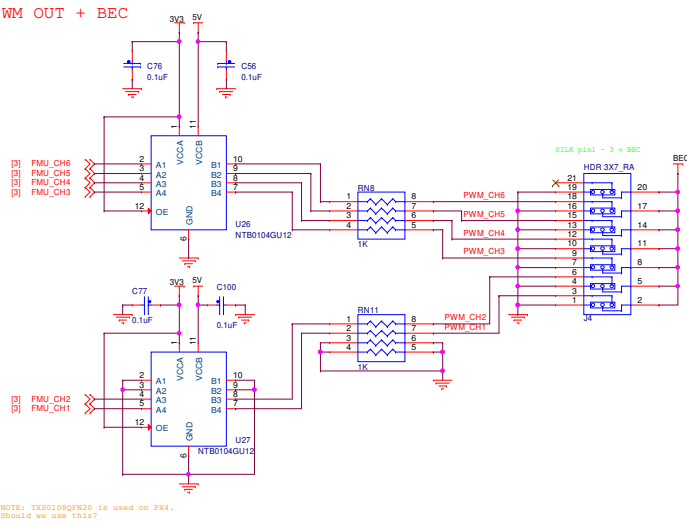
# Ultrasonic Sensor



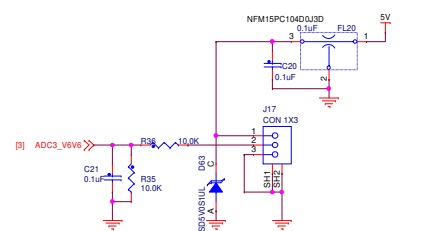
# I2C0. Also for NTAG+ NFC Tag or NFC Controller



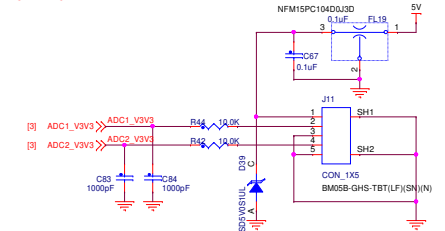
# PWM OUT + BEC



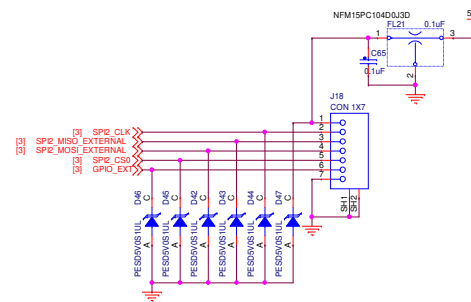
# ADC3 PORT



# ADC1 PORT

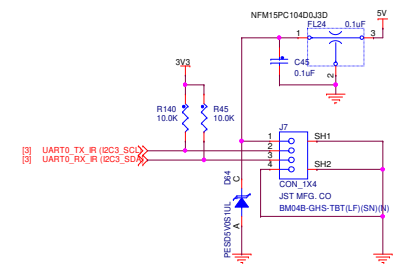


# SPI EXTERNAL



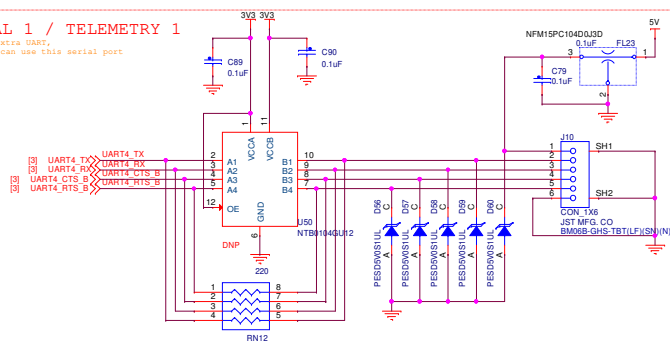
# SERIAL 2 / TELEMETRY 2 / IRDA

NOTE: UART0, Capable of I2C 12 Comm. 12 TX/RX.  
Alternate function I2C3



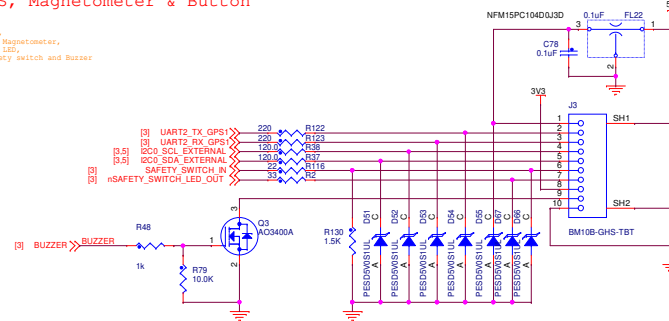
# SERIAL 1 / TELEMETRY 1

NOTE: No extra UART,  
Bluetooth can use this serial port



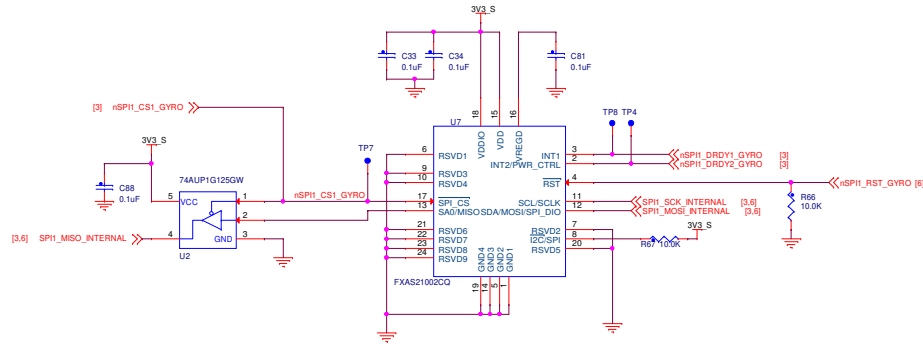
# GPS, Magnetometer & Button

- GPS,
- Bar Magnetometer,
- USB LED,
- Safety switch and Buzzer

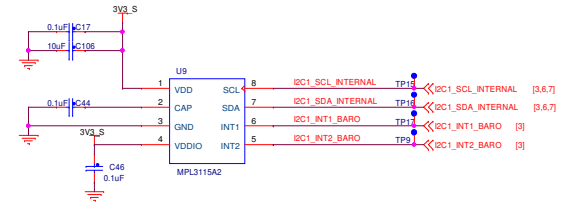


<b>PAP Classification:</b>		CP:	BU: X PUB:
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<b>ADDRONE-FMUK66</b>			
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<b>CONNECTORS</b>			
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## GYROSCOPE

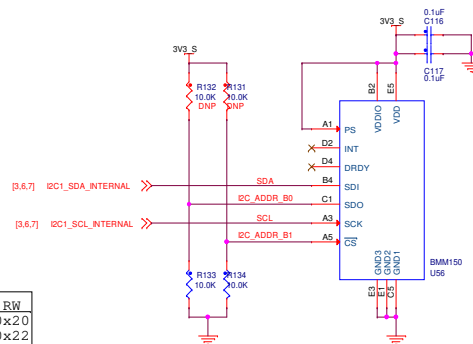


## PRESSURE SENSOR



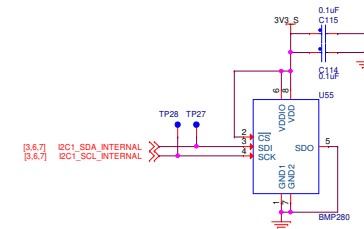
I2C	RW
110 0 000n	0x70 0xC0

## MAGNETOMETER BMM150



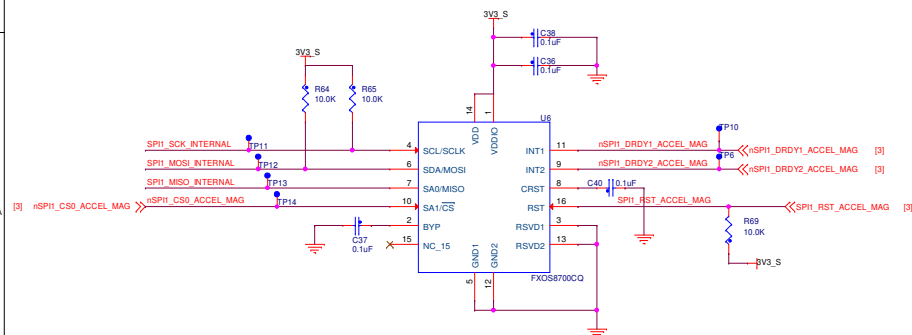
B1/B0	001	0	0Xn	I2C Addr	RW
L L	001	0	000n	0x10	0x20
L H	001	0	001n	0x11	0x22
H L	001	0	010n	0x12	0x24
H H	001	0	011n	0x13	0x26

## BAROMETER BMP280

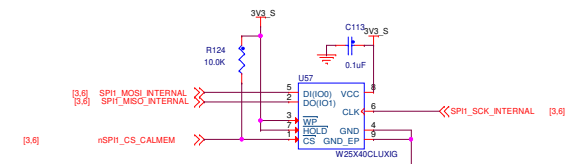


SDO	111	0	11Xn	I2C	RW
L	111	0	110n	0x76	0xEC
H	111	0	111n	0x77	0xEE

## ACCELEROMETER & MAGNETOMETER



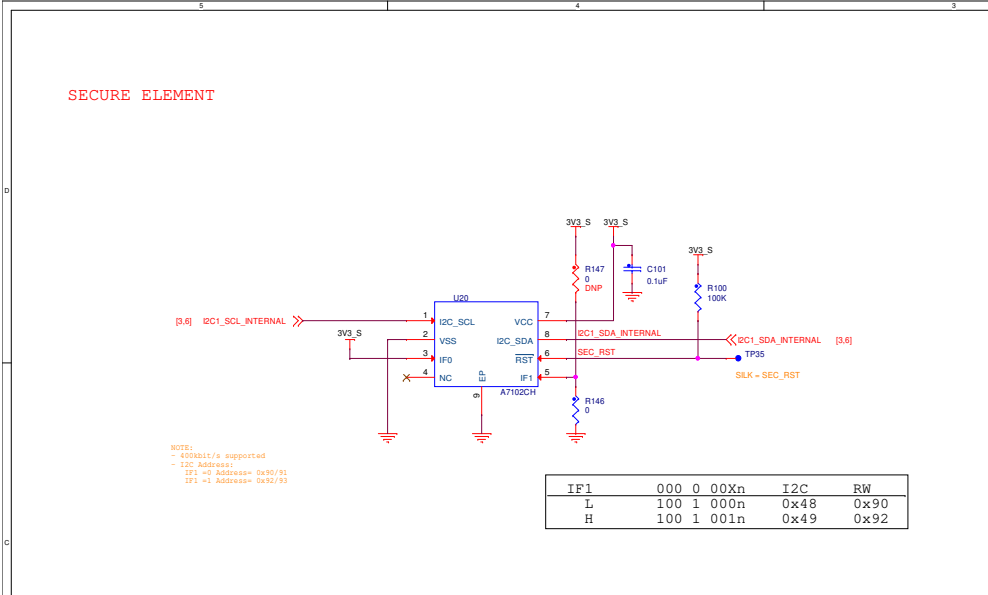
## SENSOR CALIBRATION FLASH



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NOTE:  
 - 400kbit/s supported  
 - I2C Address:  
   IF1 => Address= 0x90/91  
   IF1 !=1 Address= 0x92/93

	IF1	000	0	00Xn	I2C	RW
L	100	1	000n	0x48	0x90	
H	100	1	001n	0x49	0x92	



## SECURE ELEMENT

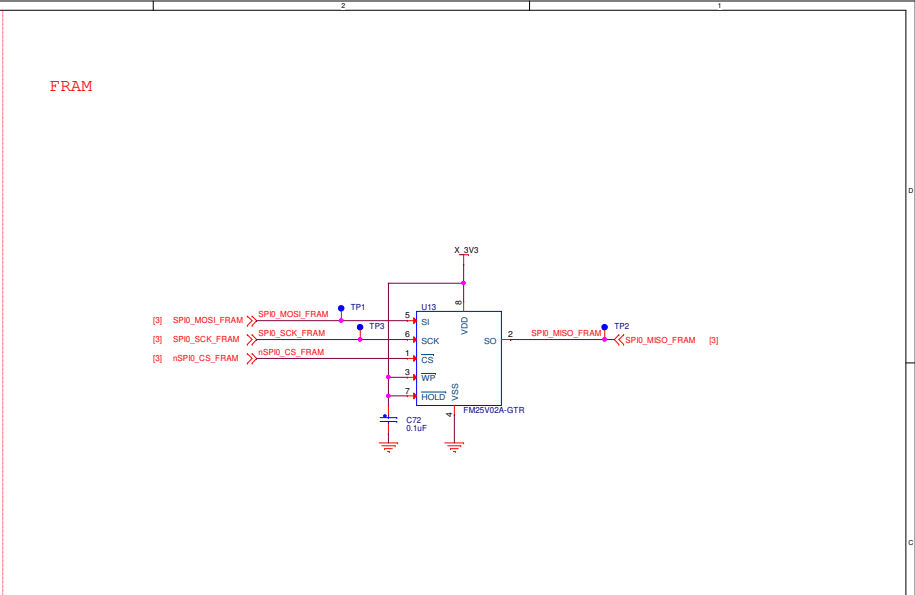
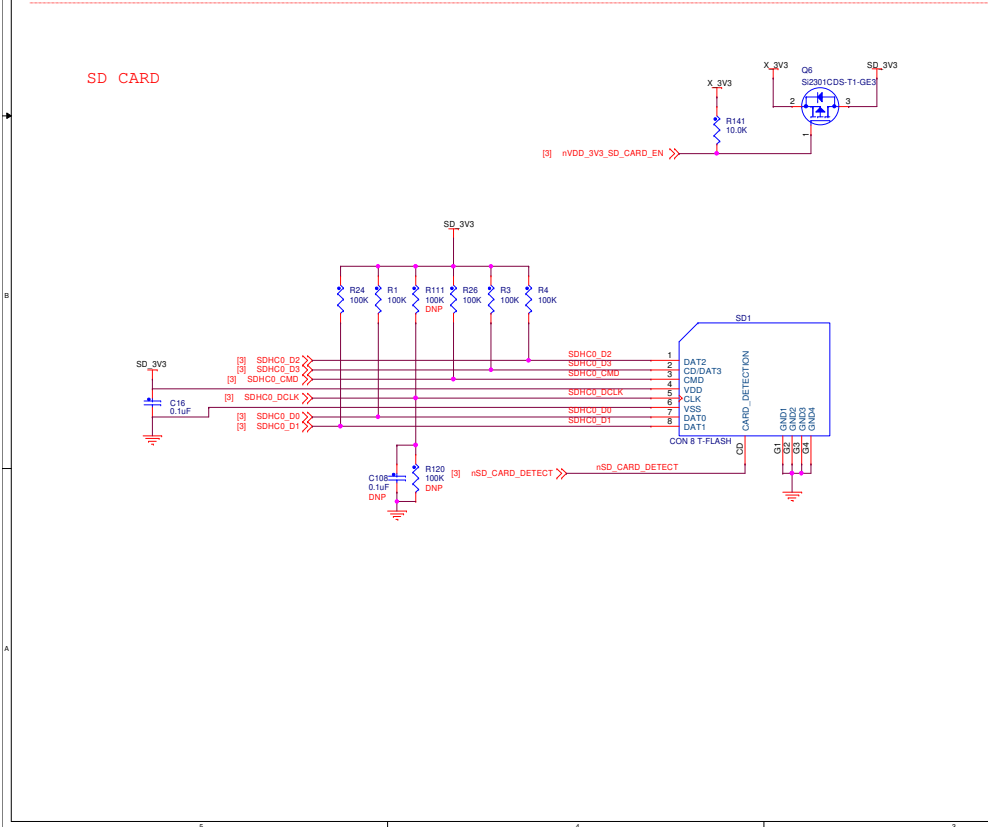
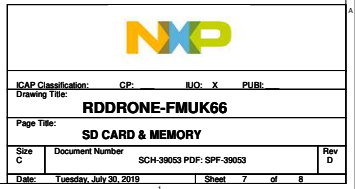
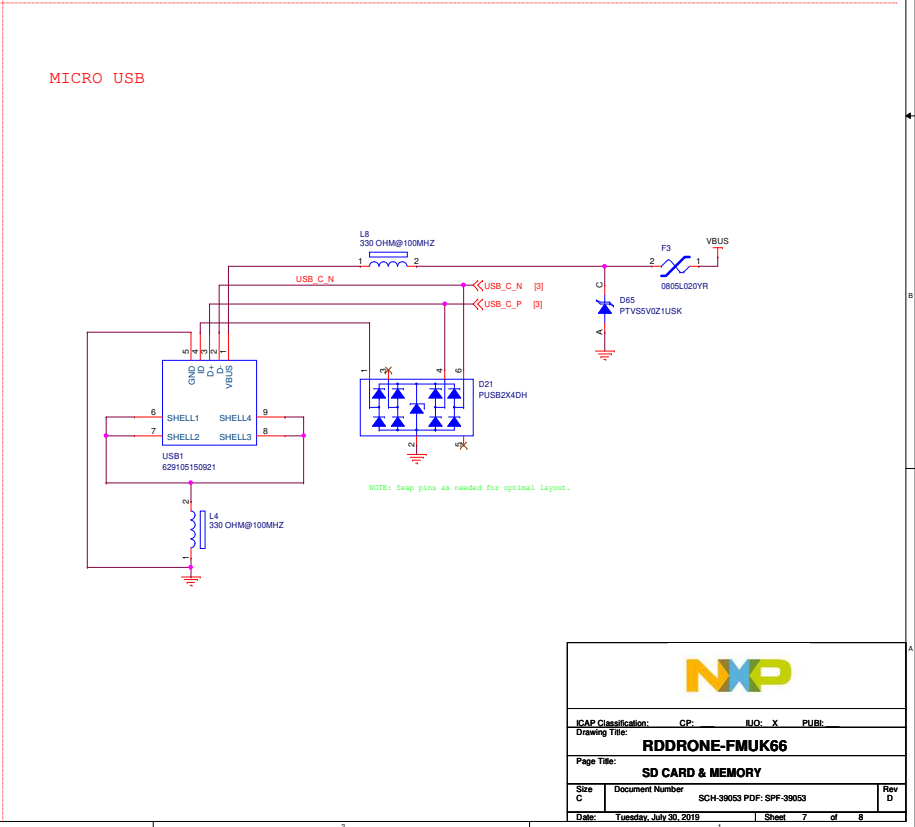
NOTE:  
 - 400kbit/s supported  
 - I2C Address:  
 IF1 => Address= 0x90/91  
 IF1 !=1 Address= 0x92/93


	IF1	000	0	00Xn	I2C	RW
L	100	1	000n	0x48	0x90	
H	100	1	001n	0x49	0x92	


The schematic shows a 1-wire I2C to SPI bridge (U13) connected to a 1-wire I2C master (U12) and an SPI slave (U11). The bridge (U13) has the following connections:


- Pin 5 (SI):** Connected to the I2C master's SDA pin (U12 pin 1).
- Pin 6 (SCK):** Connected to the I2C master's SCL pin (U12 pin 2).
- Pin 1 (CS):** Connected to the I2C master's CS pin (U12 pin 3).
- Pin 3 (WP):** Connected to the I2C master's WP pin (U12 pin 4).
- Pin 7 (HOLD):** Connected to the I2C master's HOLD pin (U12 pin 5).
- Pin 2 (SO):** Connected to the SPI slave's MOSI pin (U11 pin 1).
- Pin 4 (VDD):** Connected to the X\_3V3 supply.
- Pin 8 (SS):** Connected to the SPI slave's CS pin (U11 pin 2).


The bridge (U13) is also connected to a 0.1uF capacitor (C72) to ground. The SPI slave (U11) is connected to the X\_3V3 supply and ground. The I2C master (U12) is connected to the X\_3V3 supply and ground.


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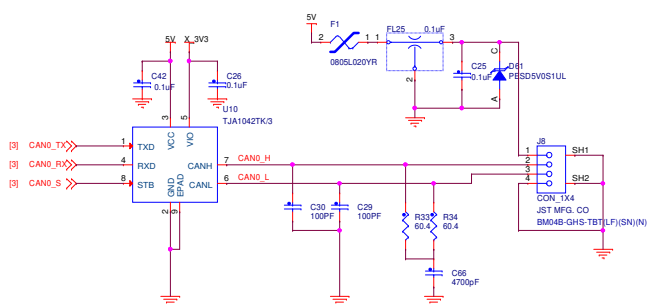
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## CAN\_0\_TRANSCIEVER



The diagram shows a CAN bus interface circuit. A 5V regulator (F2) provides power to a CAN transceiver (TJA1042TK3) and a microcontroller (STM32F103C8T6). The transceiver is connected to the CAN bus lines (CAN\_H, CAN\_L) and the microcontroller's CAN pins (TXD, RXD, STB). The microcontroller is also connected to a CAN module (J19) via a CAN bus. The CAN module is connected to a CAN bus (CAN\_H, CAN\_L) and a CAN transceiver (TJA1042TK3). The CAN module is also connected to a CAN bus (CAN\_H, CAN\_L) and a CAN transceiver (TJA1042TK3).