



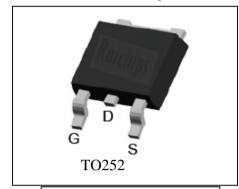
Features

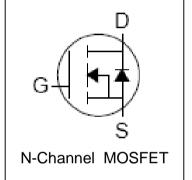
- 75V/70A, RDS (ON) = 9m (Typ.) @ VGS=10V
- Super High Dense Cell Design
- Fast Switching and Fully Avalanche Rated
- Reliable and Rugged
- 100% avalanche tested
- Lead Free and Green Devices Available (RoHS Compliant)

Applications

• High Speed Power Switching

Pin Description





Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit					
Common Ratings (T _c =25°C Unless Otherwise Noted)								
V_{DSS}	Drain-Source Voltage	75	N /					
V_{GSS}	Gate-Source Voltage	±25	V					
T_J	Maximum Junction Temperature		175	°C				
T _{STG}	Storage Temperature Range	-55 to 175	°C					
Is	Diode Continuous Forward Current	T _C =25°C	70 ^②	А				
Mounted on Lar	ge Heat Sink							
I _{DP}	300µs Pulse Drain Current Tested	T _C =25°C	280	Α				
I _D	L Continuos Proin Community 4000		70 ^②	Α				
10	Continuous Drain Current(V _{GS} =10V)	T _C =100°C	51	^				
P _D	D		103	10/				
r _D	Maximum Power Dissipation	T _C =100°C	52	W				
R _{eJC}	Thermal Resistance-Junction to Case	1.45	°C/W					
Drain-Source Av	valanche Ratings							
E _{AS}	Avalanche Energy, Single Pulsed		169	mJ				



Electrical Characteristics (T_c=25°C Unless Otherwise Noted)

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Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit	
Static Cha	aracteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	75			V	
	V _{DS} = 75V, V _{GS} =0V				1	μΑ	
I _{DSS}	Zero Gate voltage Drain Current	Gate Voltage Drain Current T _J =85°C			30		
V _{GS(th)}	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	2	3	4	V	
I _{GSS}	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$			±100	nA	
R _{DS(ON)}	Drain-Source On-state Resistance	V _{GS} = 10V, I _{DS} =35A		9	12	mΩ	
Diode Cha	aracteristics						
V _{SD}	Diode Forward Voltage	I _{SD} =35A, V _{GS} =0V			1.2	V	
trr	Reverse Recovery Time	Isb=35A, dlsb/dt=100A/μs		38		ns	
Qrr	Reverse Recovery Charge	-15D-33A, α15D/α1-100A/μ3		45		nC	
Dvnamic	Characteristics 5						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$		2.0		Ω	
C _{iss}	Input Capacitance	Vgs=0V,		2850		pF	
C _{oss}	Output Capacitance	V _{DS} =40V,		320			
C_{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz		150			
t _{d(ON)}	Turn-on Delay Time			27			
t _r	Turn-on Rise Time	Vdd=40V, Rl=1.2Ω, Ids=35A, Vgen= 10V,		32		ns	
$t_{\text{d(OFF)}}$	Turn-off Delay Time	RG= 4.7Ω		93			
t_f	Turn-off Fall Time			68			
Gate Cha	rge Characteristics 5						
Q_g	Total Gate Charge			68			
Q_{gs}	Gate-Source Charge	Vps=60V, Vgs= 10V, lps=35A		12		nC	
Q_{gd}	Gate-Drain Charge			23			

Notes: ①Pulse width limited by safe operating area.

②Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 60A.

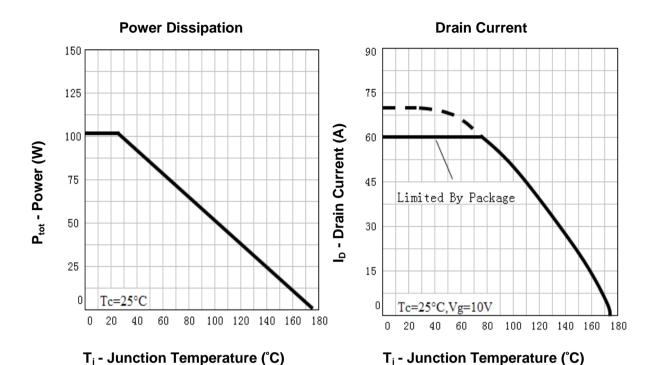
 $^{\ \ \, \}mbox{3}\mbox{Limited by T_{Jmax}, I_{AS} =26A, V_{DD} = 48V, R_G = 50 Ω , Starting T_J = 25°C. }$

⁽⁴⁾ Pulse test; Pulse width≤300µs, duty cycle≤2%.

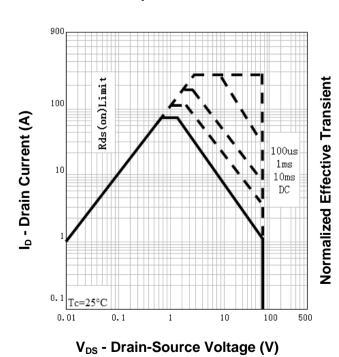
⑤Guaranteed by design, not subject to production testing.



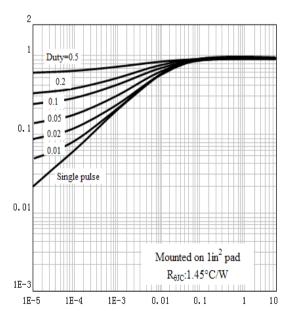
Typical Characteristics



Safe Operation Area



Thermal Transient Impedance

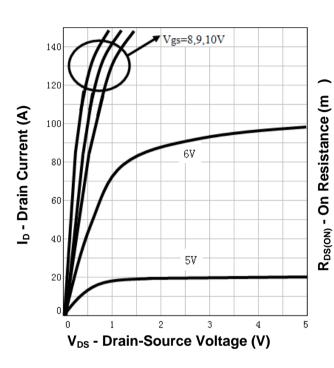


Square Wave Pulse Duration (sec)

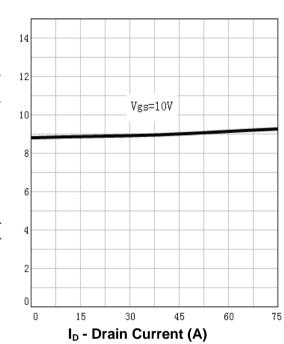


Typical Characteristics

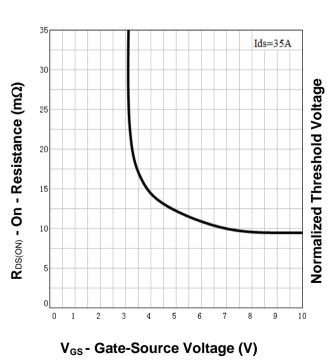
Output Characteristics



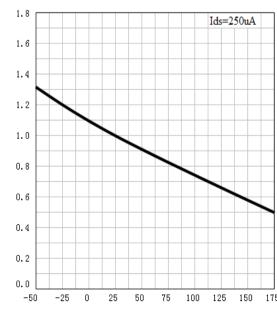
Drain-Source On Resistance



Drain-Source On Resistance



Gate Threshold Voltage

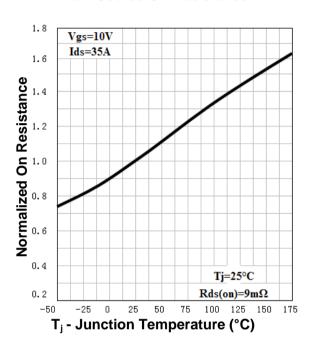


T_j - Junction Temperature (°C)

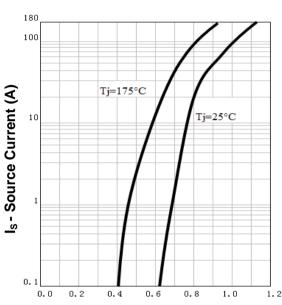


Typical Characteristics

Drain-Source On Resistance

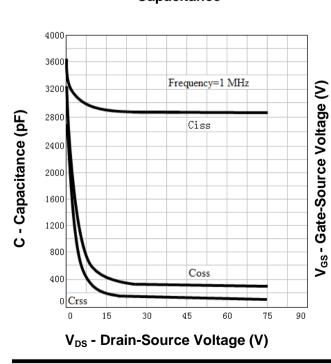


Source-Drain Diode Forward

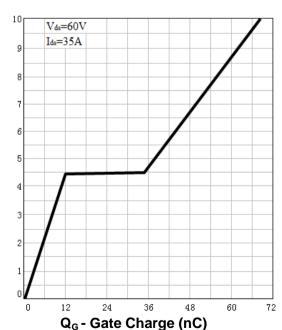


V_{SD} - Source-Drain Voltage (V)

Capacitance

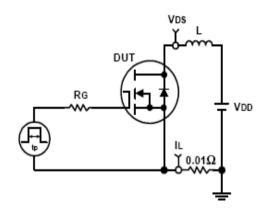


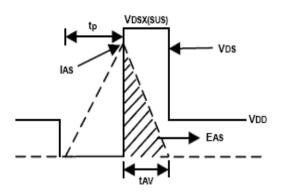
Gate Charge



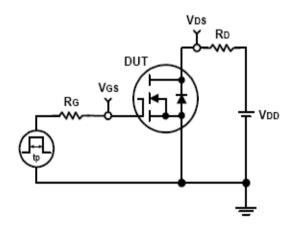


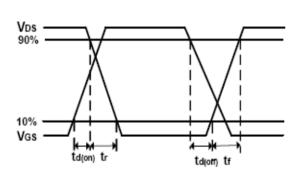
Avalanche Test Circuit and Waveforms





Switching Time Test Circuit and Waveforms







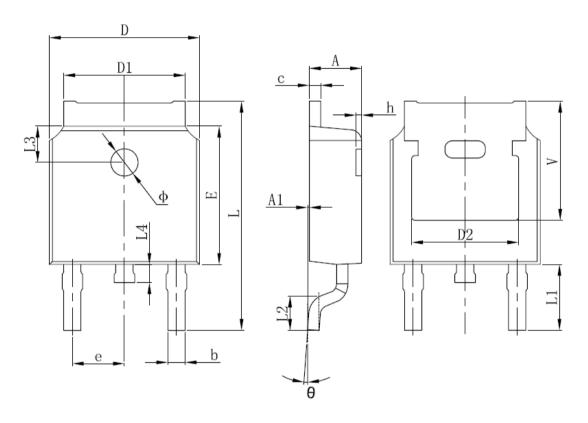
Ordering and Marking Information

Device Marking		Package Packaging		Quantity	Reel Size	Tape width	
RU7570L	RU7570L	TO-252	Tape&Reel	2500	13"	16mm	



Package Information

TO252-2L



SYMBOL	MM		INCH		an mor	MM		INCH	
SIMBOL	MIN	MAX	MIN	MAX	SYMBOL	MIN	MAX	MIN	MAX
A	2.200	2.400	0.087	0.094	L	9.800	10.400	0.386	0.409
A1	0.000	0.127	0.000	0.005	L1	2.900 REF.		0.114 REF.	
b	0.660	0.860	0.026	0.034	L2	1.400	1.700	0.055	0.067
С	0.460	0.580	0.018	0.023	L3	1.600 REF.		0.063REF.	
D	6.500	6.700	0.256	0.264	L4	0.600	1.000	0.024	0.039
D1	5.100	5.460	0.201	0.215	Φ	1.100	1.300	0.043	0.051
D2	4.830 REF.		0.190 REF.		θ	0°	8°	0°	8°
Е	6.000	6.200	0.236	0.244	h	0.000	0.300	0.000	0.012
e	2.186	2.386	0.086	0.094	V	5.350 REF.		0.211 REF.	

ALL DIMENSIONS REFER TO JEDEC STANDARD DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS



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