













delay("wfl v("vln" ?result "tran"), ...	
Expression	Value
delay("wfl v("vln" ?result "tran")	4.158E-12

ApplicationsPlacesSystem

[ajay - File Bro...][buet@cadence...][buet@cadence:...]virtuoso[not\_11\_tb]Virtuoso® Anal.../home/buet/sim...Virtuoso (R) Vis...Virtuoso (R) Vis...11:33 PM

Virtuoso® Analog Design Environment (1) - ajay not\_11\_tb schematic

LaunchSessionSetupAnalysesVariablesOutputsSimulationResultsToolsHelp

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Design Variables

Name	Value
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Analyses

Type	Enabled	Arguments
1 tran	<input checked="" type="checkbox"/>	0.400p moderate

Outputs

	Name/Signal/Expr	Value	Plot	Save	Save Options
1	vout		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
2	vin		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
3	tpdr	10.4148p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
4	tpdr	22.2403p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
5	((tpdr * tpdr) / 2)	16.3276p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	

> Results in /home/buet/simulation/not\_11\_tb/spectre/schematic

Plot after simulation: AutoPlotting mode: Replace

M:

Status: Ready | T=27 C | Simulator: spectre



















