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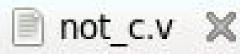








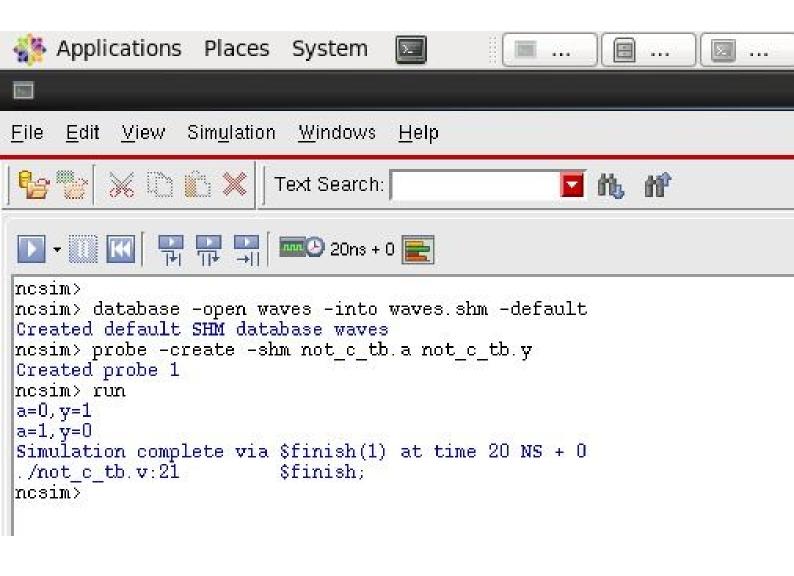


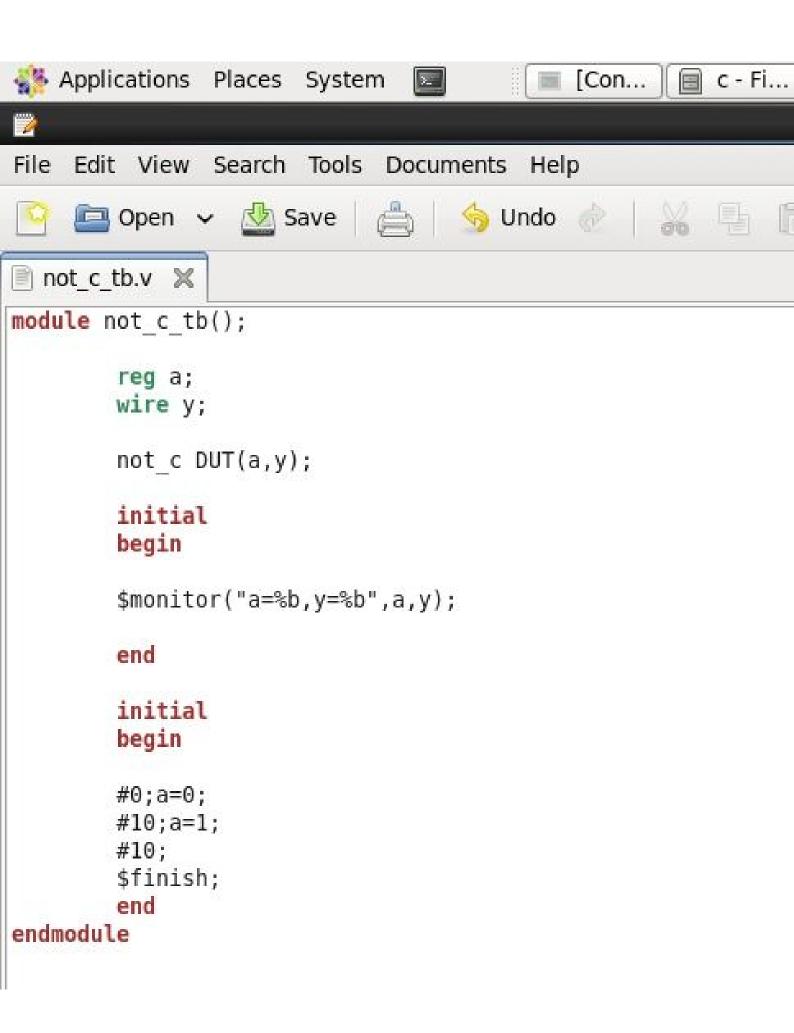


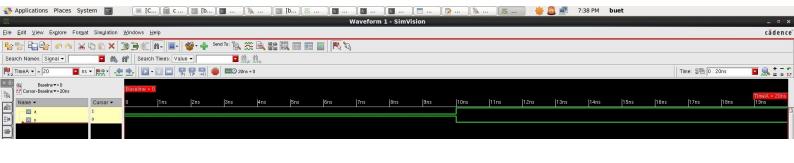
```
module not c(a,y);
```

```
input a;
output y;
supply1 vdd;
supplyθ vss;
```

```
pmos P1(y,vdd,a);
nmos N1(y, vss,a);
```

















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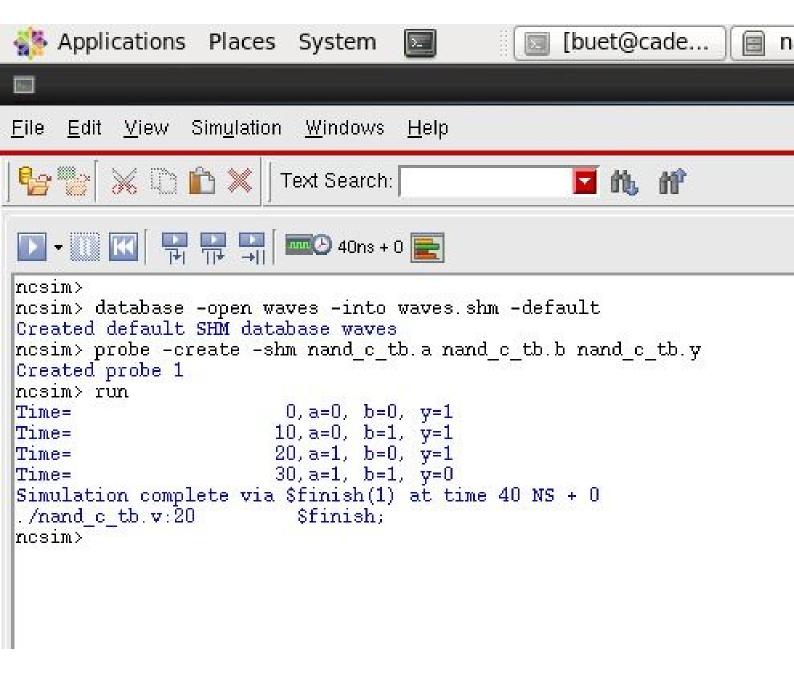


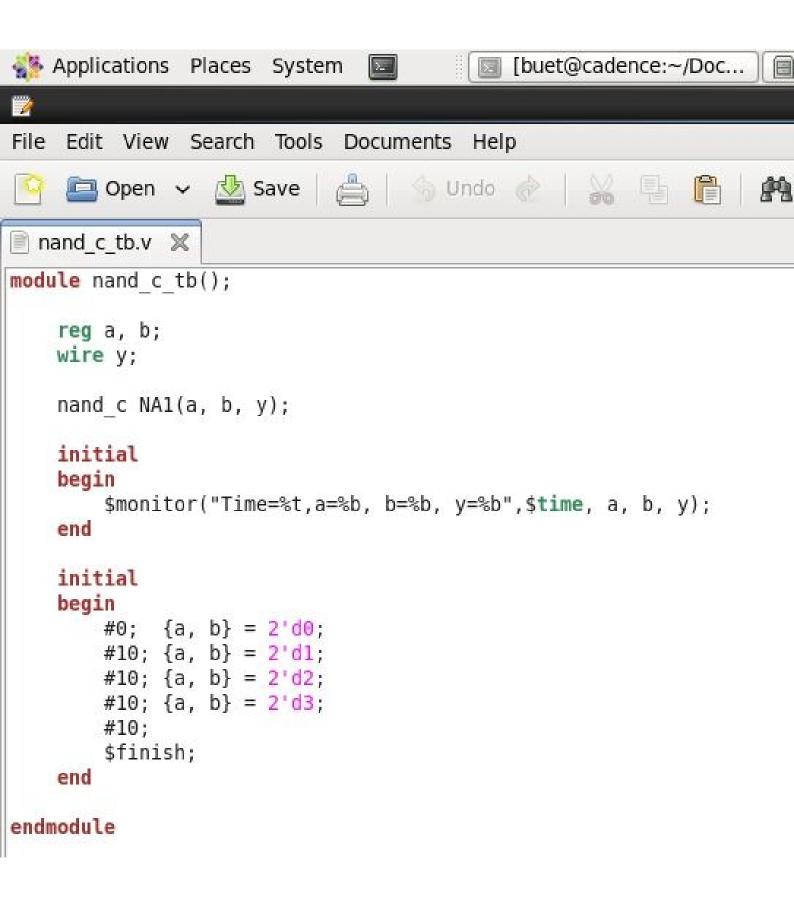


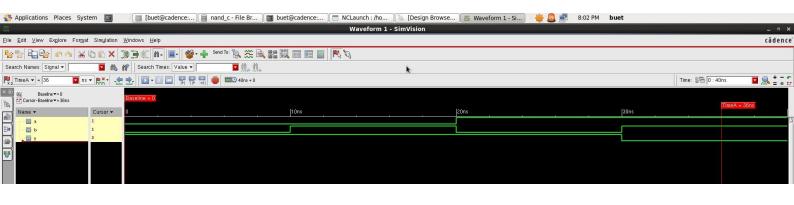
nand c.v 💥

```
module nand c(a,b,y);
```

```
input a,b;
output y;
supply1 vdd;
 supplyθ vss;
wire w;
pmos P1(y,vdd,a);
 pmos P2(y,vdd,b);
 nmos N1(w,vss,b);
 nmos N2(y,w,a);
```

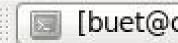














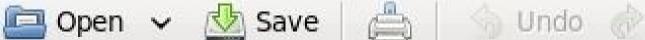
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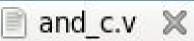




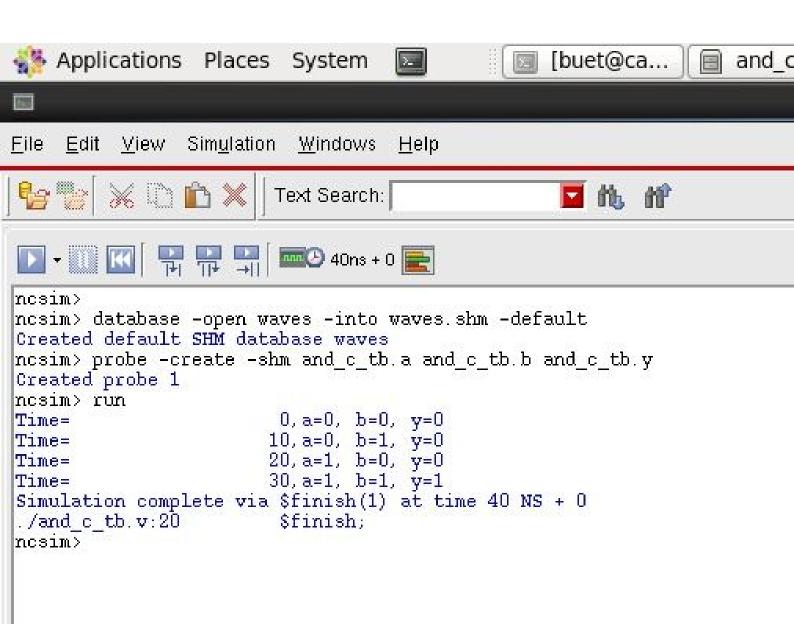


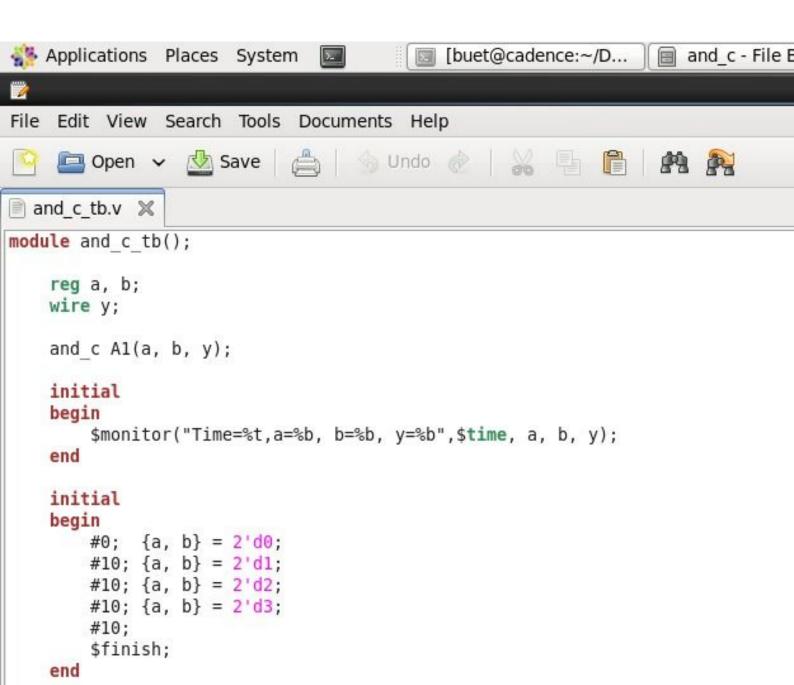


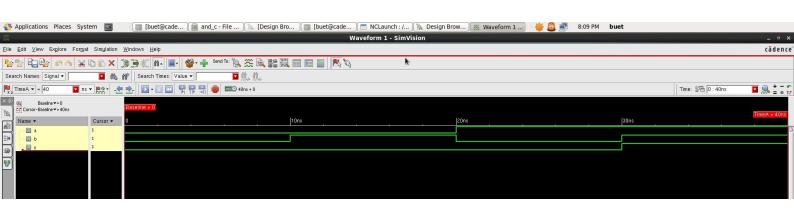


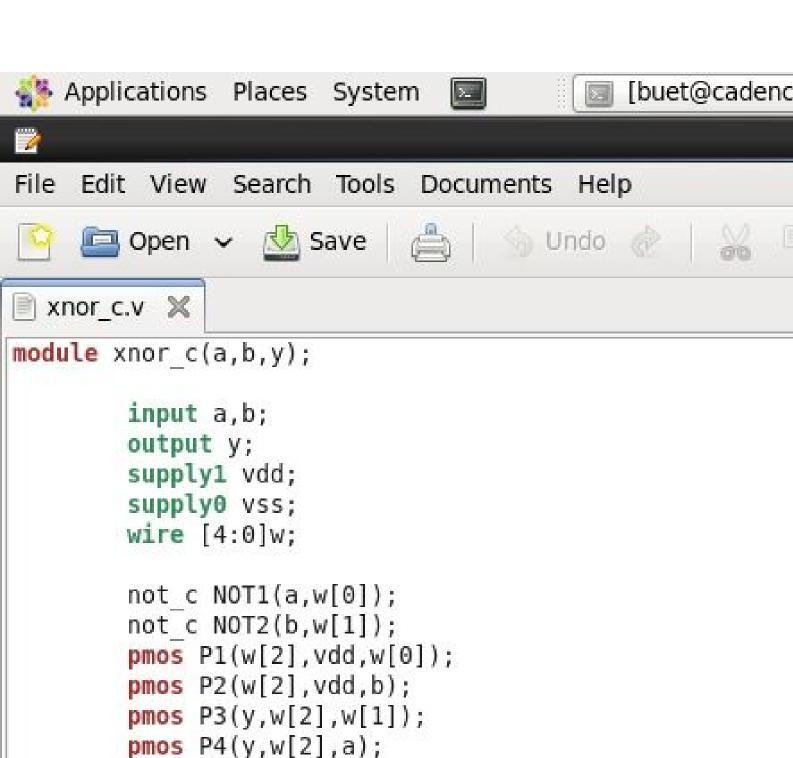


```
module and c(a,b,y);
        input a,b;
        output y;
        wire w;
        nand c NA1(a,b,w);
        not c NOT1(w,y);
```







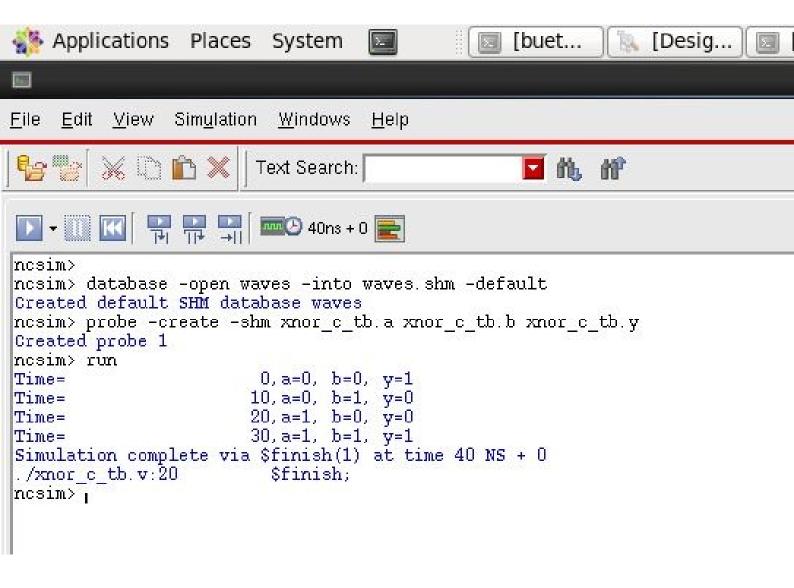


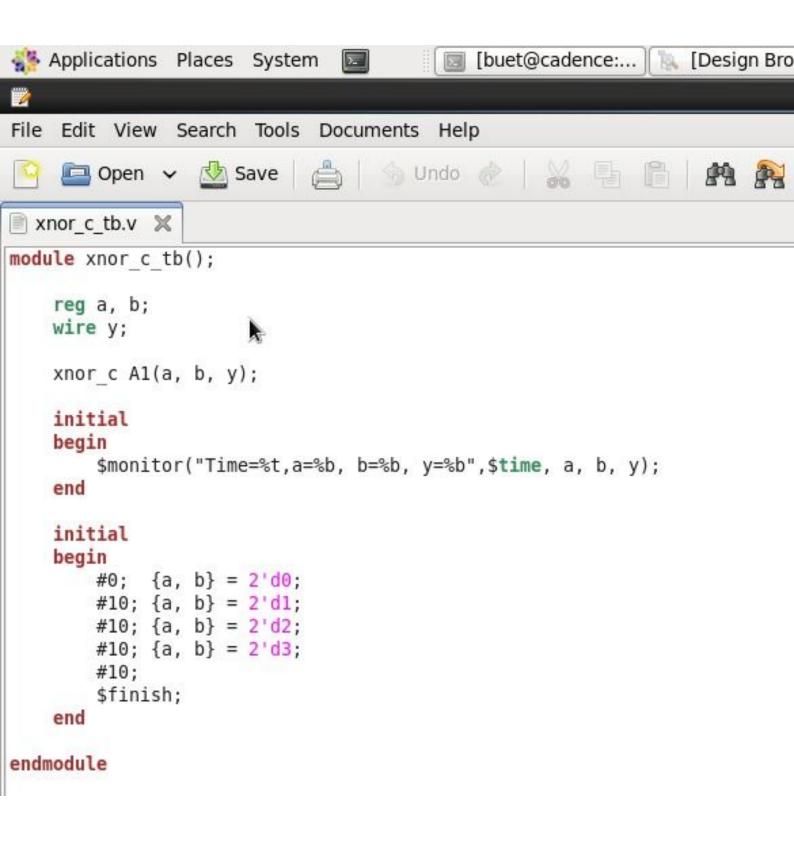
nmos N1(y,w[3],b);

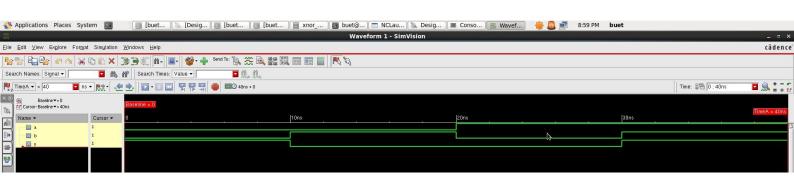
nmos N2(w[3], vss, w[0]);

nmos N3(y,w[4],w[1]);

nmos N4(w[4],vss,a);













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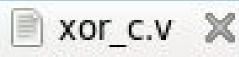
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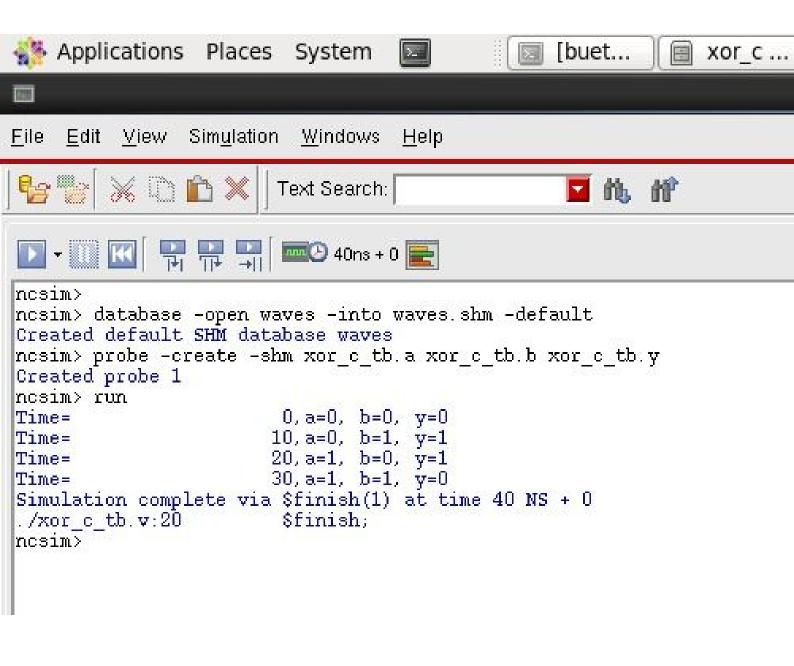


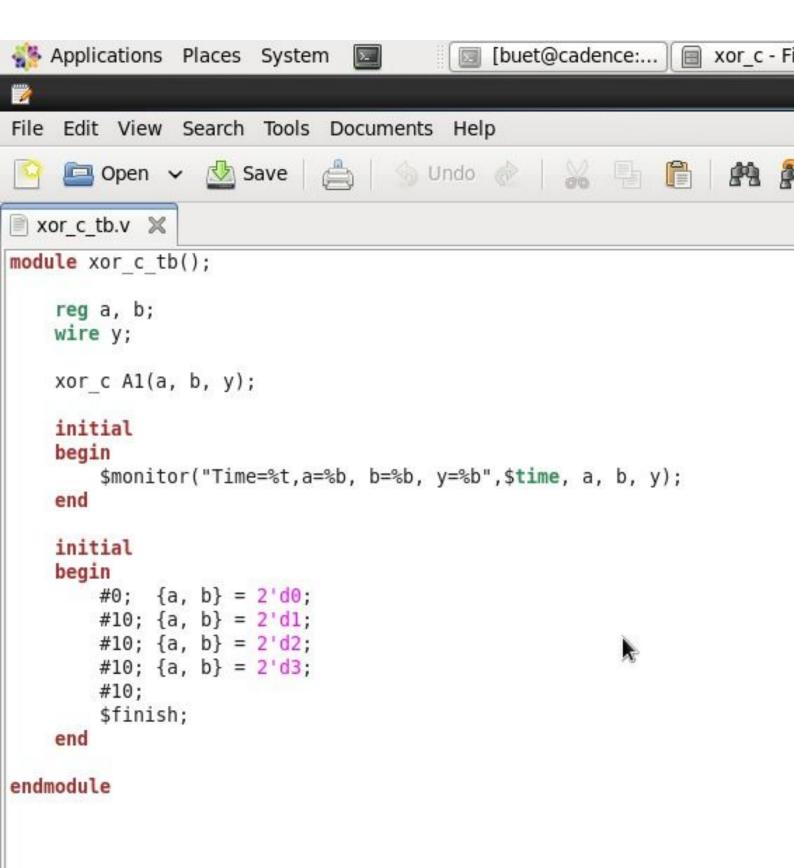
```
module xor c(a,b,y);
```

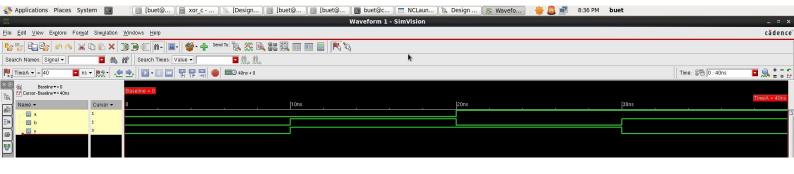
```
input a,b;
output y;
wire w;
```

```
xnor c XN1(a,b,w);
not c NOT1(w,y);
```

















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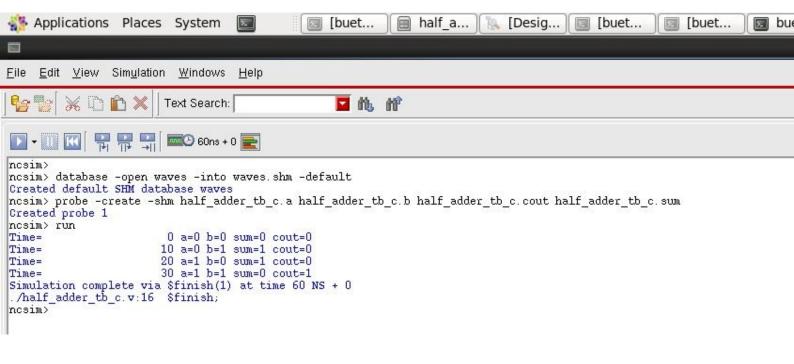


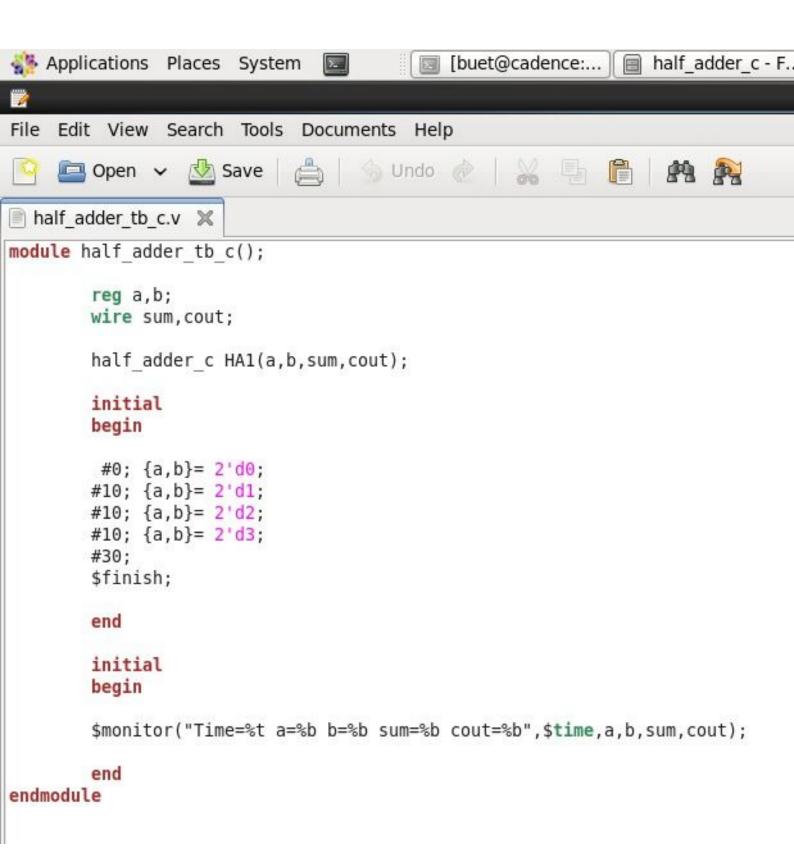
half_adder_c.v

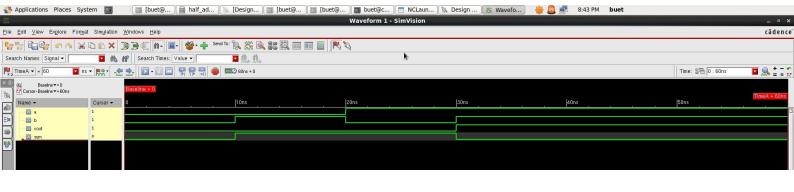
```
module half adder c(a,b,sum,cout);
```

```
input a,b;
output sum, cout;
```

```
xor c X1(a,b,sum);
and c A1(a,b,cout);
```















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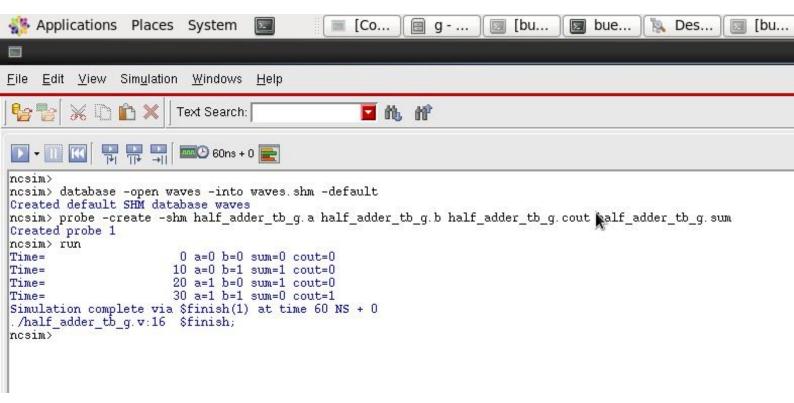


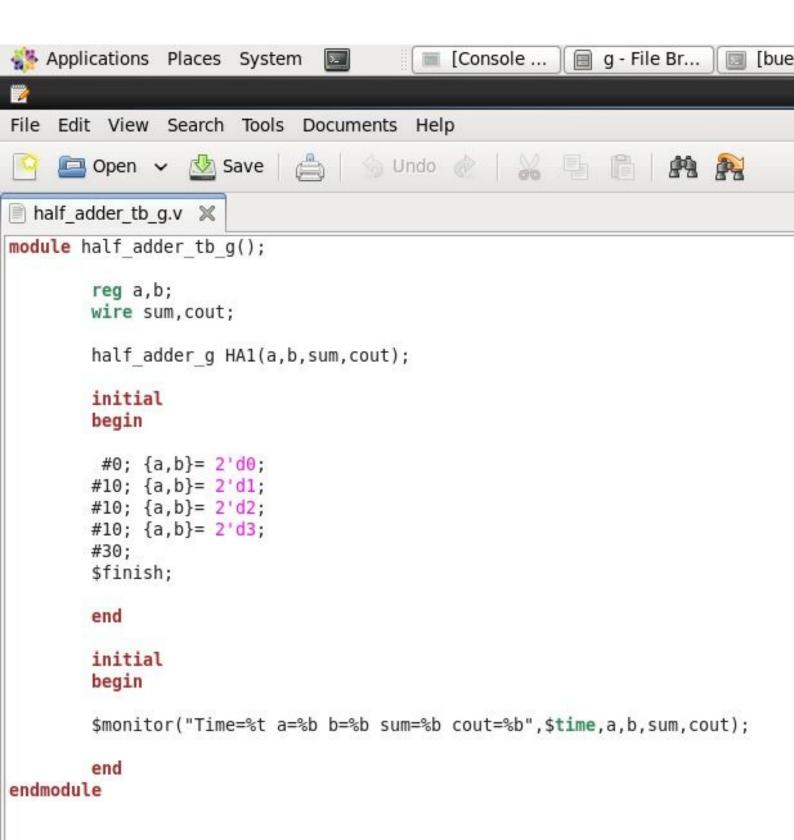
half adder g.v 💥

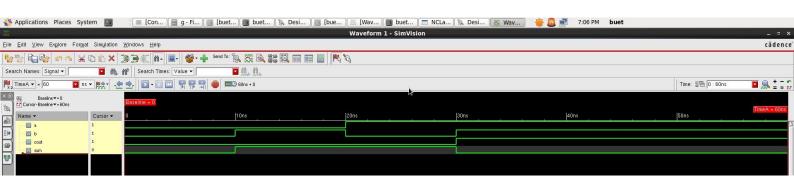
```
module half adder g(a,b,sum,cout);
```

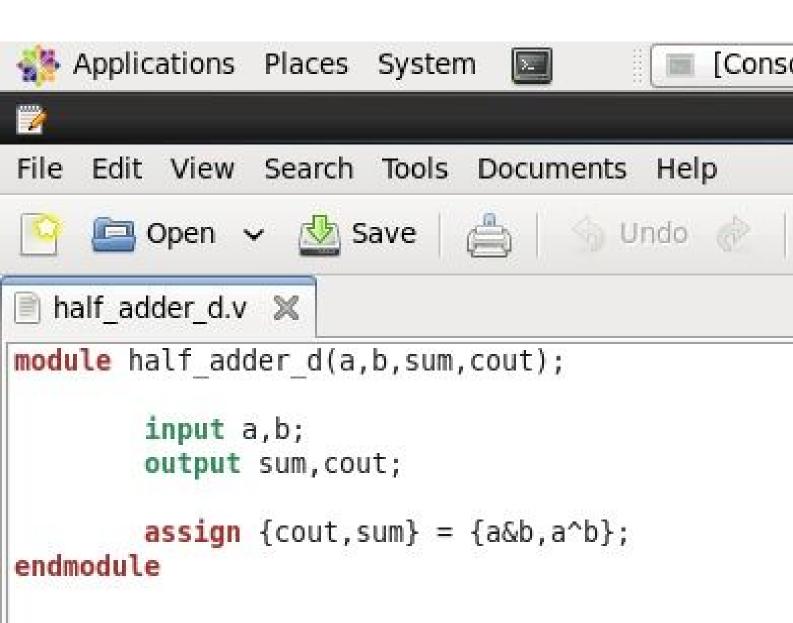
```
input a,b;
output sum, cout;
```

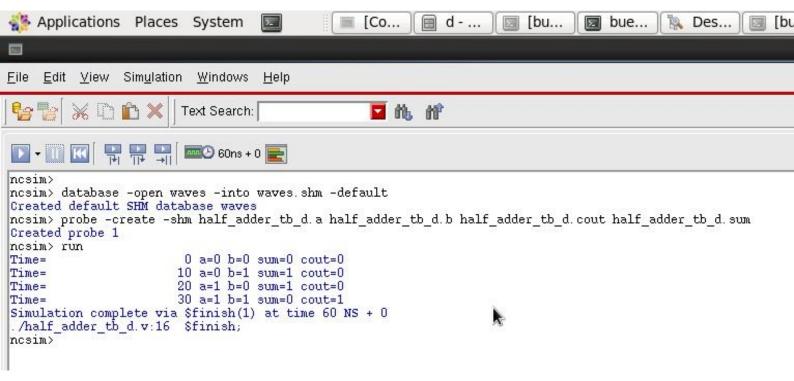
```
xor X1(sum,a,b);
and A1(cout,a,b);
```

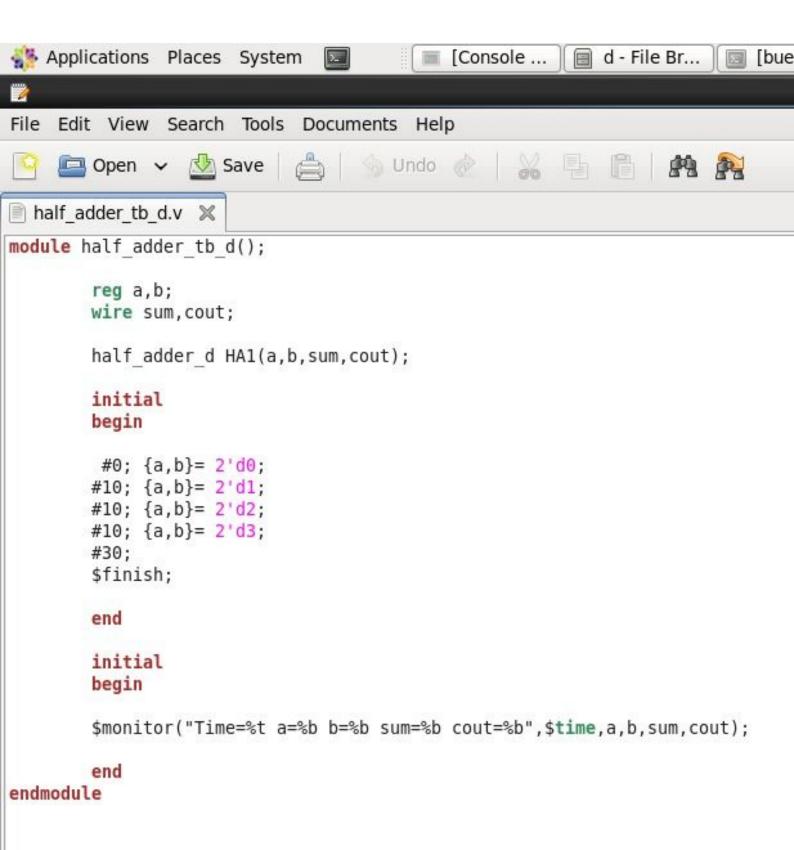


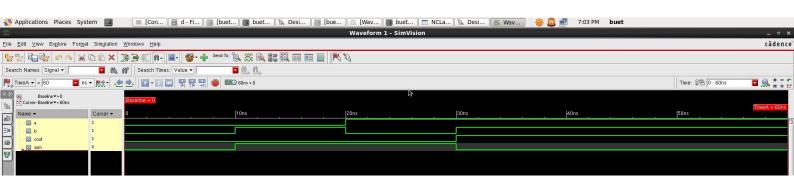


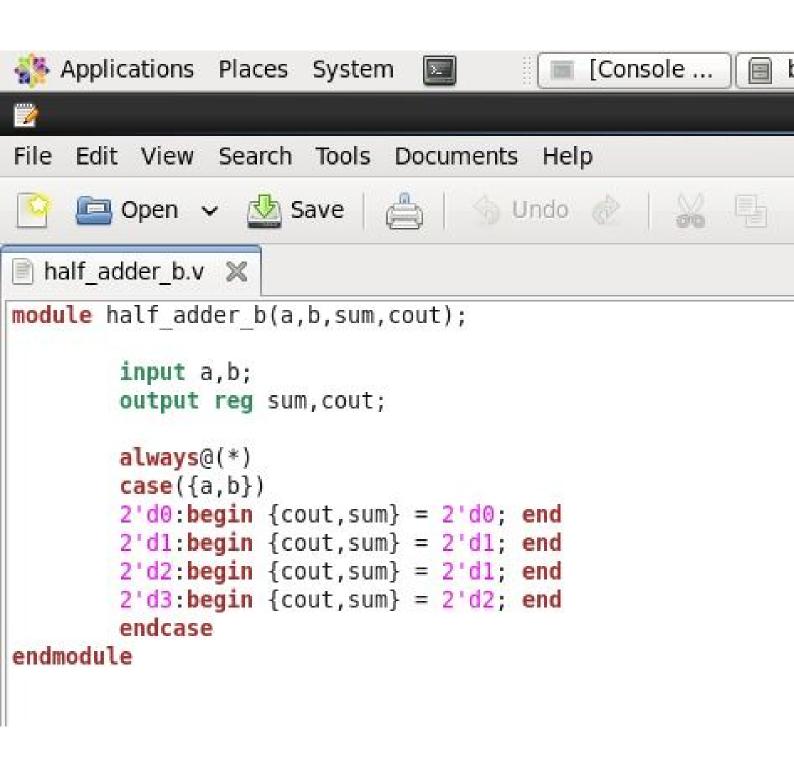


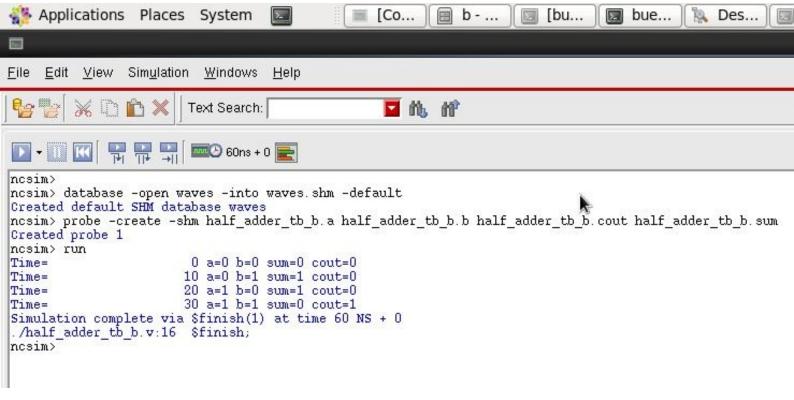


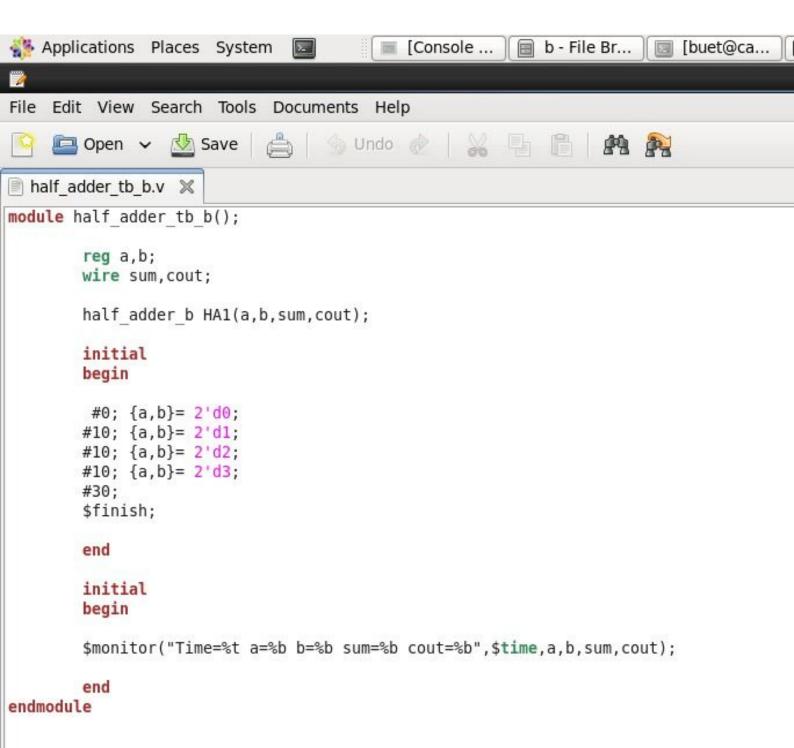


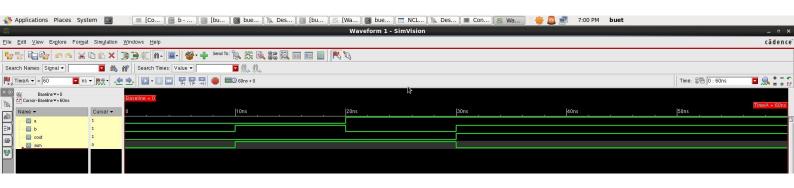




















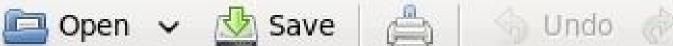
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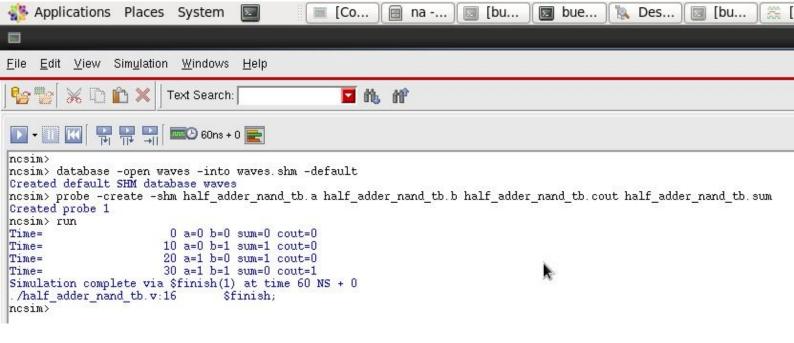


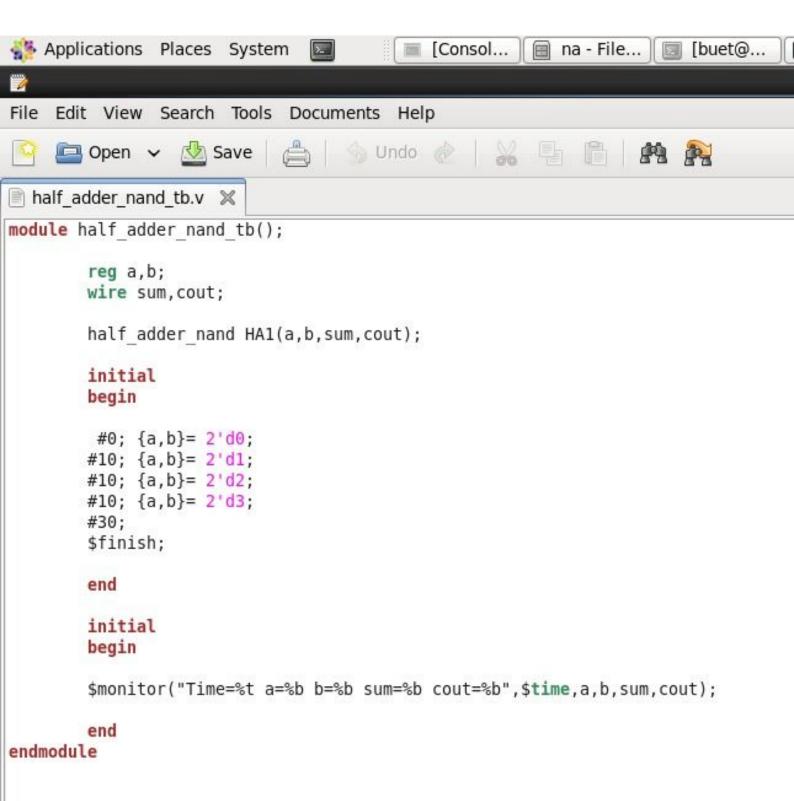


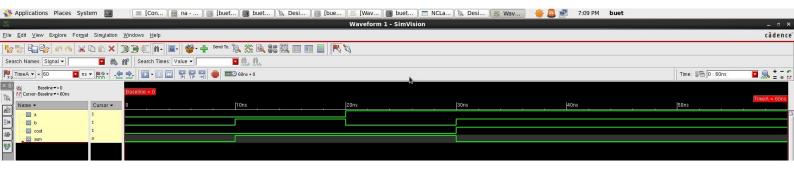
half_adder_nand.v 💥

```
module half adder nand(a,b,sum,cout);
```

```
input a,b;
        output sum, cout;
        wire [2:0]w;
        nand NA1(w[0],a,b);
        nand NA2(w[1],a,w[0]);
        nand NA3(w[2],b,w[0]);
        nand NA4(sum,w[2],w[1]);
        nand NA5(cout,w[0],w[0]);
endmodule
```















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half adder nor.v 💥



module half adder nor(a,b,sum,cout);

```
input a,b;
output sum, cout;
wire [3:0]w;
```

```
nor N01(w[0],a,a);
nor NO2(w[1],b,b);
nor N03(cout,w[0],w[1]);
nor N04(w[3],a,b);
nor NO5(sum, w[3], cout);
```

