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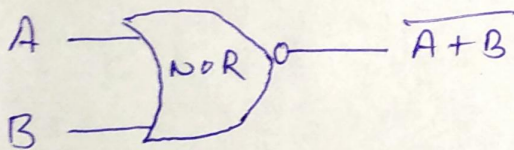
Subject: Computer
    Organization

Sec: B
Course: MCA (Ist sem)

(Ques 2). The NOR Gate is a digital gate that implements
Logical NOR- it behaves according to the truth
table to the right. A HIGH output (1) results
if both the inputs to the GATE are low (0);
if one or both input is HIGH (1), a LOW
Output (0) results.

(Truth table) :-

A ────┐
)NOR)o──── $\overline{A+B}$
B ────┘

A	B	Output
0	0	1
0	1	0
1	0	0
1	1	0

(Circuit diagram):-

C | +5v
$\gtrless$ R

A ——ww——|T₁

B ——ww——

T2

X