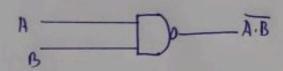
## Ans! Working of NAND gate .

A NAND gate is a logic gate that produces a low output (0) only if all its inputs are true, I ligh output (1) otherwise.

Hence, NAIVD gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.

NAND gates are known as universal gate.

The basic logical construction of the NAND gate is:



Here, in the NAND gate, the output is only logical O when donly when all inputs of the gote are 15, d in all other cases, the output of the NAND gate is light on 1.

Here, the truth table of a 1-input NAND gate:

A	13	X = A·B
0	0	1
0	1	1
1	0	t
1	1	0