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Ans) NAND gate is the combination of AND gate and NOT gate. If NAND gate is nothing but it is AND gate with an inverted output.

The logical statement of NAND is if input A is true and B is true then the output is false otherwise true.

Boolean equation of NAND gate is $Y = \overline{A \cdot B}$

Input		Output
A	B	$Y = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Diagram

