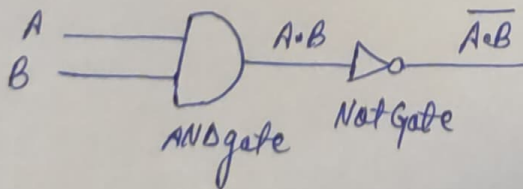


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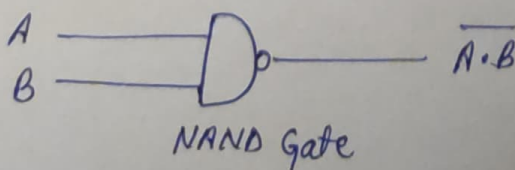
### Que 1: NAND Gate

- A NAND gate (not AND gate) is a logical gate that produces a low output (i.e 0) only if all its inputs are true, and high output (1) otherwise.
- NAND gate is the inverse of AND gate.
- Its circuit can be produced by connecting an AND gate to a NOT gate.
- It can have any number of inputs but only one output.

### Logical Construction of NAND gate



### Symbol of Nand Gate



The output of the NAND gate is opposite / reverse of that of AND gate.

## Truth Table

<u>Inputs</u>		<u>Output</u>
A	B	$X = A \cdot B$
0	0	1
0	1	1
1	0	1
1	1	0

## IC diagram

