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NAND

~~The gate has an~~

The NAND gate is a logic gate that performs the reverse operation of an AND logic gate. It is a blend of AND and NOT gates and is a commonly used logic gate.

Symbol



Truth table

$$Y = \overline{A \cdot B} = \bar{A} + \bar{B}$$

| Input | | output |
|-------|---|--------|
| 0 | 0 | 1 |
| 0 | 1 | 1 |
| 1 | 0 | 0 |
| 1 | 1 | 0 |

Circuit Diagram

