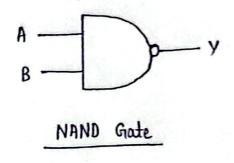
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The NAND gate is the combination of two basic logic gates. the AND gate and the NOT gate connected in series. The NAND gate and NOR gate can be called the universal gates.

The output of NAND gate is high when eighter of injents is high on if both the injuts are low. The logic NAND function is given by the boolean expression $y = \overline{A} \cdot B$. Here A, B are the inputs and Y some is the output.



Touth Table

A	В	у	-
0	0	1	
٥	1	1	
1	0	1	
1	1	0	

Circuit Diagram

