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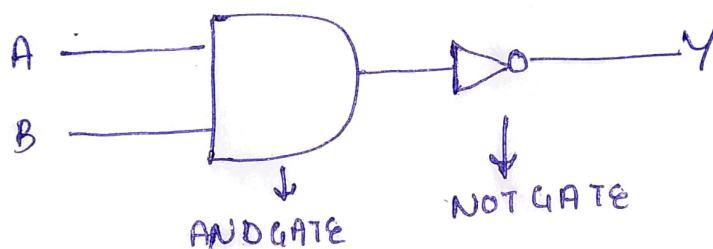
Subject - Computer Organization and Architecture (mid-term practical exam)

Ans 1. NAND GATE

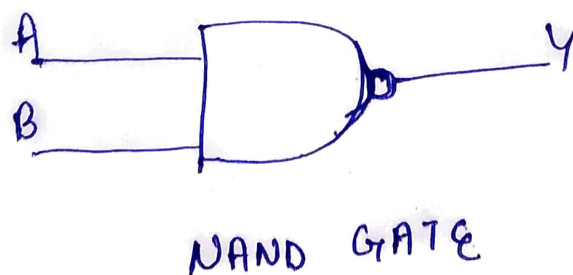
- It is a combinational circuit
- It is combination of two basic logic gates, the AND and NOT gate.

Symbol : $Y = A \cdot B$

Circuit diagram



or can be represented as -



TRUTH TABLE

A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

⇒ { output will be 0 if both input are 1
 otherwise output will be 1.

⇒ it works as a complement of AND GATE.