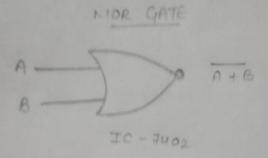
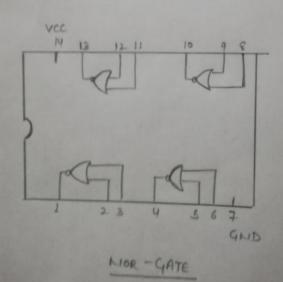
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## MOR gate:

It is actually a series of or gate with NOT gate. If we connect the output of an or gate to the input of a NOT gate, this combination will work as NOT-OR one NOR gate.

Its output is o when any or all input are, otherwise output is 1.

## Perocedure ,

- 1 Make the connection as per the Circuit diagram on breadboard.
- D Apply various combination of output according to truth table of NOR.
- (3) Note down the output reading for different combination inputs. TRUTH TABLE

A	B	TYT
0	0	1
0	1	0
	0	0
	1	0