

MID-TERM PRACTICAL

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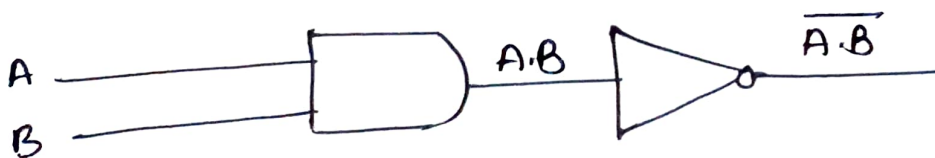
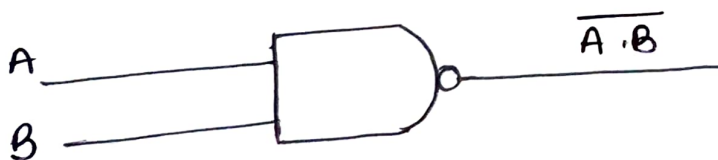
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Ques 1 In NAND gate, if the value of A is one and value of B is also 1 then the Output will be 0.

→ It is the combination of two gates one is AND gate and other one is NOT gate.

→ NAND gate is a logic gate which produces an output which is false only if all its inputs are true; thus its output is complement to that of an AND gate.

→ A LOW (0) output results only if all the inputs to the gate are HIGH (1), if any input is LOW (0), a HIGH (1) output results.



Kuldeep
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Truth Table

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A	B	$A \cdot B$	$\overline{A \cdot B}$
0	0	0	1
0	1	0	1
1	0	0	1
1	1	1	0

Kuldeep
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