

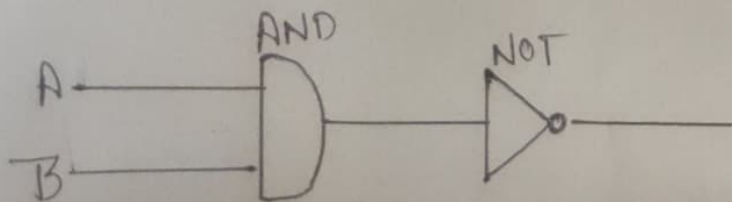
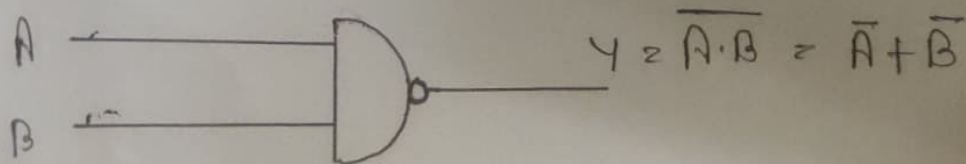
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Working of NAND Gate ÷ A NAND Gate is a logic Gate that produces a low Output (0) Only if all its inputs are true, and High Output (1) otherwise. Hence the NAND Gate is the inverse of an AND Gate, and its Circuit is produced by Connecting AND Gate to a NOT Gate.

NAND Gate may have any Number of input problems but One Output.

NAND Gate are also Known as Universal Gate. which Means they are type of logic Gate.

Circuit Diagram ÷



Not Gate Works as an Inverter

By Using AND Gate and NOT Gate We Can Implement NAND Gate

A	$\overline{B}$	$\overline{A}$	$\overline{B}$	Y
0	0	1	1	1
0	1	1	0	1
1	0	0	1	1
1	1	0	0	0