

Graphic Era Hill University  
Dehradun

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Section: B

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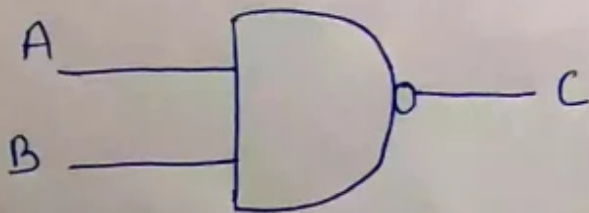
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Ans 1

A NAND gate is a logic gate that produce a low output (0) only if all its input are true, and high output (1) otherwise. Hence the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate. Just like an AND gate, a NAND gate may have any number of input probes but only one output probe.

NAND gate performs the logical NAND operation. NAND GATES ARE known as Universal gates which means they are type of logic gate which can implement any Boolean function without need to use any other gate type.



$$C = \overline{A \cdot B}$$

Truth table

Input		output
A	B	$C = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Himanshu

