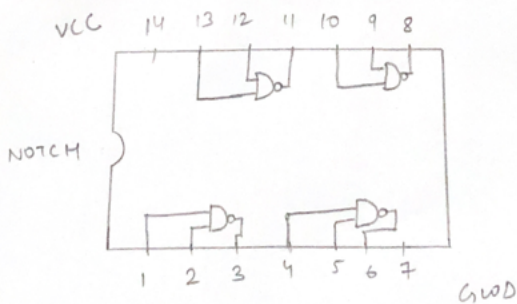


## \* NAND Gate -

The logic NAND gate is generally claimed as a "Universal gate". All the gates can be implemented by NAND gate. Its output is low when all the inputs are high. It is an inverter of AND gate.

## IC - diagram -



IC-7400

## logic diagram -



Truth Table -

Input		Output
A	B	$C = \overline{(A \cdot B)}$
0	0	1
0	1	1
1	0	1
1	1	0