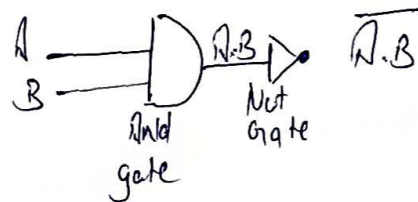


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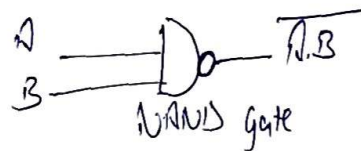
Subject COA
mid term practical

Nand Gate: A NAND gate ("not AND gate") is a logic gate that produces a low output (0) only if all its inputs are true, otherwise high output (1).

It is inverse of AND gate, and called universal gate, which means they are type of logic gate which can implement any Boolean function without the need to use any other logic gate type.



which is equal to



Truth Table of Nand Gate.

Inputs		Output
A	B	$x = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0