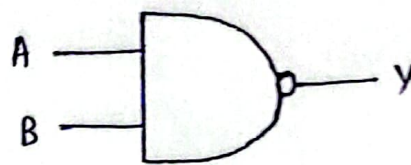


Q.1

The NAND gate is the combination of two basic logic gates, the AND gate and the NOT gate connected in series. The NAND gate and NOR gate can be called the universal gates.

The output of NAND gate is high when either of inputs is high or if both the inputs are low. The logic NAND function is given by the boolean expression  $Y = \bar{A} \cdot B$ . Here A, B are the inputs and Y is the output.

NAND GateTruth Table

A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

# Circuit Diagram

