

MID TERM PRACTICAL

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MID-TERM Practical

Q1. Discuss --- NAND gate --- truth table.

Ans NAND gate - A NAND gate

is a logic gate that produces a low output (0) only if all its inputs are true, and high output (1) otherwise. Hence

the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.

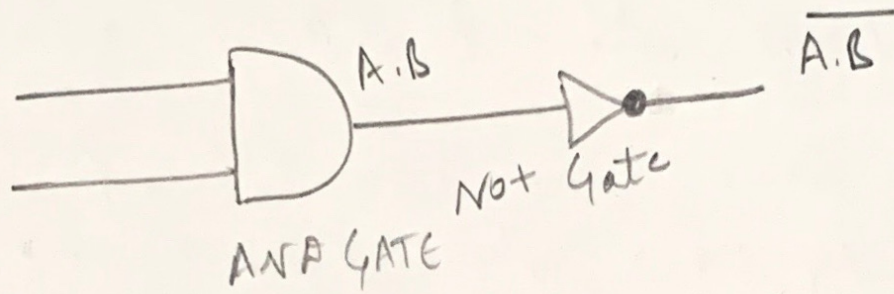
Just like an AND gate, a NAND gate may have any number of input probes but only one output probe.

Working of NAND gate - The NAND gate performs the logical NAND operation.

NAND gates are known as universal gates (along with NOR gates), which means they are a type of logic gate which can implement any Boolean function without the need to use any other gate type.

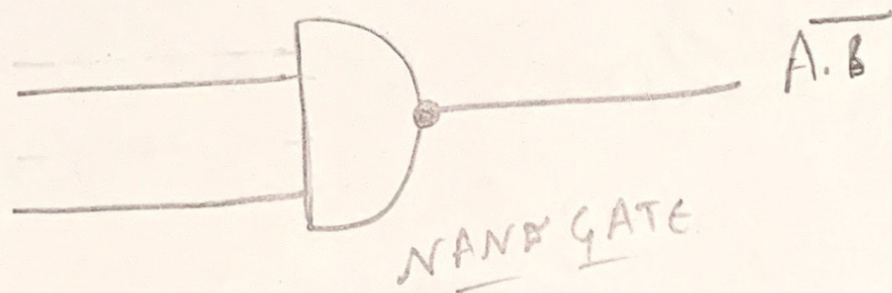
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2.

The basic logical construction of the NAND gate is shown below :-



The symbol of a NAND gate is similar to the AND gate, but a bubble is drawn at the output point of the AND gate.

The symbol of the NAND gate is shown below :-

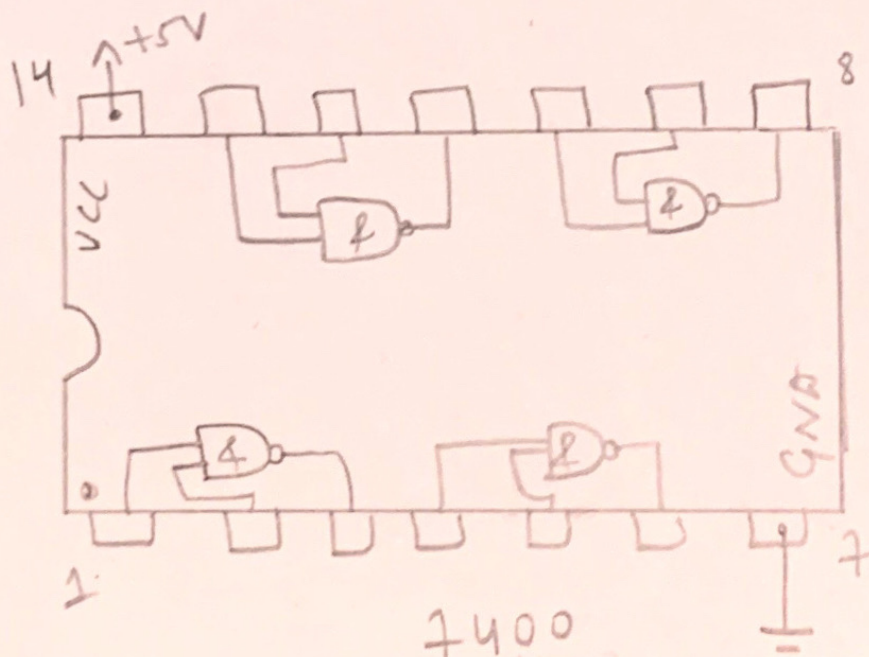


Truth table :-

2 input NAND gate

Inputs		Output
A	B	$X = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Circuit diagram :-



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