

Name - Jagat Singh Talwar, Roll No - 2101086

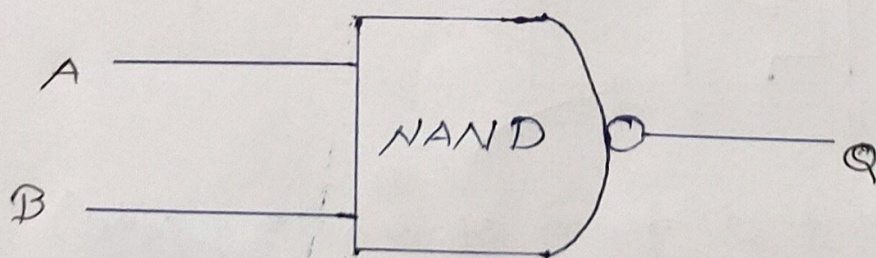
Course - MCA 1st (A)

Signature - Jagat

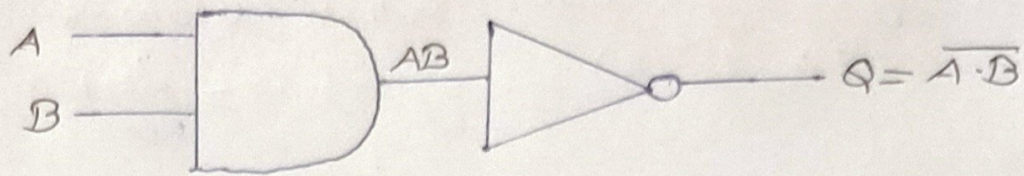
Sub -

Logic NAND gate

The logic or Boolean expression given for a logic NAND gate is that for logical addition, which is the opposite to the AND gate, and which it performs on the complements of the inputs. The Boolean expression for a logic NAND gate is denoted by a single dot or full stop symbol; (\cdot) with a line or overline, ($-$) over the expression to signify the NOT or logical negation of the NAND gate giving us the Boolean expression of $\overline{A \cdot B} = Q$.



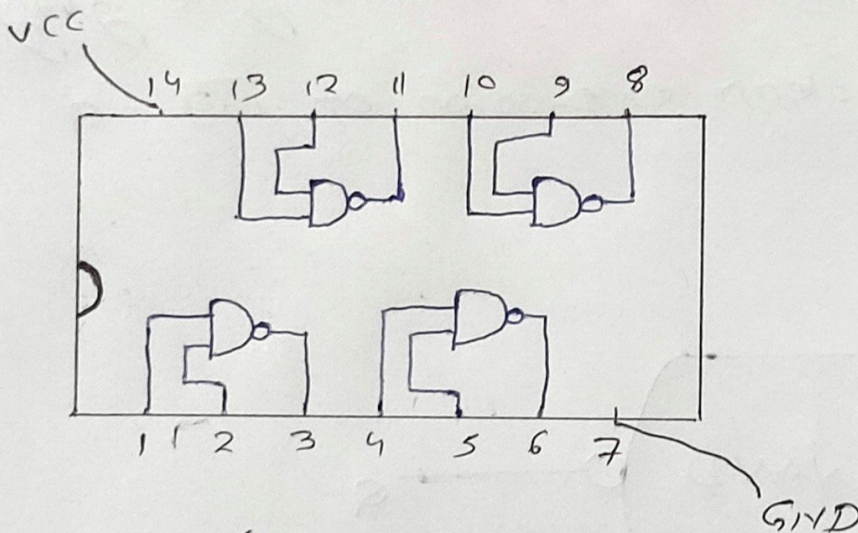
(logic diagram of NAND gate)



(NAND gate using AND or NOT gate)

Truth Table

A	B	Q
0	0	1
0	1	1
1	0	1
1	1	0



(Circuit diagram of NAND gate)