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GPIO schematic and dimension documentation



Dubravko Penezic

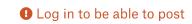
Dubravko Penez...

(@dubravko-penezic)



GPIO schematic and dimension documentation

Is it a GPIO board developed with OpenHardwer in mind?, if so where can the schematic and board dimensions be founded?







Posted: 15/10/2024 6:35 pm









RE: GPIO schematic and dimension documentation

The GPIO board is not any magic. It's just a breakout board generally with an I2C to GPIO expander chip.

Most information is here: https://help.prusa3d.com/article/gpio-module_734695

Regarding the size: I have aready seen some cases for the GPIO board on printables

https://www.printables.com/model/998147-prusa-gpio-hackerboard-case

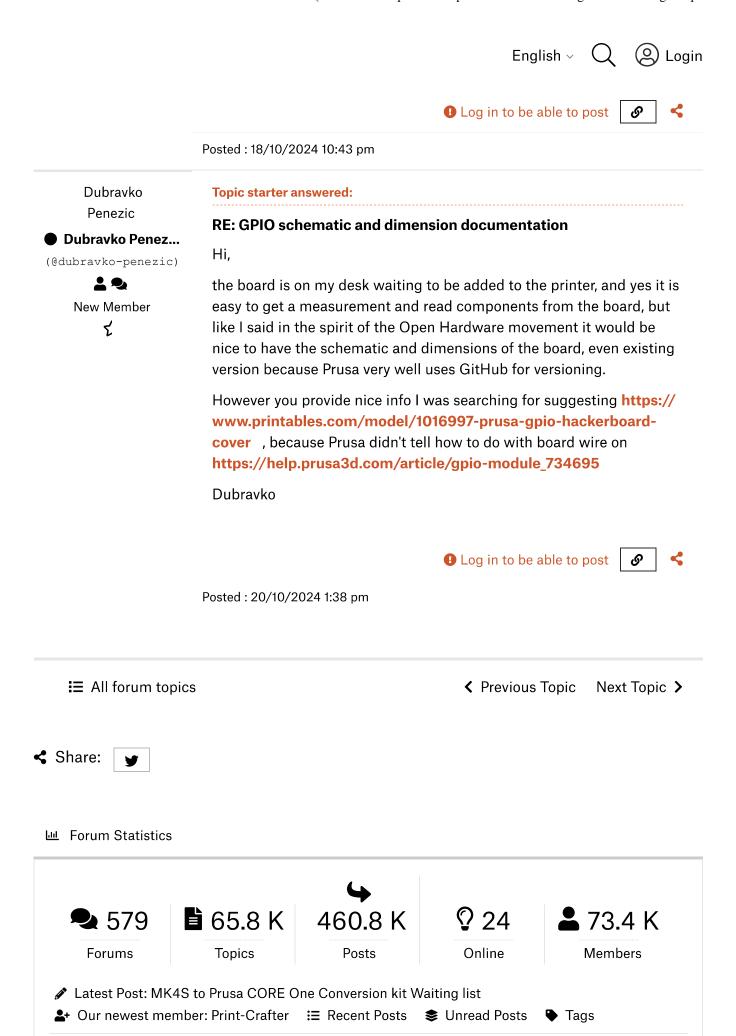
https://www.printables.com/model/1016997-prusa-gpio-hackerboard-cover

So this could give you some measurements for the board.

I expect that the schematics will be release later as the current board states 2024-08-20. (Maybe there will be slight changes in the future).

I think the main chip of this board is something like a PCF8574 from Texas Instruments. At least is look like it, but this is my guess! So no magic at all. But you need the Firmware 6.2.0-apha2 (or newer)

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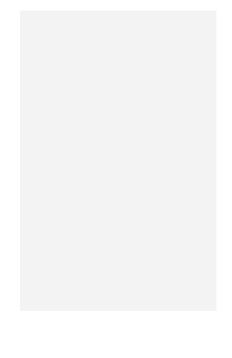
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