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Study of SelectIO & Clocking Resources – Artix-7

Module: FPGA Architecture

Study of SelectIO & Clocking Resources – Artix-7



- Objective: Study of SelectIO & Clocking Resources
- Reference Material:
 - Refer and study “ug471_7Series_SelectIO” document from Xilinx for SelectIO Resources
 - Refer and study “ug472_7Series_Clocking” document from Xilinx for Clocking Resources
- Assignment:
 - Which all different I/O Standards are supported by Artix-7 FPGA?
 - List all the Select IO Logic resources.
 - How many CMTs are available in XC7A35T FPGA?