

Update on Ara

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Summary

- ASAP conference Presentation
 - Prepare slides
 - Record presentation
- New benchmark
 - DWT

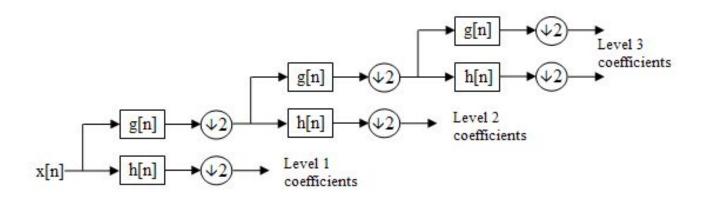
- Performance analysis
- HW
 - FP reductions integration

Benchmarks - Analysis ongoing

- [i,f]matmul crucial kernel
- [i,f]conv2d vslides
- roi_align
- jacobi2d stencil, misaligned accesses
- axpy mem bound
- spmv indexed mem ops
- dropout memory bound
- dwt segmented or strided
- fft segmented, masked permutations
- softmax fpred, fpdivisions
- float cos, log, exp
- memcpy, strncmp, strncpy
- [i,f]dotp reductions
- pathfinder

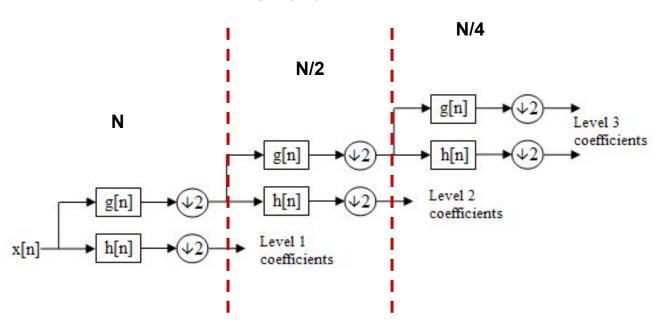
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log2(N) rounds



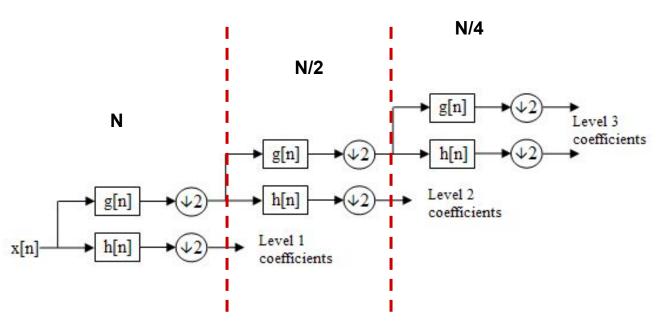
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log2(N) rounds



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log2(N) rounds



First implementation: 2 coefficients/filter!

- DWT (float32) Memory bound
- Downsampling
 - Segmented mem ops
 - ✓ Strided mem ops
 - Intrinsic BW limitation
- DWT 512 samples
 - Performance on max: 41% (48% only first iteration)
- Improvement over scalar
 - 8x

Further

- Complete performance analysis for our benchmark pool
- Add softmax (+exp, log, cos)
- Add FP reductions
 - Solve timing criticalities
- For each benchmark, report analysis

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