Column1	w1	w2	w3	w4	w5	w6	w7	m1	m12	m21	m20	m31	m30	m4	m51	m50	m61	m60	m71	m70	m8	m92	m91	m90	m101	m100	wc	wz	mz	alu_op
sa	1		1					0														0	1	1	0	1				
s0			1							0	1																			
s1					1	1	1						0				0	1	0	1	0									
s2				1													1	0				0	1	0	1	0	1	1		0
s2'				1													1	0				0	1	0	1	0	0	1		1
s3				1								1	0		1	1														
s4				1													1	0				0	0	1	1	0	1	1		
s5				1								0	1		1	1														
s6				1								0	0		0	1														
s7						1													1	0		0	0	1	1	1				
s8				1						0	0						0	0												
s9		1							0	0	0																			
s10				1								0	0		1	1											1	1	1	
s11						1			1	1									1	1										
s12				1	1							1	1		1	0	1	0				0	1	1	0	0				
s13					1	1								1			1	0	0	0		0	1	1	0	0				
s14		1					1		1	1	1										1	0	1	1	1	0				
s15	1							0														0	0	0	0	1				
s16				1		1						0	0		0	0			0	1										
s17	1							0														0	0	1	0	1				
s18	1							1																						
s19							1														1	0	1	1	1	0				

w = 1 means active/enable write.
default w is 0 (disabled)
In w's coloum, empty implies 0

m's are mux selectors
In m's column, empty implies don't care.