SHUBHRA DUBEY

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PERSONAL DETAILS

- Date of Birth : 01/06/1995
- Nationality : India
- GitHub : https://github.com/ShubhraDubey

PROFESSIONAL SUMMARY

Proficient Project Engineer with experience of driving Projects to completion. Skilled in overseeing technical staff ensuring that projects are completed on schedule, within budget and according to specifications. Conducting Government officials training on Robotics Process Automation using Ui-Path, Faculty Development programme. Results-oriented and Proficient in developing course materials, including syllabi, lesson plans and exams. Offering background in delivering Robotics Process Automation, IOT, ASIC and Embedded System lectures and classroom discussions.

WORK EXPERIENCE

Sep '20 - Present

National Institute of Electronics and information Technology (NIELIT), Gorakhpur
Project Engineer (Under Future skills PRIME- Robotic Process Automation (RPA)

- Liaised with company executives and project managers to acquire resources to move project forward.
- Design automation process as per the operational needs of an organization, Scripting and coding in UiPath tool to resolve automation issues and also Documenting the automation procedures
- Conducting Government Officials Training on Robotics Process Automation under Future skills PRIME project. The objective is to create an in-depth awareness on RPA among government officers so that they can recommend RPA technology in their work place. This Training introduces the techniques and applications of RPA in different domains.
- Contributes in Faculty Development programme (FDP), as a resource person, this intends to provide financial assistance to facilitate up-gradation of knowledge, skill and intends to provide opportunities for induction training to teachers employed in discipline Engineering and Technology, Diploma etc.
- Conducting Batches on RPA, it make Lerner familiar with the concepts of RPA.
- To research, build, test and document state of the art IoT solutions with integrated electronics and firmware development. Also develop local IoT devices circuits. And conducting batches on IoT for o level students.
- Taking lectures in M.Tech programme in Electronics design and Technology (ED&T) and VLSI Design on Embedded System and ASIC.
- Used variety of learning modalities and support materials to facilitate learning process and accentuate presentations, including visual, aural and social learning modalities.

EDUCATION

- **M.Tech: Embedded System and Technology**
  1. 2018 to 2020
  2. **Amity University** Lucknow
  3. Graduate with 9.3 CGPA
  4. **Dissertation**: "Design & Analysis of QCA for Reversible logic Combinational circuit and applications."
  5. **Webinar**: "Quantum Dot cellular Automata" organised by IEEE RITB on 2nd May 2020.
  6. **Internship**:"Design of Reversible gate using Quantum cellular automation (QCA)" from NIELIT Gorakhpur from 17.06.2019 to 12.07.2019.
- **B.Tech: Electronics &Telecommunication Engineering**
  1. 2013 to 2017
  2. Institute of Technology and Management, Gorakhpur
  3. **Training: "**Vocational Training at ALL INDIA RADIO GORAKHPUR" From 22nd June to 2st July 2016.
  4. **Project**: "Pulse Oximeter"
  5. **Mini Project**: " Remote control for home appliances"
  6. **Academic**: Seminar presentation on "GI-FI"

PUBLICATIONS

- **Study and Design of a Novel Half Adder Circuit Using QCA Gates with Optimized Parameters**
  Publisher Name- Springer, Singapore
  First online-14 December 2021
  DOI- https://doing.org/10.1007/978-981-16-2761-3_96
  Print ISBN 978-981-16-2760-6
  Online ISBN- 978-981-16-2761-3
- **An Optimized QCA Implementation Approach for Half Adder Circuit**
  Publisher Name: Junrnal of Xidian University.
  DOI- https://doi.org/10.37896/jxul4.6/113
  Page No: 1000-1004
  ISSN No. 2400
  Volume 14, Issue 6, 2020

TECHNICAL SKILLS

- Robotics Process Automation using UI Path Tool
- Presentation Creation
- Programing languages: C, C++ , Python, .Net
- Operating System: Windows, Ubuntu
- Hardware Description language: VHDL , verilog
- Uipath, QCA designer, Xilinx, ModelSim, MS office tools.

ACHIEVEMENTS & AWARDS

- Conducted 5 days Faculty Development Programme in Nanasaheb Mahadik College of Engineering, Sangli, Department of E&TC and CSE on "Robotic Process Automation" from 13th to 17th July 2021
- Contribute as Resource person in All India Council for Technical Education (AICTE) Training and learning (ATAL) Academy FDP on RPA from 17/05/2021 to 21/05/2021
- Diploma of Completion: RPA Design and Development (24 September 2020)
- Participated in 3rd international Conference on VLSI, Communication and Signal processing (VCAS 2020), Organized by Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology Allahabad,held during October 09th-11th 2020.
- Participated in International Conference (COTII-2020) 28 & 29 feb 2020, Ambalika Institute of management and Technology.
- First Position in Chess in Eram Girls Degree College 2020. First position in Chess in Sangathan 2019 at Amity University. Participated in Chess at the "State Level Sport Fest" 2017 at IMSEC Ghaziabad. First Position in Chess at the "Zonal Level Sport Fest" 2017.
- Attended a week long Military Training Camp, Sep2019 Conducted by Amity Indian Military School, Mansur.

LANGUAGES

- English
- Hindi
- Working knowledge of German

FIELD OF INTEREST

- Robotics Process Automation (RPA)
- Quantum Dot cellular Automation (QCA)
- Internet of Things (IoT)

DECLARATION

- I hereby declare that all the information mentioned above is true and correct to my knowledge and I take full responsibility for the accuracy of the particulars mentioned.