

LTspice SOFTWARE

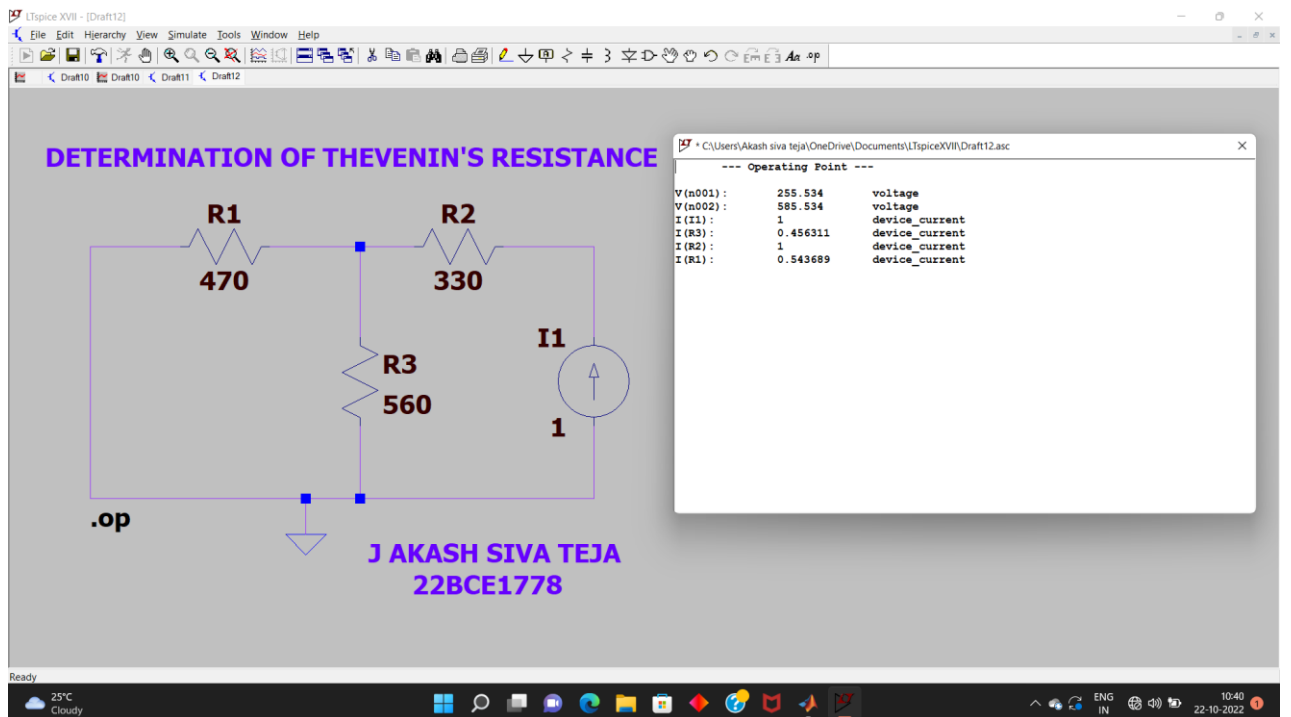
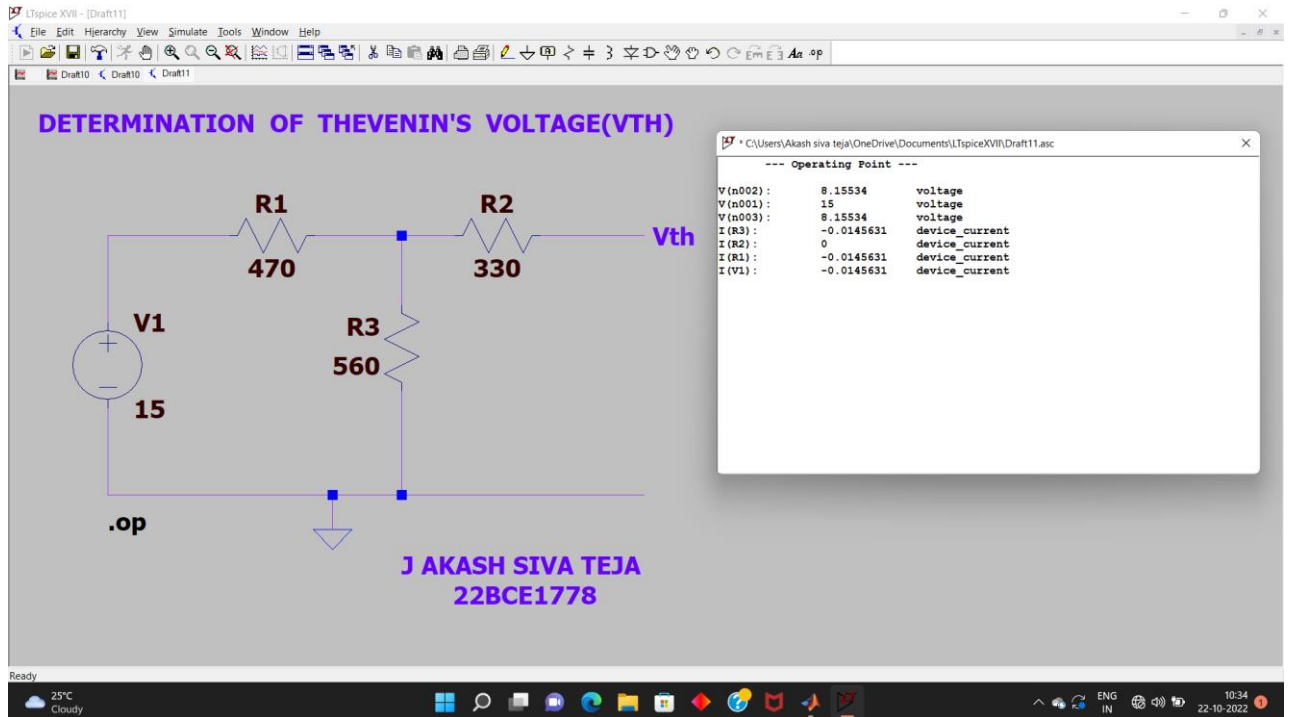


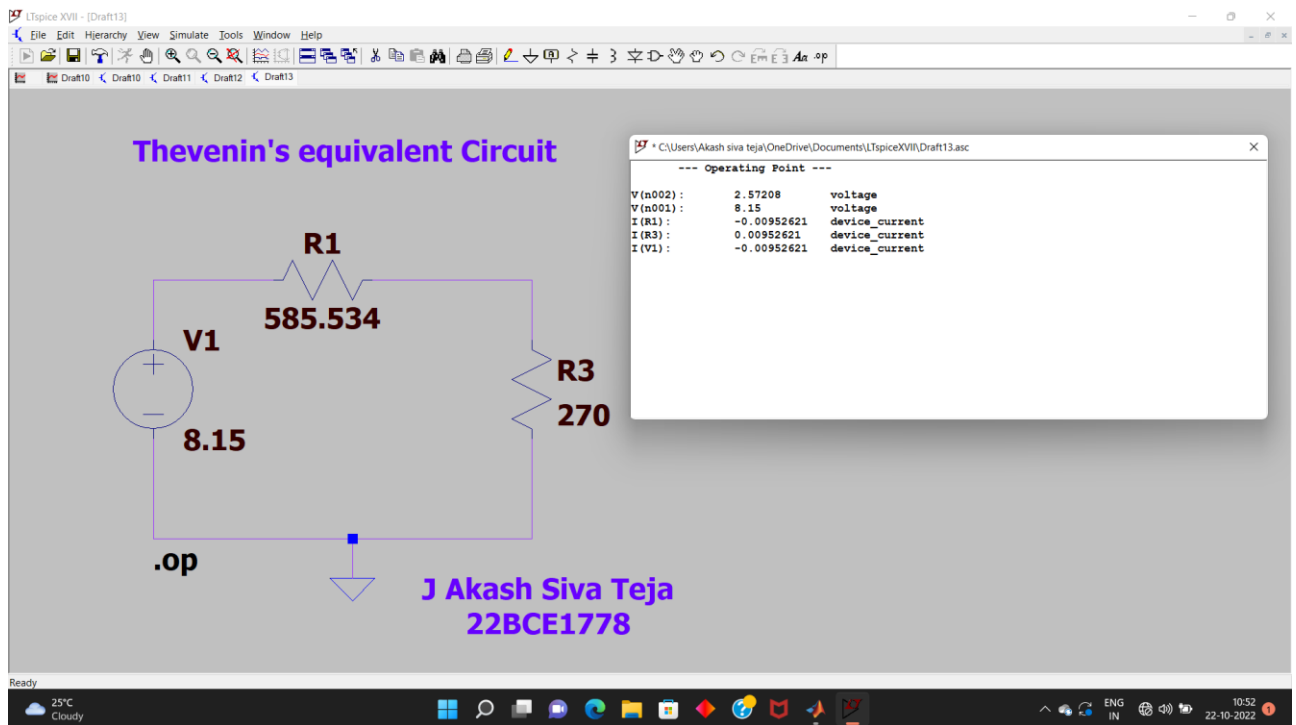
Jayampu Akash Siva Teja

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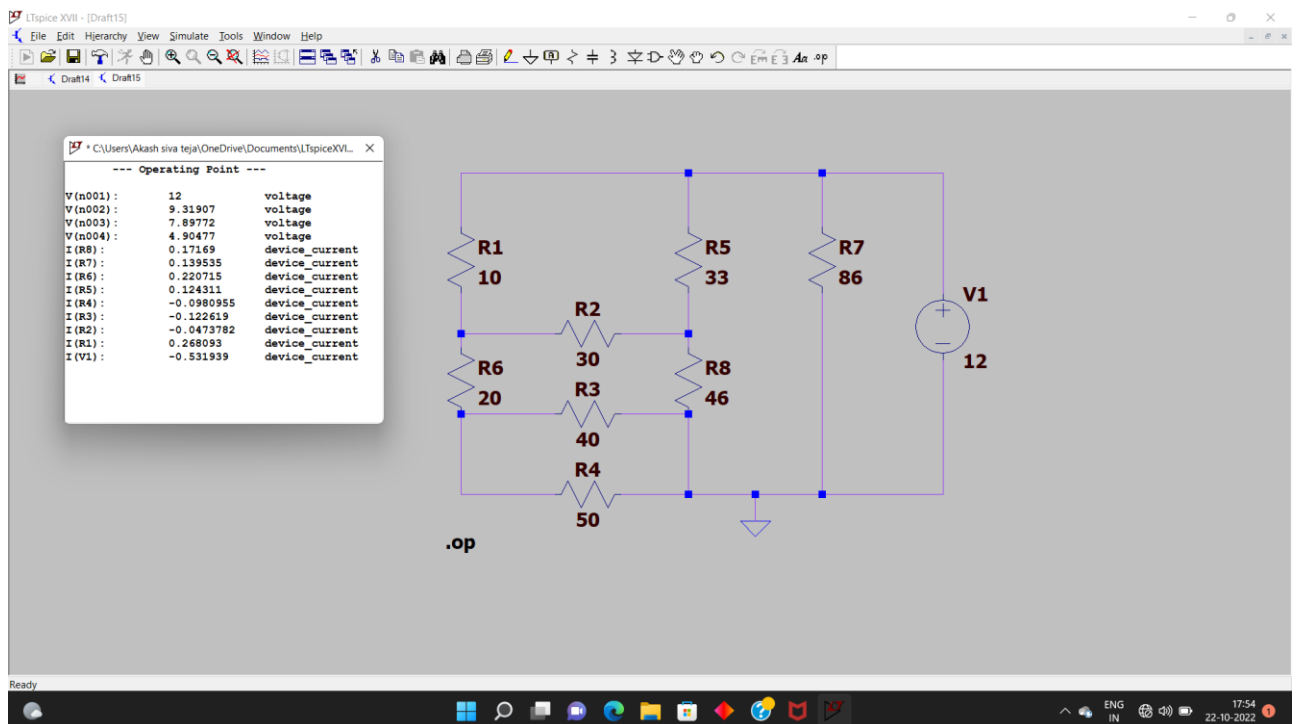
Submitted To: Dr.Lavanya V

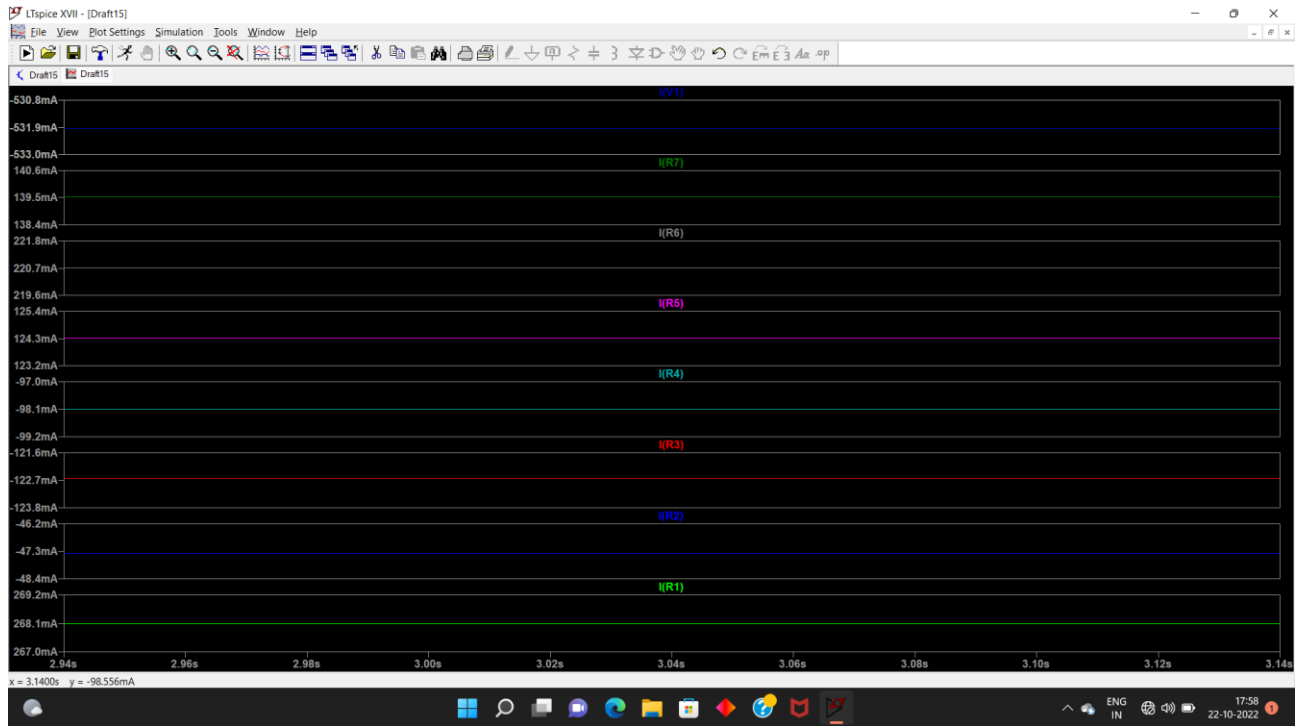
THEVENIN'S CIRCUIT USING LTspice:



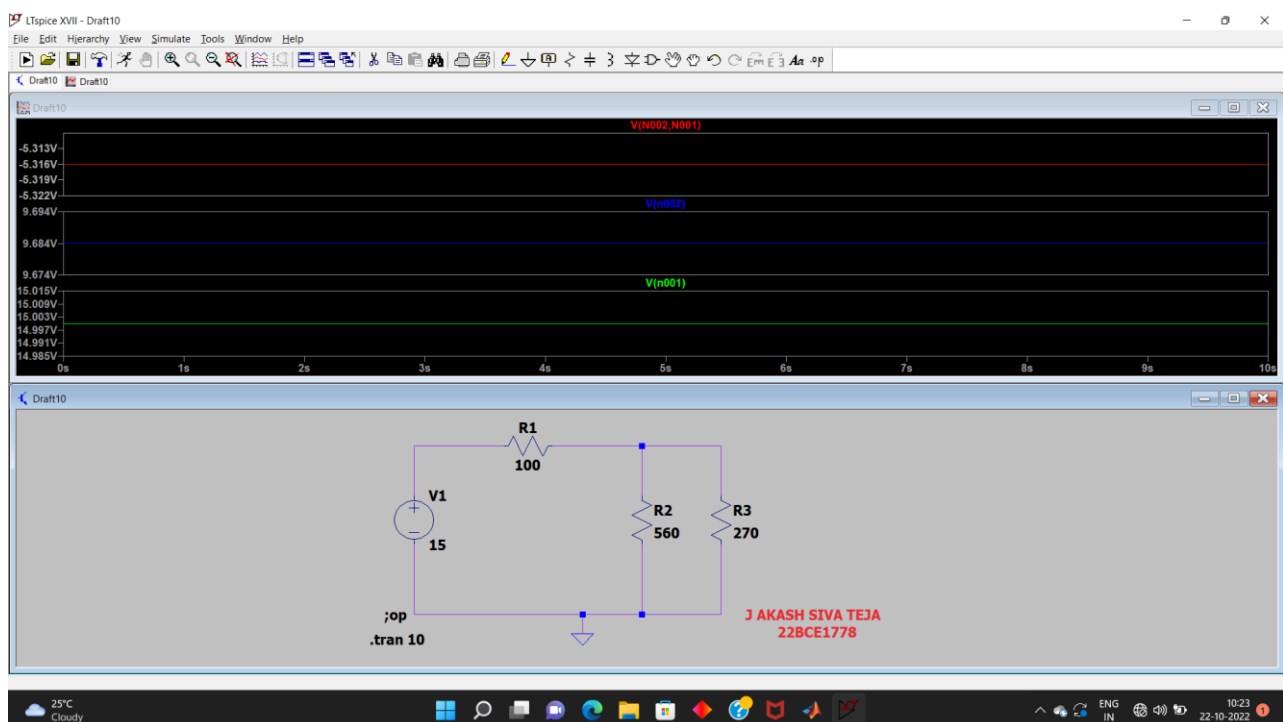
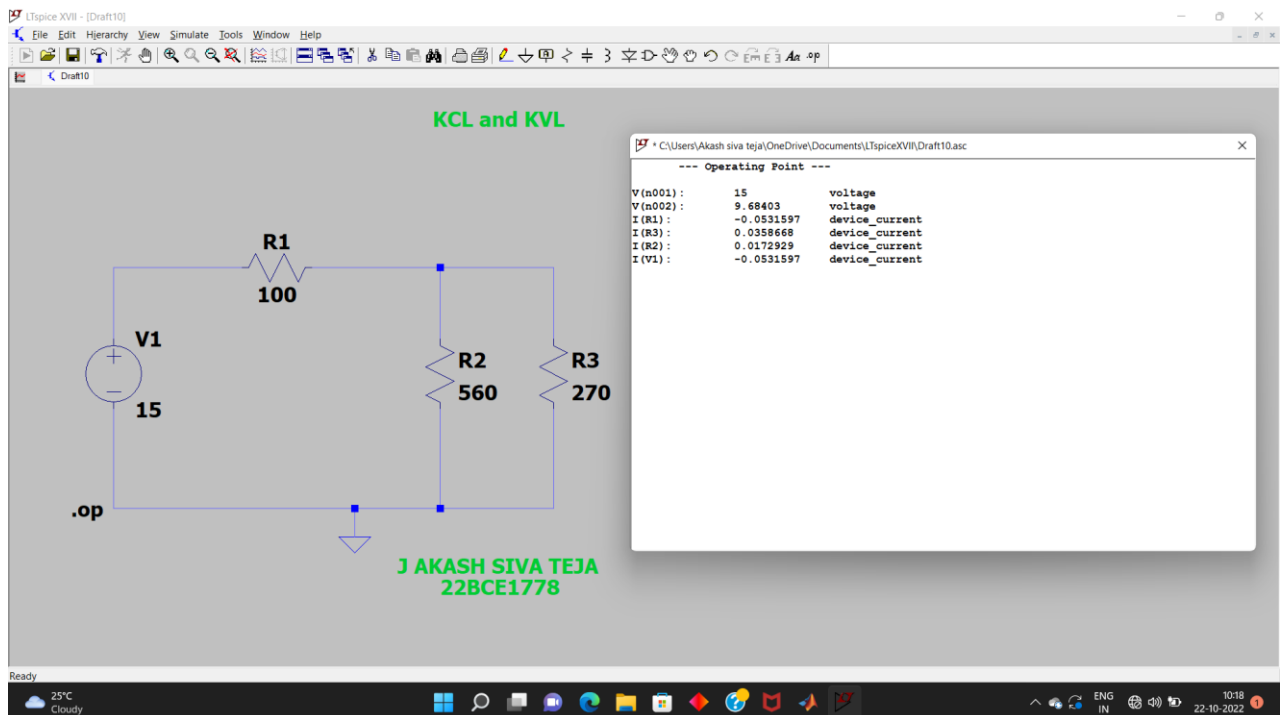


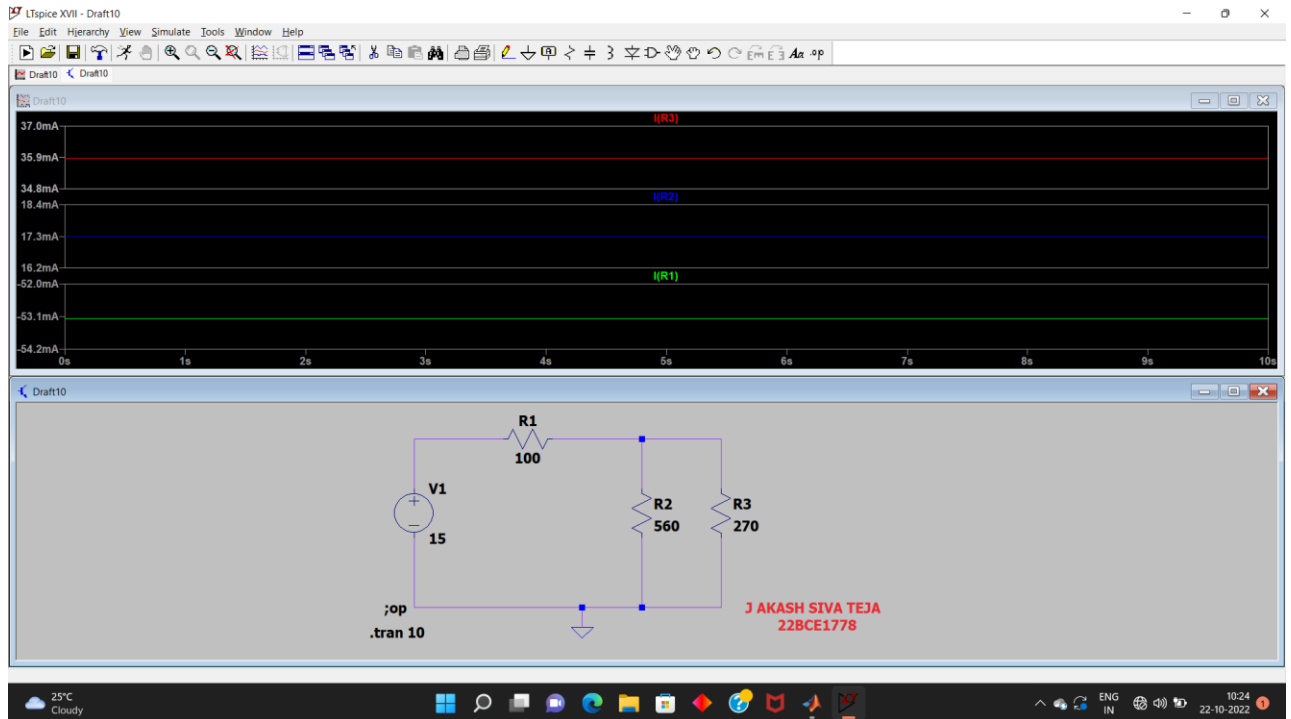
FINDING NODE VOLTAGES AND CURRENT THROUGH RESISTORS:



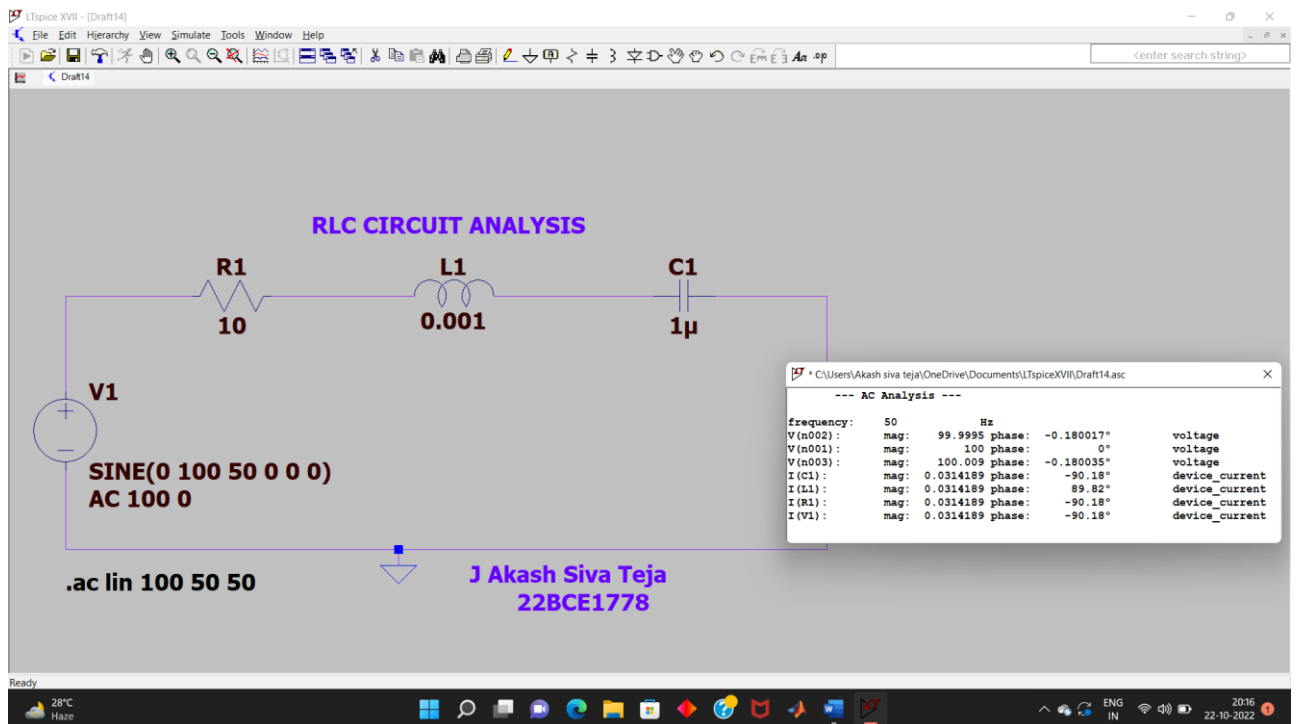


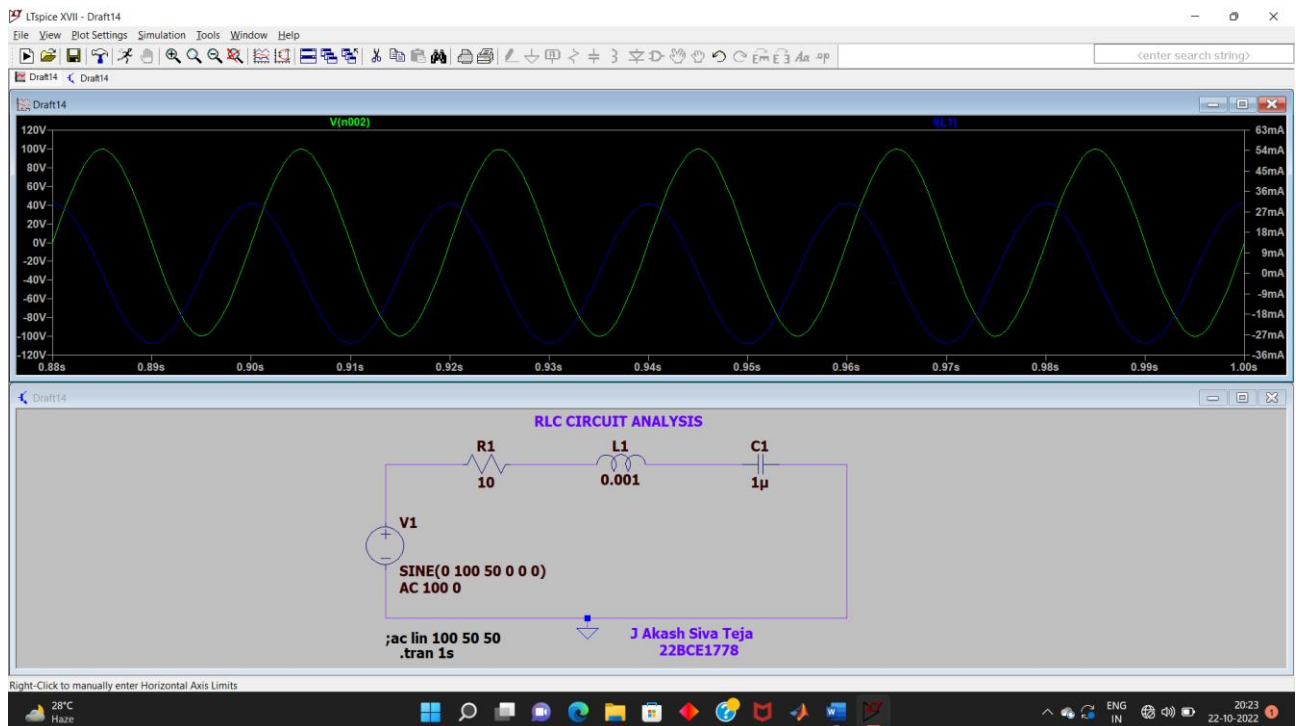
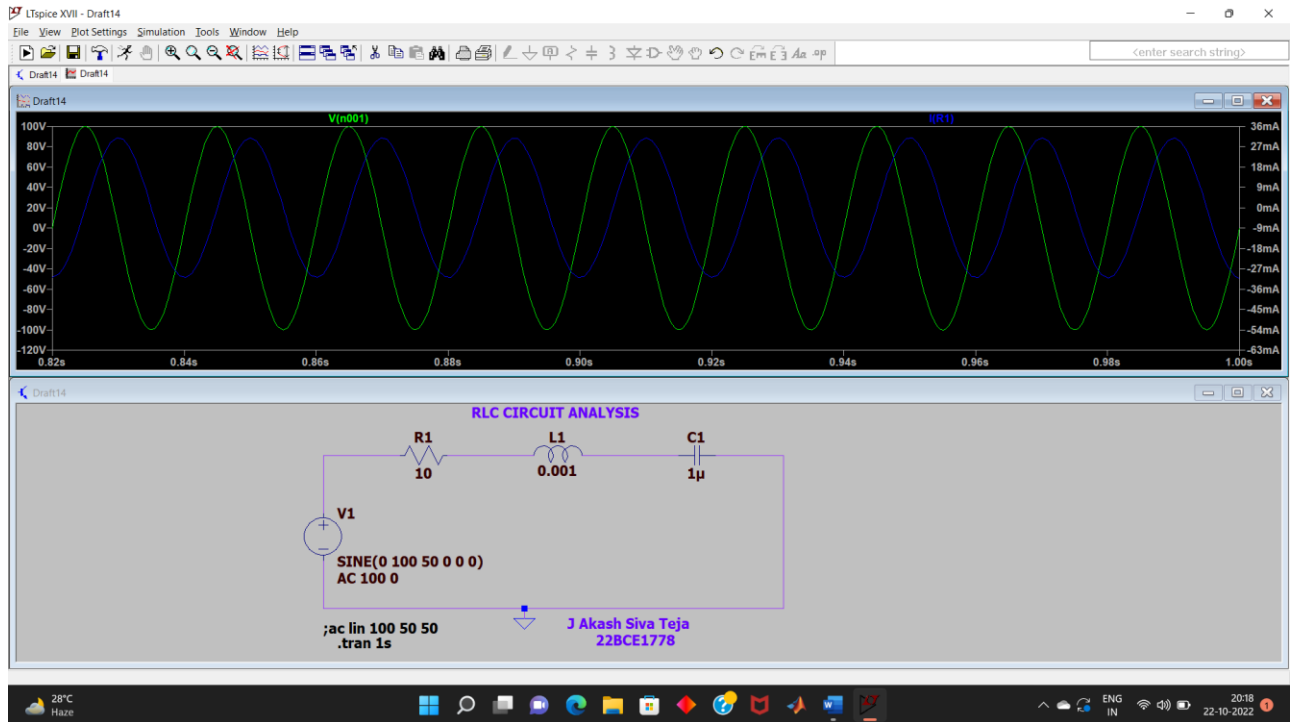
VERIFICATION OF KCL AND KVL USING LTspice:

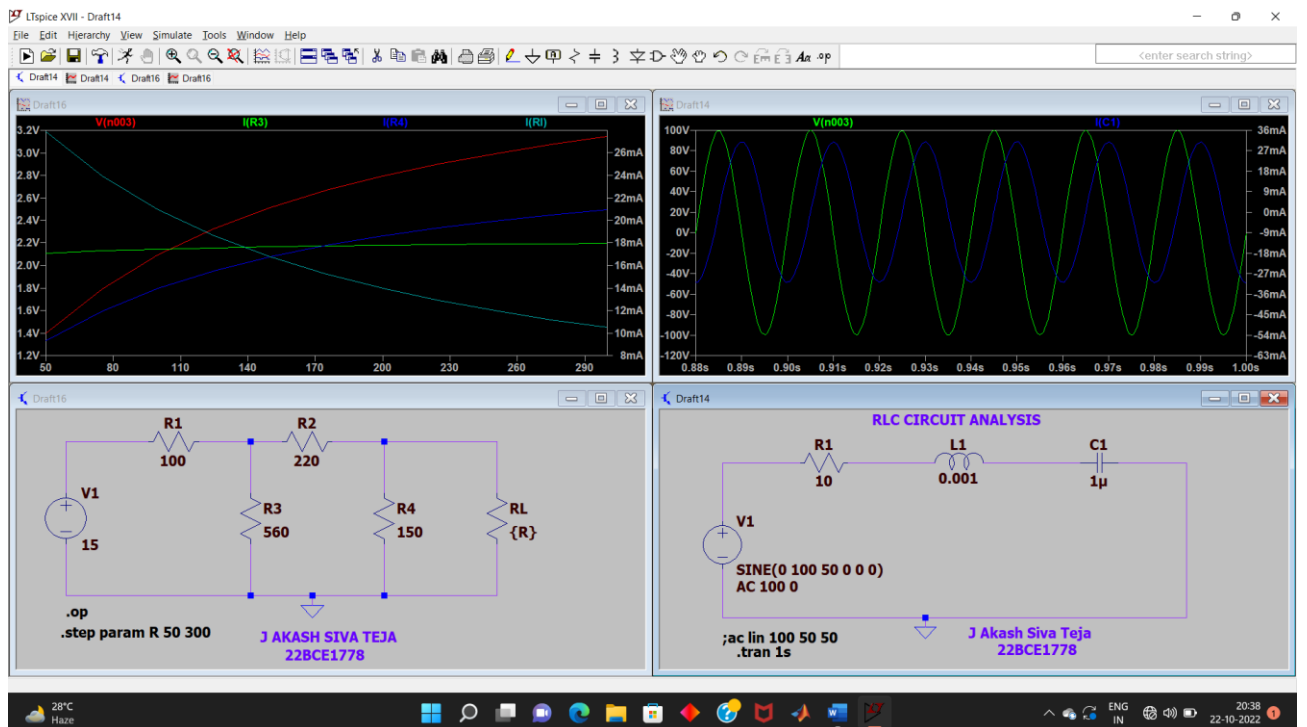
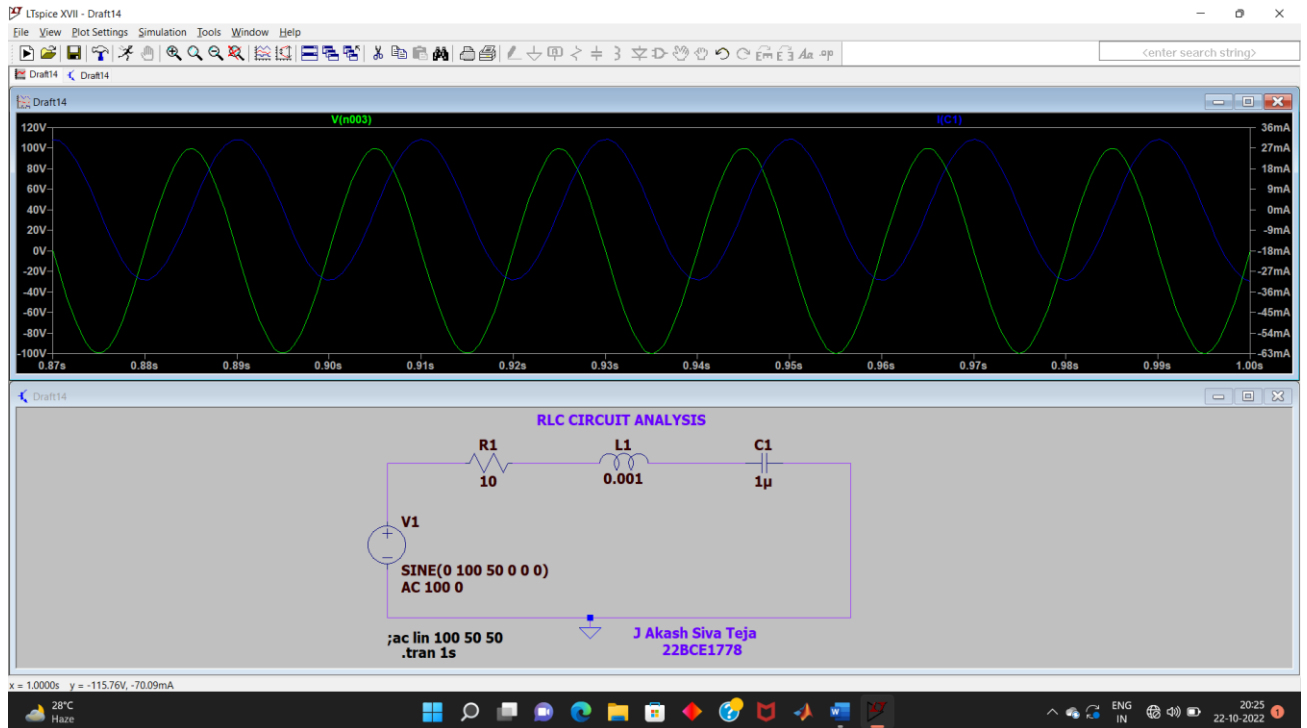


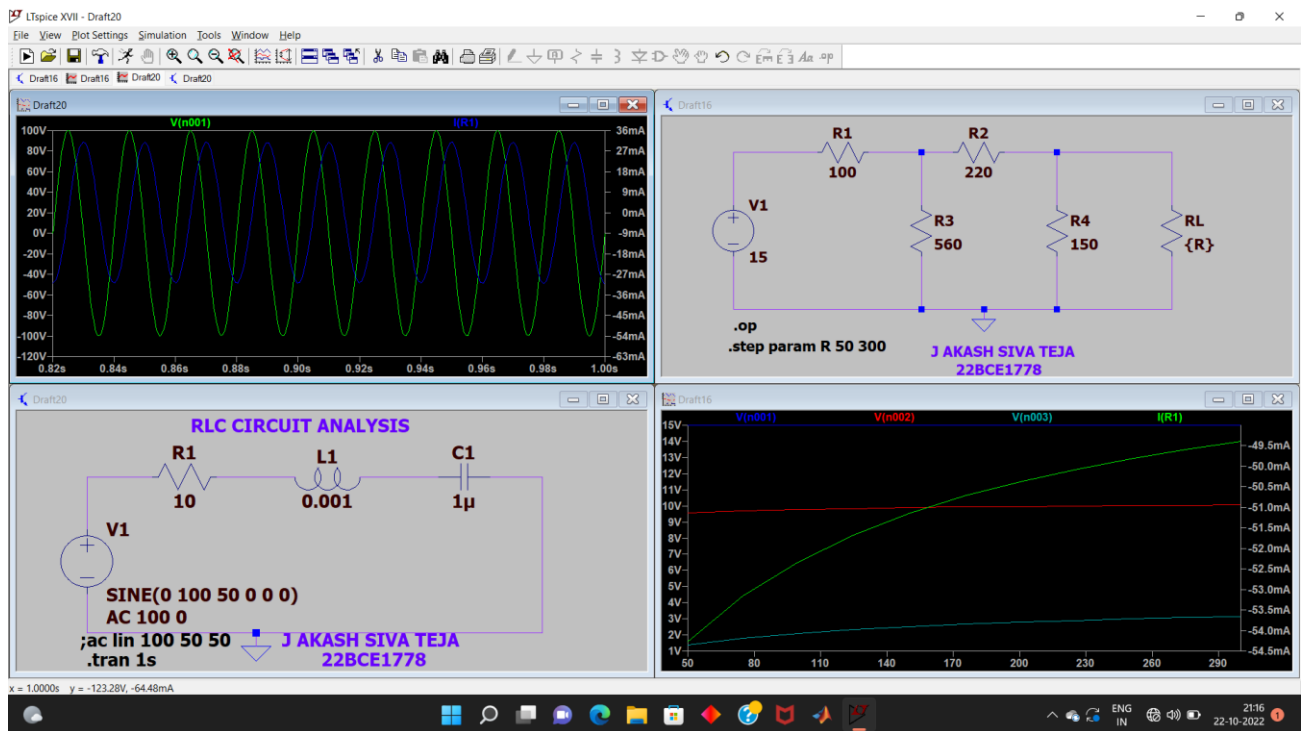
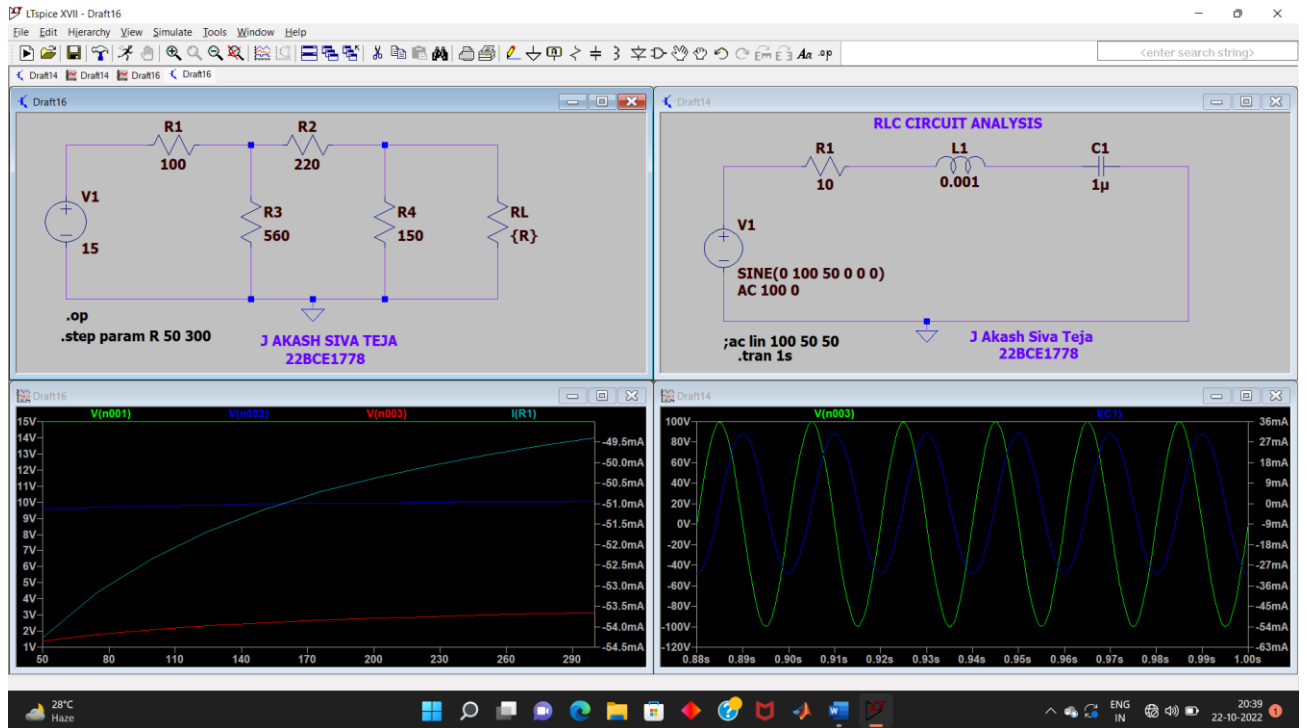


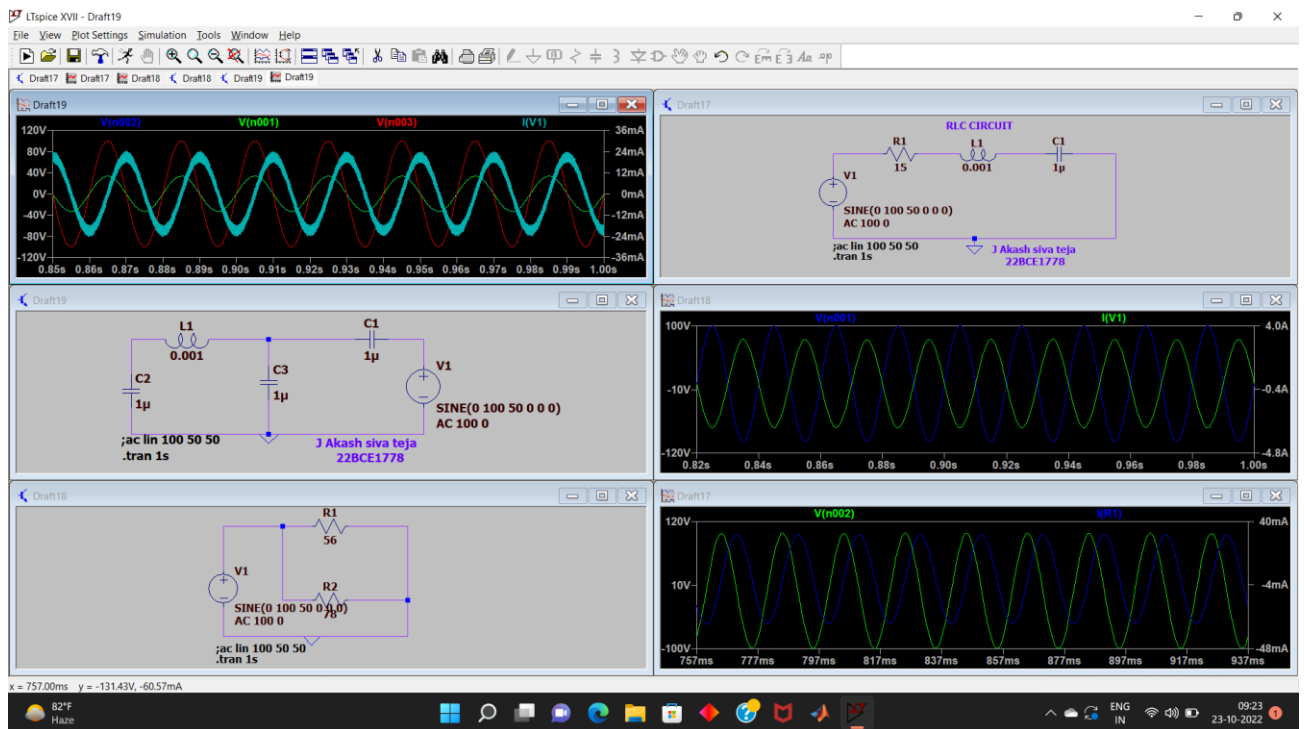
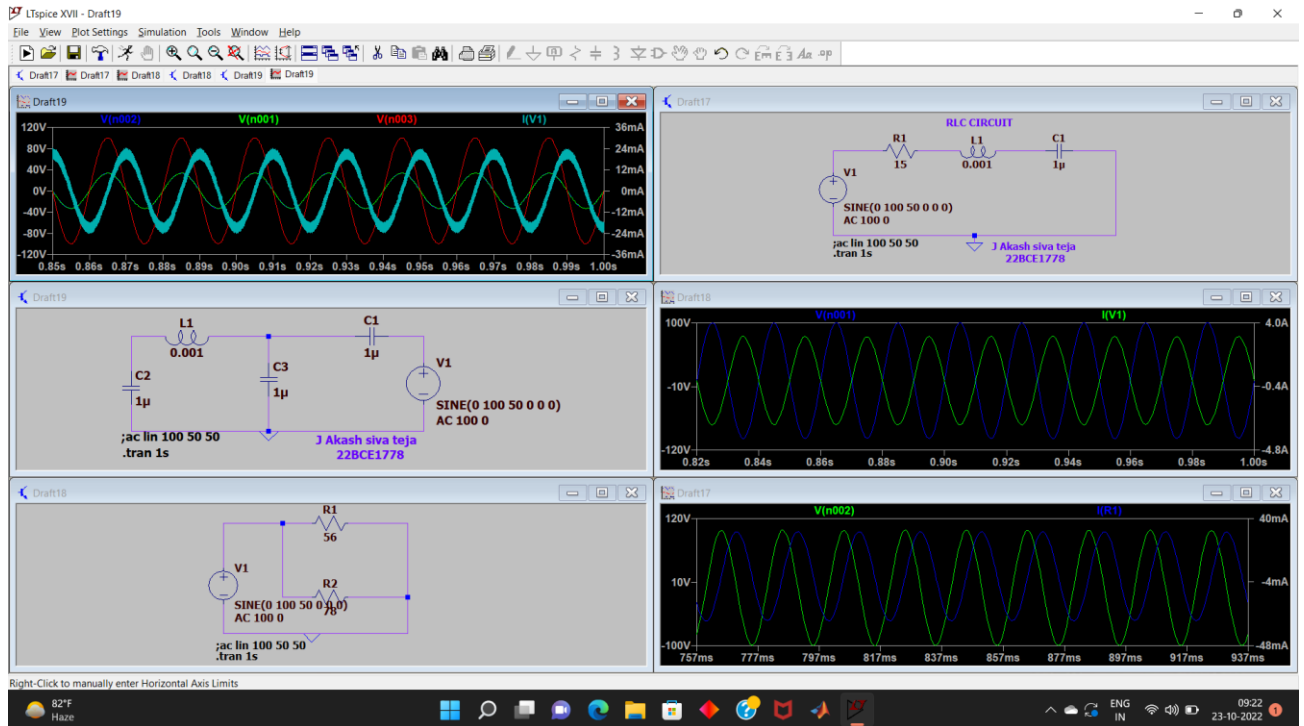
RLC CIRCUIT ANALYSIS USING LTspice:

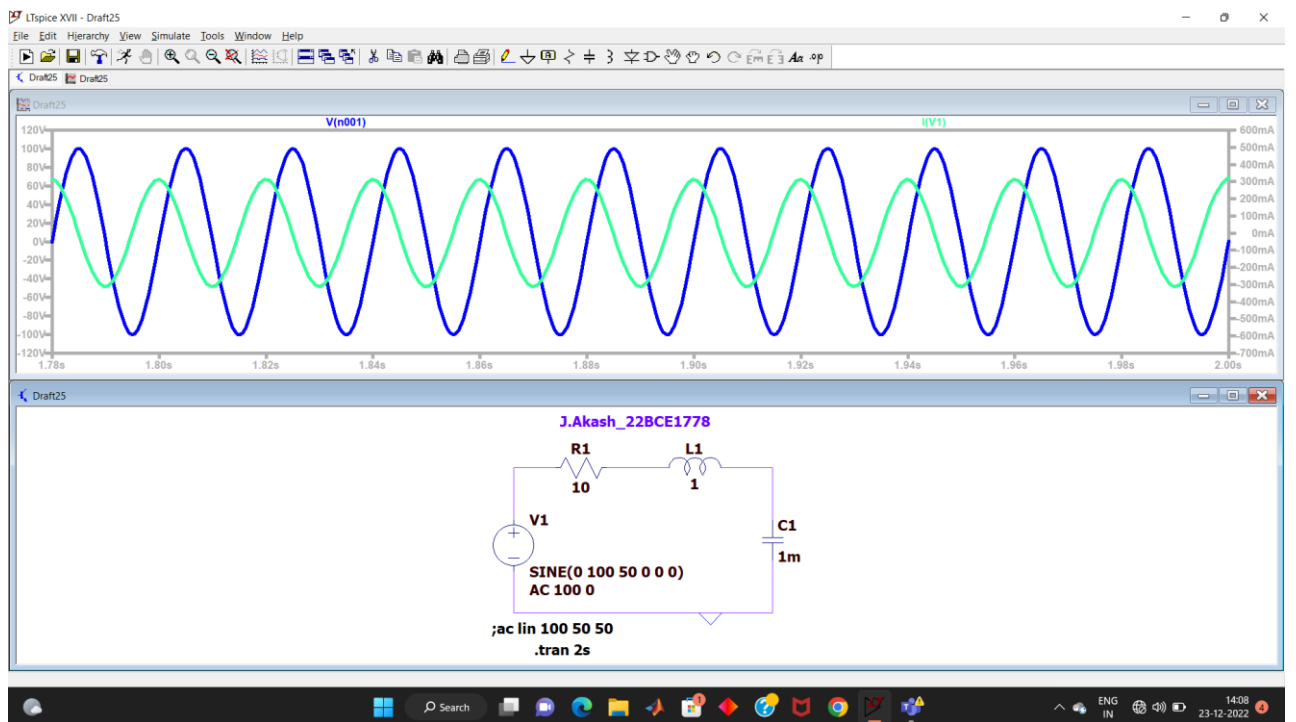
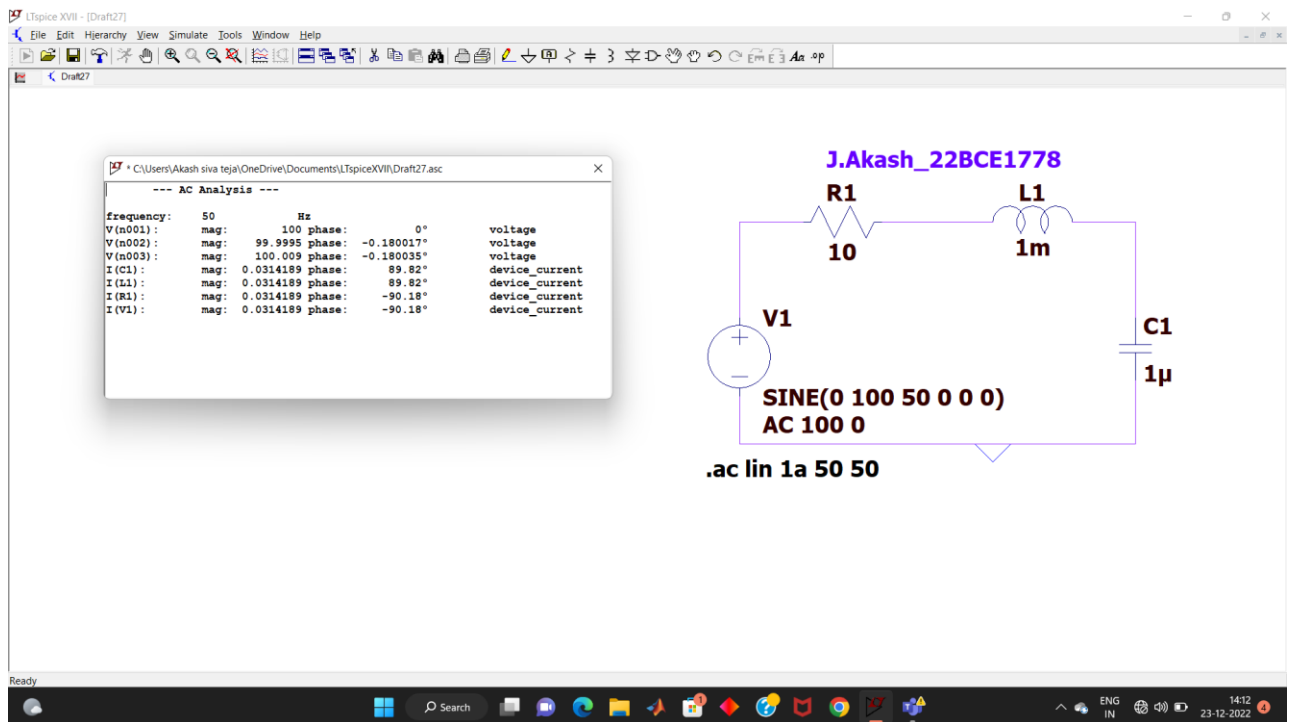


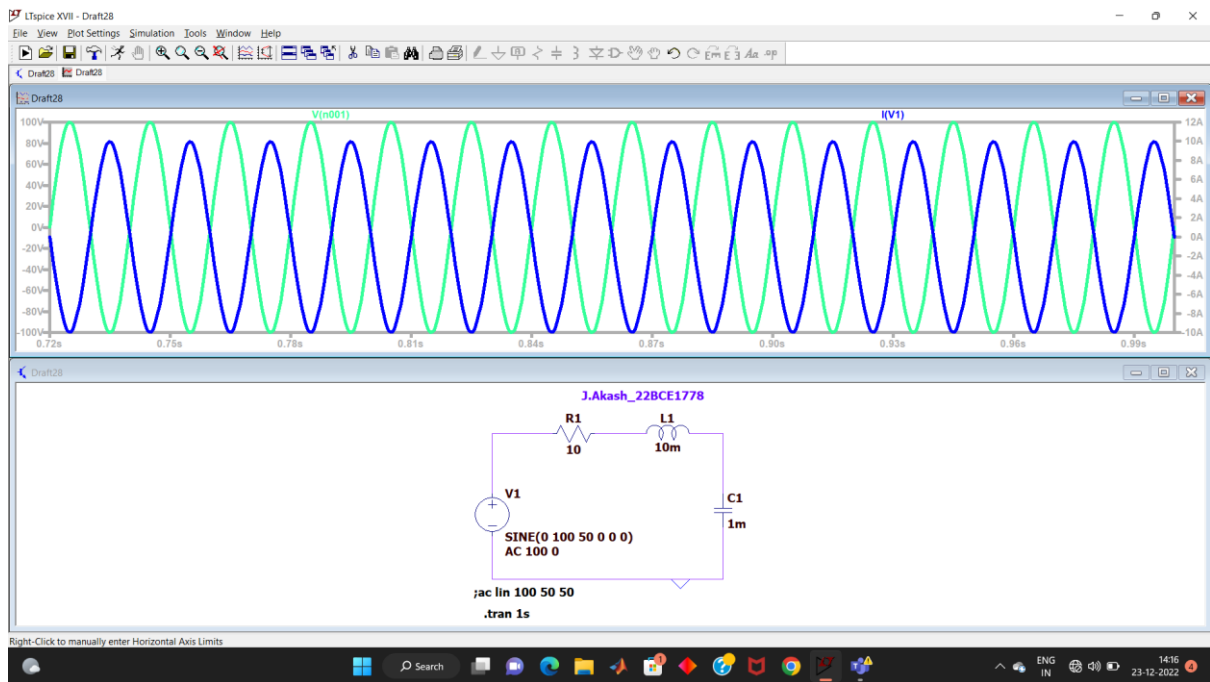




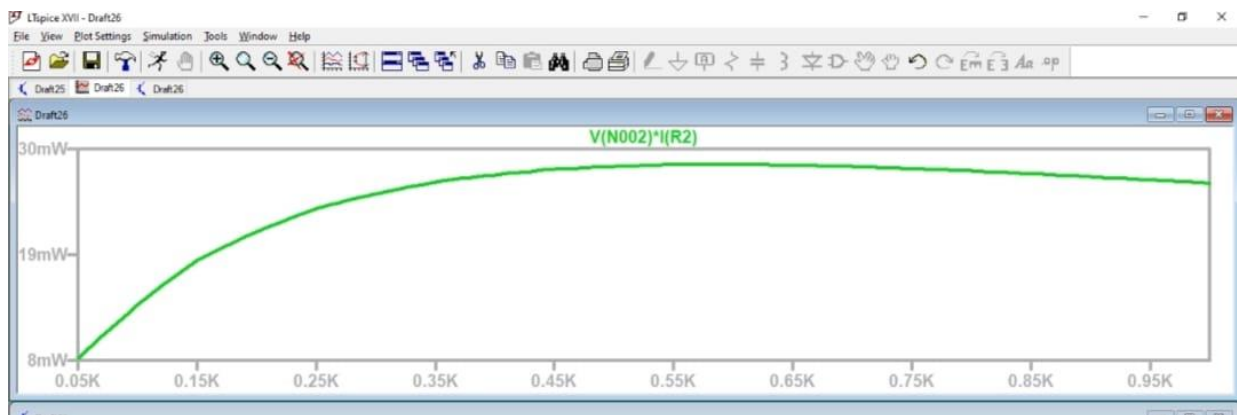
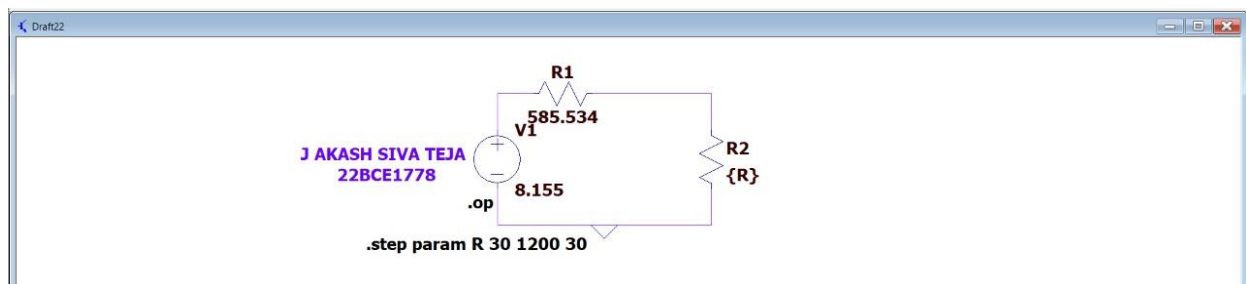


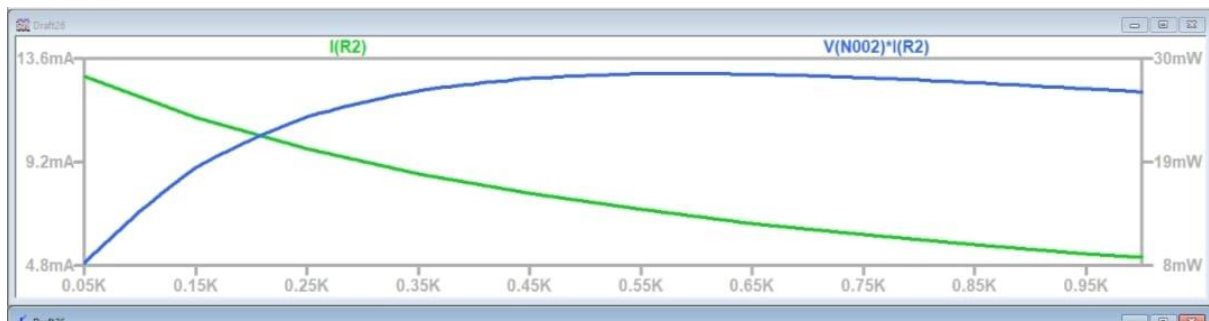




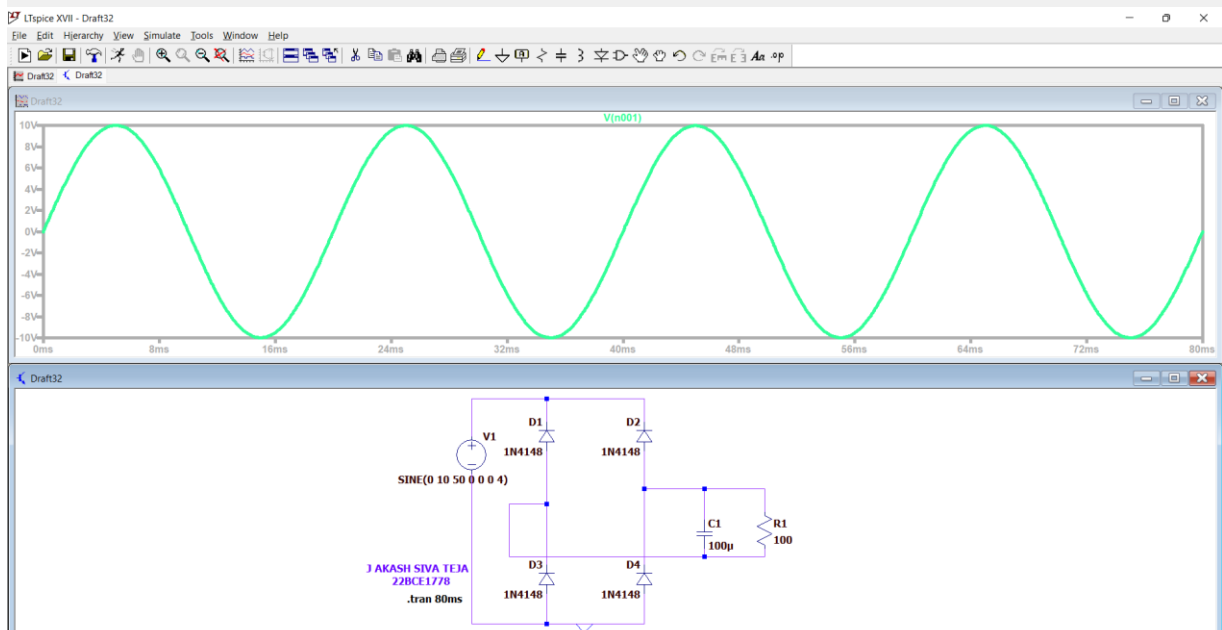
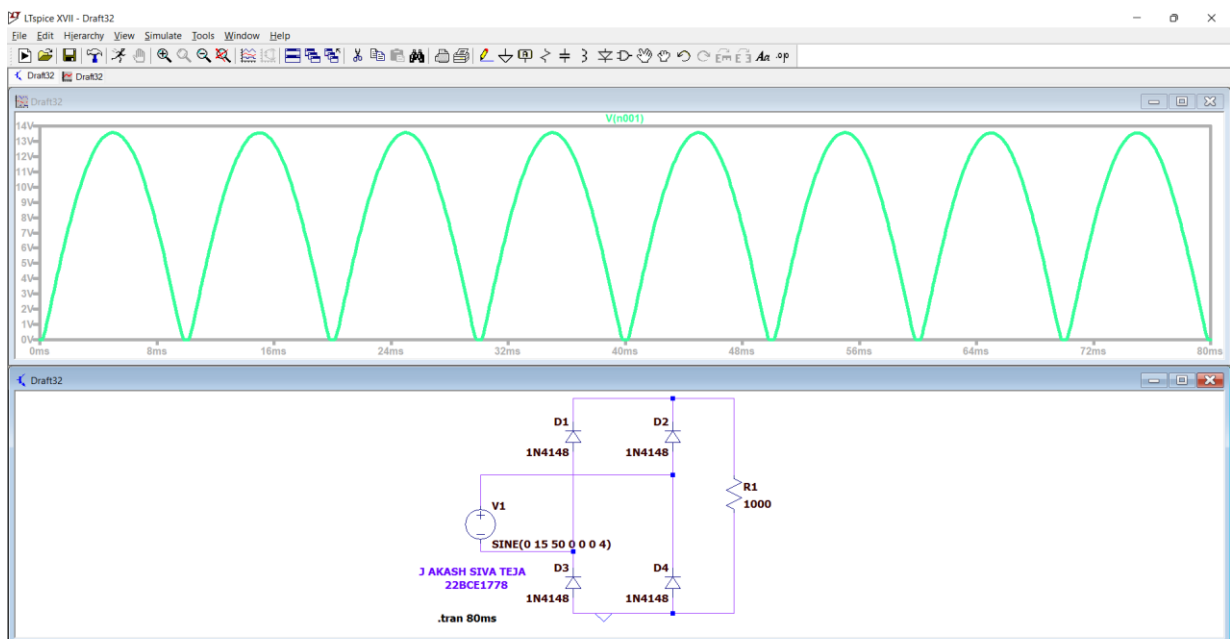


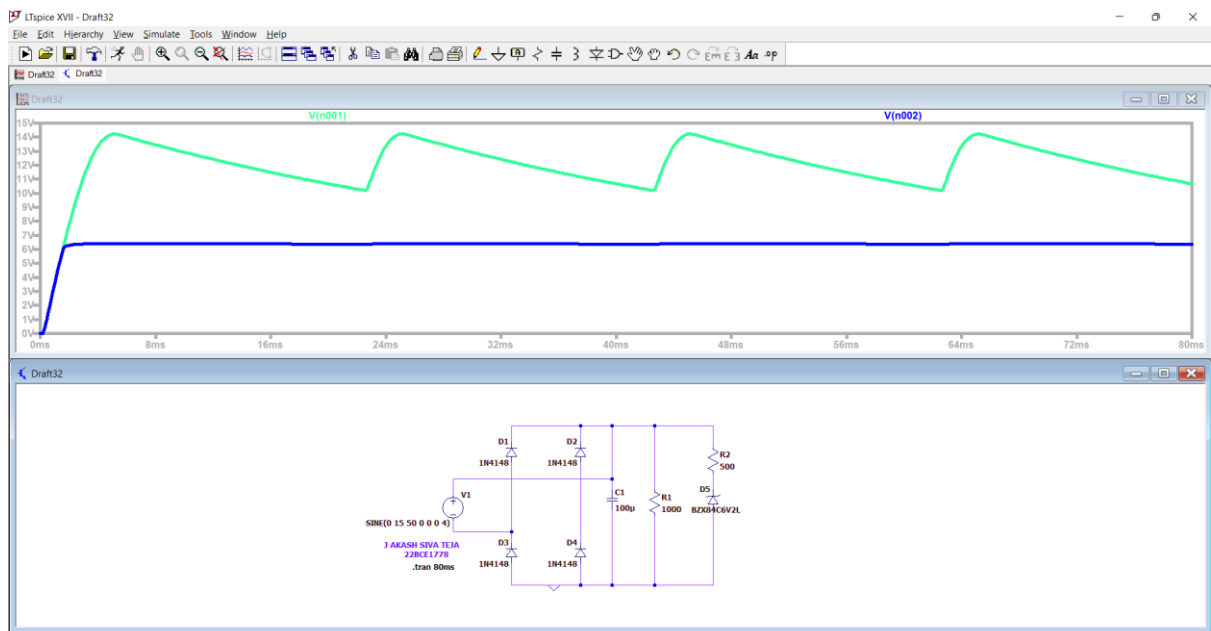
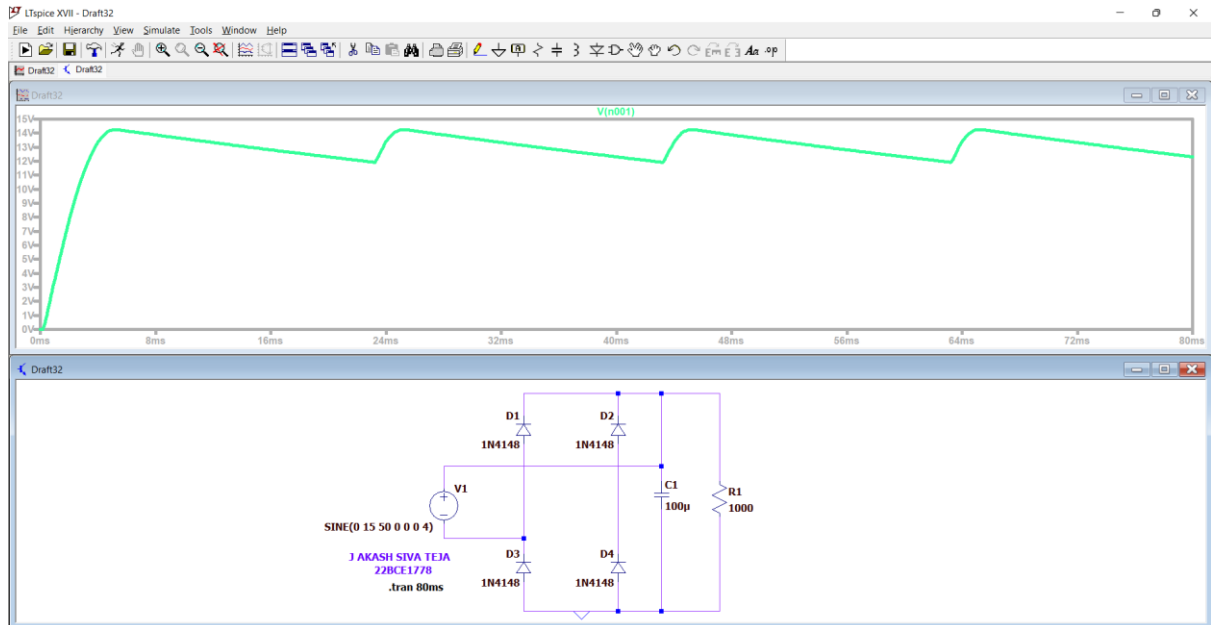
MAXIMUM POWER TRANSFER THEOREM USING LTspice:

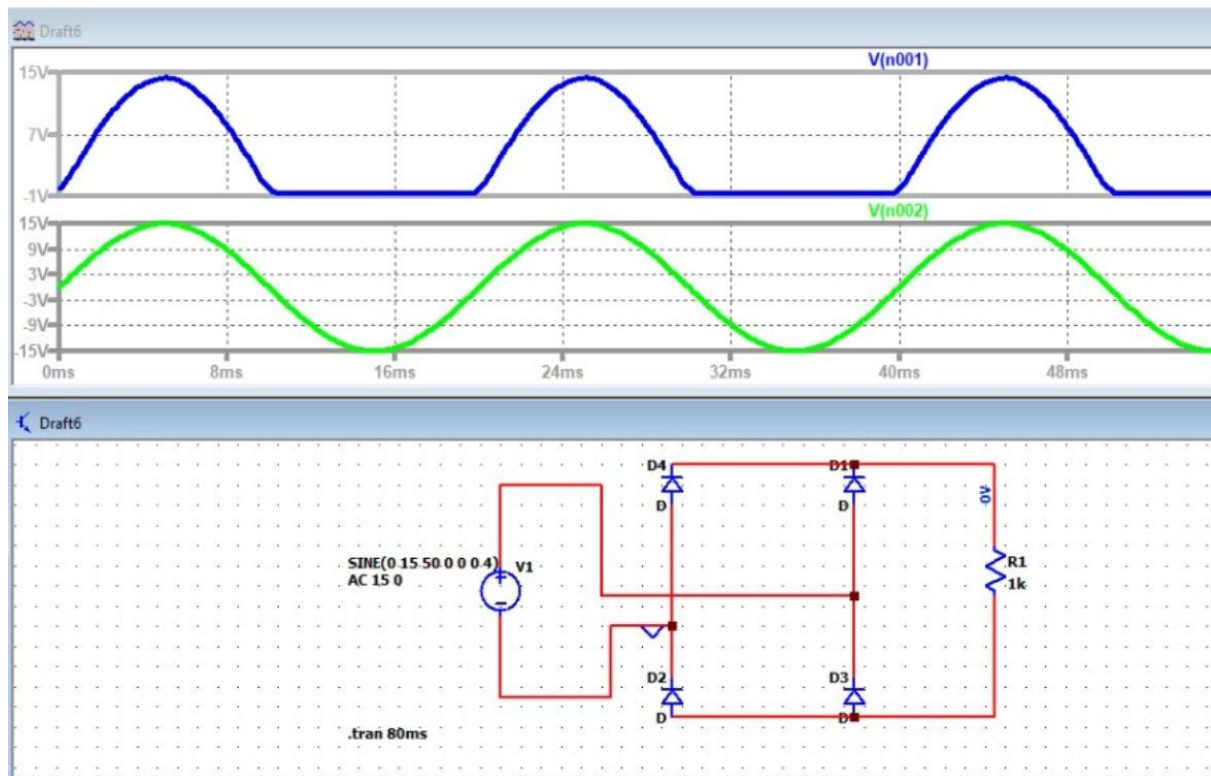
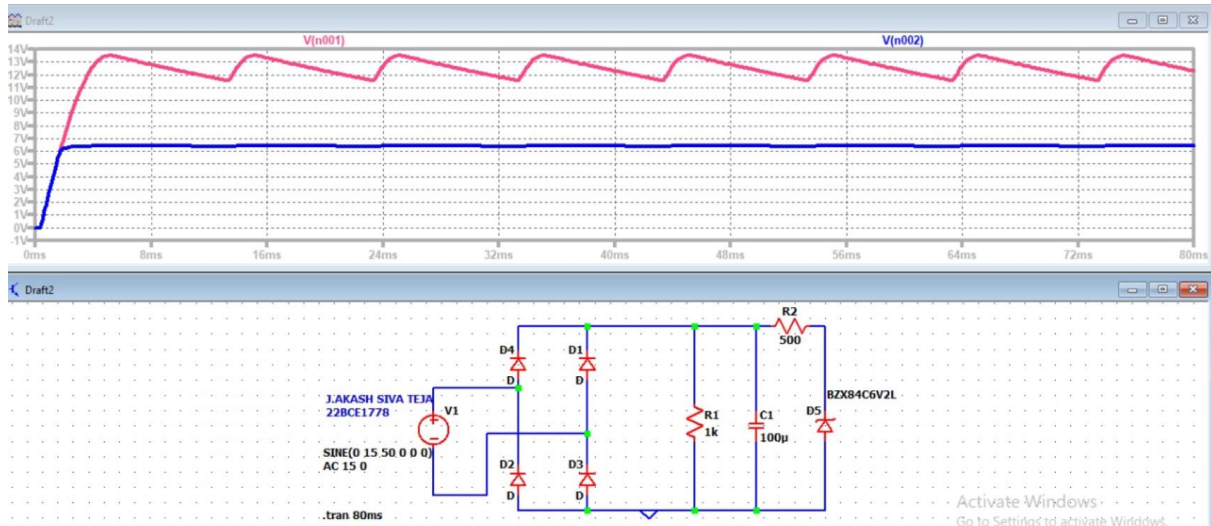


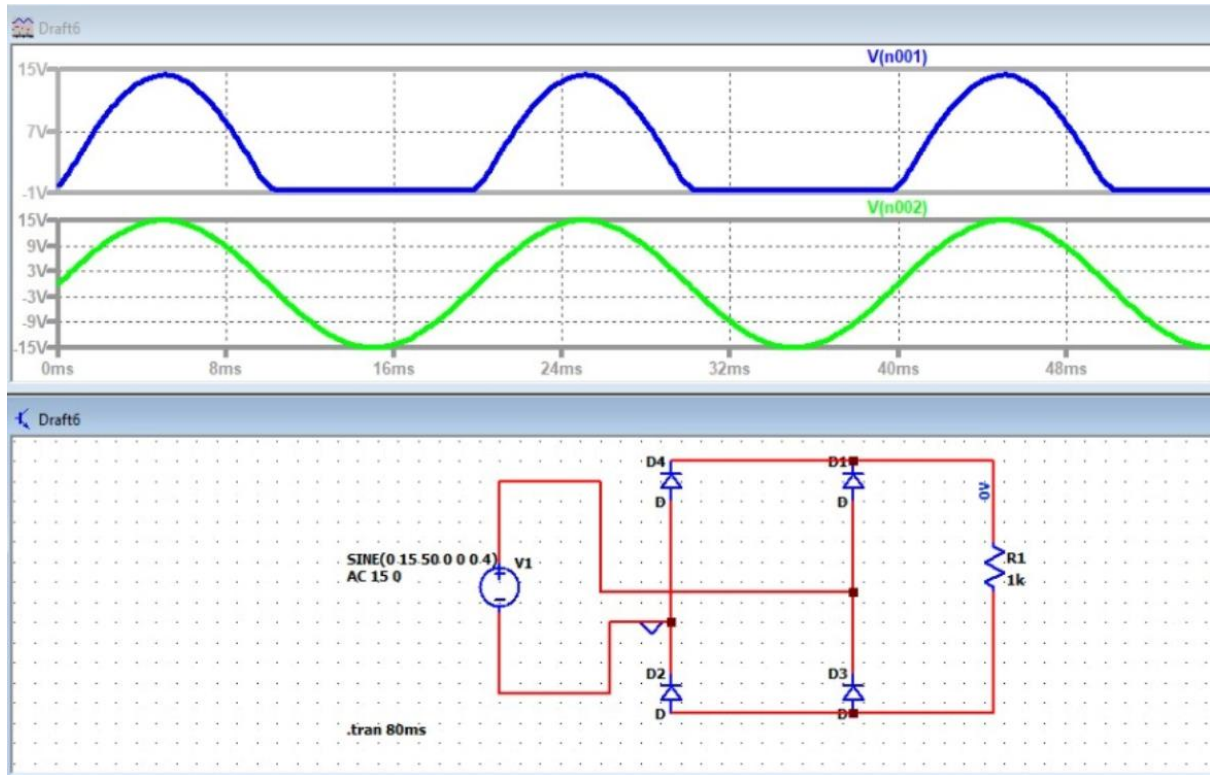


CHARACTERISTICS OF PN JUNCTION DIODE AND ZENER DIODE USING LTspice:









FULL ADDER AND HALF ADDER CIRCUITS USING LTspice:

