

## #Midterm Solutions#

①

Operating System: The software that acts as an intermediary between computer user and computer hardware.  
→ Software that manages the computer hardware  
Ex: UNIX, Windows, MAC OS, Linux,

Pipeline: mechanism for inter-process communication using message passing.

Ex:

- ②
- a) A multi-threaded process has two program counters per thread. **F**
  - b) The short-term scheduler controls the degree of pre-empting. **F**
  - c) Interrupt-driven I/O provides better performance when moving large amounts of data than DMA. **F**
  - d) \$@ symbol in a makefile represents the left side of the : symbol. **F?**
  - e) A child process can only be an orphan process while its parent can be either orphan or a zombie process. **F**
  - f) There must be a space character in the beginning of any command in a Makefile. **F**
  - g) When using fork system call, parent and child processes have the same address space. **T**
  - h) With NUMA, some parts of memory may take longer to access than other parts. **T**
  - i) CD-R and DVD-R are examples for WORM devices. **T**
  - j) CPU registers has faster access time than any other device including CPU Cache memory **T**
  - k) Privilege escalation allows user to change file access permission. **F**
  - l) Emulation used when source CPU type different from target type. **T**
  - m) Operations in Message Passing architecture is faster than shared memory architectures **F**

- ③
- a) A trap or exception is a software-generated interrupt caused either by an error or a user request.

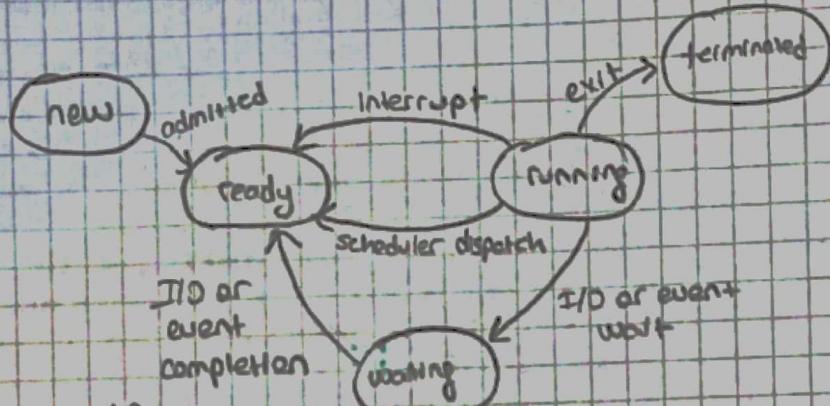
- b) When an interrupt occurs, the OS preserves the state of the CPU by storing every registers and the program counter.
- c) The load instruction moves a byte or word from main memory to an internal register within the CPU while the store instruction moves the content of a register to main memory.
- d) The sequence of steps that the CPU follows to process instructions is called as instruction cycle.
- e) In a multiprocessor environment all CPUs must have the most recent value in their cache which is known as cache coherence.
- f) In the context of Cloud Computing, Google Docs is an example for SaaS.
- g) One of the example Shell program in a Linux/UNIX system is Bash Shell.
- h) One method for system call parameter passing is to use registers.
- i) PID value of 1 is assigned to the init process on Linux systems.

④ core dump = Failure of an application can generate a core dump  
Capturing memory of process.

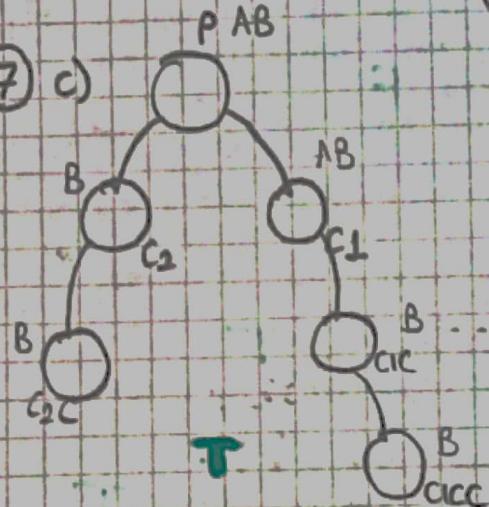
Crash dump = OS failure can generate a crash dump containing kernel memory.

⑤

⑥



⑦ c)



- a) A: 2 times  
b) B: 6 times T

⑧ Shared variables  $\Rightarrow$  int x = 2;  
int y = 0;

Process A

```
while (x == 2);  
printf("E");  
y = 1;  
y = 0;  
printf("m");  
y = 1;
```

Process B

```
printf("L");  
x = 1;  
while (y == 0);  
printf("A");
```

⑨

$P_1 =$   
T1;  
sleep(S);  
 $P_2 =$   
wait(S);  
T2;

## #Final Exam #

Pid	Submission Time	Required time
A	0	4
B	1	6
C	3	3
D	4	1

a) Round-Robin. Quantum of 2. Response, waiting and TAT for each process? → preemptive

A	B	C	D	A	B	C	B
0	2	6	7	9	11	12	14

### Waiting Times

$$A = 0 + 5 = 5$$

$$B = 1 + 5 + 1 = 7$$

$$C = 1 + 5 = 6$$

$$D = 2$$

### Completion Times

$$A = 9$$

$$B = 14$$

$$C = 12$$

$$D = 7$$

### Turn-Around-Times

$$A = 9$$

$$B = 13$$

$$C = 9$$

$$D = 3$$

$$A = 0$$

$$B = 1$$

$$C = 1$$

$$D = 2$$

• Turn around time = Required Time + Waiting Time

• Completion = Ready bits + Time

Response = First Time - Executed Arrival Time

b) Shortest-Job First? → Can be both

A	D	C	E
0	6	5	8

### Waiting Times

$$A = 0$$

$$B = 7$$

$$C = 2$$

$$D = 0$$

### Response Time

$$A = 0$$

$$B = 7$$

$$C = 2$$

$$D = 0$$

### TAT

$$A = 4$$

$$B = 13$$

$$C = 5$$

$$D = 1$$

### Completion Time

$$A = 4$$

$$B = 14$$

$$C = 8$$

$$D = 5$$

c) First Come First Serve → Non-preemptive

A	B	C	D
0	4	10	13 14

### Waiting Times

$$A = 0$$

$$B = 3$$

$$C = 7$$

$$D = 9$$

### Response Time

$$A = 0$$

$$B = 3$$

$$C = 7$$

$$D = 8$$

### TAT

$$A = 4$$

$$B = 9$$

$$C = 10$$

$$D = 10$$

### Completion Time

$$A = 4$$

$$B = 10$$

$$C = 13$$

$$D = 14$$

(2)

## Process

 $P_1$   
 $P_2$   
 $P_3$   
 $P_4$   
 $P_5$ 

	Burst				
$P_1$	10	5	0	...	
$P_2$	29	26	18	14	
$P_3$	3	0	0	4	
$P_4$	7	2	0	4	
$P_5$	12	7	2	0	

- a) FCFS
- 
- b) SJF
- 
- c) RR,
- $q=5$

	$P_1$	$P_2$	$P_3$	$P_4$	$P_5$	
	0	10	39	42	49	61

	Waiting Times	Response Times
$P_1$	0	0
$P_2$	10	10
$P_3$	39	39
$P_4$	42	12
$P_5$	49	49

	TAT	Completion Times
$P_1$	10	10
$P_2$	39	39
$P_3$	42	42
$P_4$	49	49
$P_5$	61	61

	$P_3$	$P_4$	$P_1$	$P_5$	$P_2$	
	0	3	10	20	32	61

	Waiting Times	Response Times
$P_1$	10	10
$P_2$	32	32
$P_3$	0	0
$P_4$	3	3
$P_5$	20	20

	TAT	Completion Times
$P_1$	20	20
$P_2$	61	61
$P_3$	3	3
$P_4$	10	10
$P_5$	32	32

	$P_1$	$P_2$	$P_3$	$P_4$	$P_5$	$P_1$	$P_2$	$P_3$	$P_4$	$P_5$	$P_1$	$P_2$	$P_3$	$P_4$	$P_5$
	0	5	10	13	18	23	28	33	35	40	45	47	52	57	61

## Waiting Times

$$\begin{aligned}
 P_1 &= 0 + 18 = 18 \\
 P_2 &= 5 + 18 + 7 + 2 = 32 \\
 P_3 &= 10 \\
 P_4 &= 13 + 15 = 28 \\
 P_5 &= 18 + 12 + 5 = 35
 \end{aligned}$$

## Response Times

$$\begin{aligned}
 P_1 &= 0 \\
 P_2 &= 5 \\
 P_3 &= 10 \\
 P_4 &= 13 \\
 P_5 &= 18
 \end{aligned}$$

## TAT

$$\begin{aligned}
 P_1 &= 28 \\
 P_2 &= 61 \\
 P_3 &= 13 \\
 P_4 &= 35 \\
 P_5 &= 47
 \end{aligned}$$

## Completion Times

$$\begin{aligned}
 P_1 &= 28 \\
 P_2 &= 61 \\
 P_3 &= 13 \\
 P_4 &= 35 \\
 P_5 &= 47
 \end{aligned}$$

## (3) Effective Access Time

 $\alpha = 0.9$  $E = 10 \text{ ns}$  for TLB

250 ns for memory access

 $\alpha = 0.9$  $E = 10 \text{ ns}$  $m = 250 \text{ ns}$ 

$$EAT = \alpha(E+m) + (1-\alpha)(E+m+m)$$

$$0.9(260) + (0.1)(270)$$

$$= \underline{\underline{285 \text{ ns}}}$$

Max

Allocated

	P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>	P <sub>4</sub>
R <sub>1</sub>	1	6	3	4
R <sub>2</sub>	2	1	1	2
R <sub>3</sub>	2	3	4	2

	P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>	P <sub>4</sub>
R <sub>1</sub>	1	6	2	0
R <sub>2</sub>	0	1	1	0
R <sub>3</sub>	0	2	1	2

Available

Total

R <sub>1</sub>	0
R <sub>2</sub>	1
R <sub>3</sub>	1

R <sub>1</sub>	9
R <sub>2</sub>	3
R <sub>3</sub>	6

- a) Need matrix?  
b) Is the system safe?

Need

	P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>	P <sub>4</sub>
R <sub>1</sub>	0	0	1	4
R <sub>2</sub>	2	0	0	2
R <sub>3</sub>	2	1	3	0

b) Available

	R <sub>1</sub>	R <sub>2</sub>	R <sub>3</sub>
P <sub>2</sub>	0	1	1
P <sub>3</sub>	6	2	3
P <sub>4</sub>	8	3	4
P <sub>1</sub>	9	3	6

$\left\langle P_2, P_3, P_4, P_1 \right\rangle$   
↳ Safe.

Allocation

	A	B	C	D	E
P <sub>0</sub>	2	0	0	1	0
P <sub>1</sub>	0	1	2	1	1
P <sub>2</sub>	2	1	0	3	1
P <sub>3</sub>	1	3	1	0	2
P <sub>4</sub>	1	4	3	2	2

Max

	A	B	C	D	E
P <sub>0</sub>	4	1	1	2	3
P <sub>1</sub>	8	2	5	2	3
P <sub>2</sub>	2	3	1	7	3
P <sub>3</sub>	1	4	2	4	4
P <sub>4</sub>	3	6	6	5	4

Available

	A	B	C	D	E
P <sub>1</sub>	3	3	3	1	2
P <sub>0</sub>	5	4	5	3	3
P <sub>4</sub>	6	8	8	5	5
P <sub>2</sub>	8	9	8	8	6
P <sub>3</sub>	9	12	9	8	8

- a) Total matrix?  
b) How many resource are there for each resource type in this system?  
c) If a request from P<sub>4</sub> arrives for (0,0,2,0,1) can the request be granted immediately?

Need

	A	B	C	D	E
P <sub>0</sub>	2	2	1	1	3
P <sub>1</sub>	3	1	3	1	2
P <sub>2</sub>	0	2	1	4	2
P <sub>3</sub>	0	1	1	4	2
P <sub>4</sub>	2	2	3	3	2

a) 9, 12, 9, 8, 8 for A, B, C, D, E respectively.

b) Yes.  $\left\langle P_1, P_0, P_4, P_2, P_3 \right\rangle$  is safe sequence.

c) Request  $\leq$  need  $\rightarrow$  available resource  
request  $\leq$  allocation  $\rightarrow$  request time.

? { allocation  $\leftarrow$  request  
need  $-$  request  
It can easily granted }

## ⑥ Prioritized round robin.

a) Suppose the scheduler gives higher priority to processes that have larger quanta. Is starvation possible in the system?

→ No. Although priority scheduling may cause starvation, we have a round-robin. So when higher priority processes are done with q<sub>i</sub> or new processes added, they will be added to the end of the ready queue. (If process has remaining time, of course.) So at some point, lower priority will be executed.

b) Suppose instead that scheduler gives higher priority to processes that have smaller quanta. Is starvation possible in the system?

→ No. The same reason above. Each process is given quanta for execution or fixed time to execute.

⑦

- 1-) Only one writer writes at a time.
- 2-) While writing, reading is not allowed.
- 3-) While reading, writing is not allowed.
- 4-) Up to 5 readers can read at the same time.

### Writer Process

```
wait (wif);  
writing is performed  
signal (wif);
```

### Reader Process

```
wait (mutex);  
read count ++;  
if read count = 1 then wait (wif)  
signal (mutex);  
reading is performed  
wait (mutex);  
read count --;  
if read count = 0 then signal (wif);  
signal (mutex);
```

wait (mutex),

read count ++;

if read count = 1

wait (wif);

if read count <= 5

signal (mutex)

{ if mutex = 1 }  
wait (mutex)

read count --;

if (read count = 0)

signal (wif)

signal (mutex)

(8)

Female - 3 toilet  
Male - 2 toilet

2. Male toilet occurs.

Eğerki yoksa kadın kullanır.  
Kadın yoksa erkek "

Wait (semaphore)

Signal (semaphore)

int Sem (semaphore, value);  
getValue (value)

while (+true) {

    Wait (female);

    femaleCount ++;

    if (femaleCount == 1)

        Wait (male);

        Signal (female);

    Wait (female);

    femaleCount --;

    if (femaleCount == 0)

        Signal (male);

        Signal (female);

}

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