

AKHIL D R

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Objective

I am looking forward to an opportunity where I can utilize my skills and contribute effectively to the success of the organization and further improve my personal skills.



Education & Certification

- 2018 BE - Electronics & Communication**
Visveswaraya Technological University,
Kammavari Sangham Institute of Technology,
Bangalore
Attained an aggregate of 61.29%
- 2014 PU - PCME**
Sri Kumaran Children's Home Composite Junior
College, Bangalore
Attained an aggregate of 70%
- 2012 10th - Karnataka Secondary Education
Examination Board**
New Baldwin International Residential School,
Bangalore
Attained an aggregate of 87.8%



Achievements

- Secured silver medal in State level athletics (4x100m) relay representing Karnataka State in 2012
- I was awarded 1st place in Technical Quiz at college which was conducted by IEEE



Declaration

I hereby declare that the above information is correct to the best of my knowledge and belief



Technical Skills

Programming : Basics of C, OOPS concept,
Skills Digital, Verilog

Tools : ISE - Xilinx



VLSI Project

Router 1x3 – RTL design and Verification

HDL: Verilog
EDA Tools: ISE

Description: The router accepts data packets on a single 8-bit port and routes them to one of the three output channels, channel0, channel1 and channel2

Responsibilities:

- Architected the design
- Implemented RTL using Verilog HDL.
- Synthesized the design



Project Undertaken

Project Name: Wireless Power Transmission

Description: The main concept of the project is to charge a device without any physical wires or cords. Using antennas has tested and got few results by further improving the distance between transmitting antenna and receiving antenna and even improving the load at receiver.