

1. What are the minimum numbers of *NOT* gates and 2-input *OR* gates required to design the logic of the driver for this 7-segment display?
- (A) 3*NOT* and 4*OR*
 - (B) 2*NOT* and 4*OR*
 - (C) 1*NOT* and 3*OR*
 - (D) 2*NOT* and 3*OR*