```
system.cpu.commit.op class 0::SimdAlu ≤ 62.0
                     gini = 0.360608
                     samples = 208
                    value = [191, 59]
                     class = benign
                                       False
           True
                     system.l2.ReadExReq mshr miss rate::total ≤ 0.99007
   aini = 0.0
                                         gini = 0.094552
 samples = 42
                                         samples = 166
 value = [0, 49]
                                         value = [191, 10]
class = malicous
                                          class = benign
                                            system.cpu.iew.iewExecSquashedInsts ≤ 1032.5
                         gini = 0.0
                                                             gini = 0.03045
                        samples = 7
                                                            samples = 159
                       value = [0, 7]
                                                            value = [191, 3]
                      class = malicous
                                                             class = benian
                                                     gini = 0.0
                                                                          aini = 0.0
                                                   samples = 3
                                                                       samples = 156
                                                   value = [0, 3]
                                                                       value = [191, 0]
                                                 class = malicous
                                                                       class = benign
```