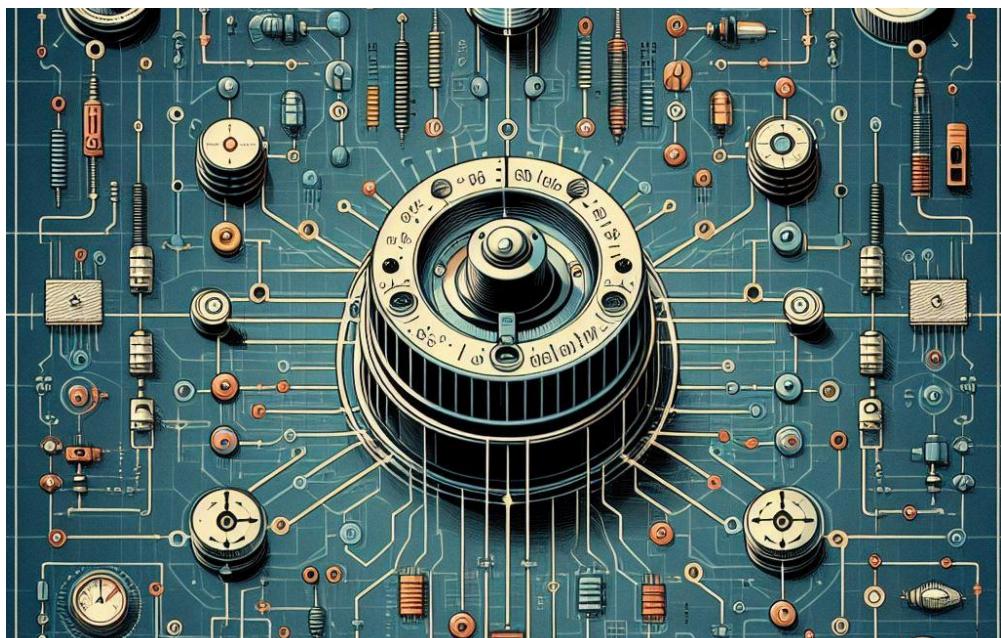


Assignment of Digital Logic Design

3-bit Up/Down Synchronous Counter



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