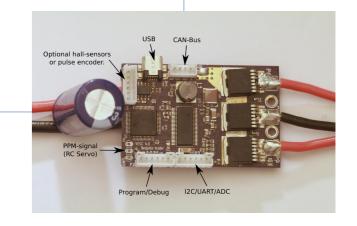




How are these three wires connected?



			ODROID-XU4 B	LOC	K DIAGRAM					
Exynos 5422 Application Processor										
USB2.0 Host Type A	USB 2.0 Host	CF Cortex=A15 Quad (2.0 GHz)		Cortex-A7 Quad (1.4GHz)				USB 3.0 #0	USB 3.0 Hub	2 x USB 3.0 Host Type A
eMMC 0 Module Socket 8bit	eMMC 5.0 8bit	Cortex-A15 32KB/32KB I/D-Cache NEONv2 + VFPv4	Cortex-A15 32KB/32KB I/D-Cache NEONv2 + VFPv4		Cortex-A7 B/32KB I/D-Cache EONv2 + VFPv4	Cortex-A7 32KB/32KB I/D-Cache NEONv2 + VFPv4		USB 3.0 #1	Gigabit Ethernet Controller	Ethernet 10/100/1000
Micro SD Slot MMC 2	SD3.0 Host	Cortex-A15 32KB/32KB I/D-Cache NEONv2 + VFPv4	Cortex-A15 32KB/32KB I/D-Cache NEONv2 + VFPv4	-	Cortex=A7 B/32KB I/D=Cache EONv2 + VFPv4		Cortex-A7 B/32KB I/D-Cache EONv2 + VFPv4	12C #4	PMIC	DC 5V/4A
Serial Console	UART#2	SCU ar	SCU and ACP		SCU					
		2MB L2-Cache with ECC			512KB L2-Cache			PWM		- Cooling Fan
I/O expansion Port (30pin)	I2C #1	128-bit AMBA ACE Coherent Bus interface		12	128-bit AMBA ACE Coherent Bus interface					HDMI
	UART #0	Multimedia			DRAM			HDMI		Type-A
	SPI#1	GPU ARM Mali-T628 MP6 (600MHz) OpenCL 1.1 Full profile OpenGLES 1.1, 2.0 and 3.0			JPEG	Ш	LPDDR3 933MHz	I2S		
	ADC			Enc/Dec MFC 1080p 60 Enc/De		4	32bit 2-port 14.9Gbytes/sec	125		I/O expansion Port
	GPIO					ec	2GByte PoP	I2C #5		(12pin)



