

RX Family

R01AN1683EJ0160

Rev.1.60

Jan. 29, 2016

BYTEQ Module Using Firmware Integration Technology

Introduction

This module provides functions for creating and maintaining byte-based circular buffers.

Target Device

The following is a list of devices that are currently supported by this API:

- All RX MCUs

When using this application note with other Renesas MCUs, careful evaluation is recommended after making modifications to comply with the alternate MCU.

Related Documents

- Firmware Integration Technology User's Manual (R01AN1833EU)
- Board Support Package Firmware Integration Technology Module (R01AN1685EJ)
- Adding Firmware Integration Technology Modules to Projects (R01AN1723EU)
- Adding Firmware Integration Technology Modules to CS+ Projects (R01AN1826EJ)

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1. Overview

The Byte Queue (BYTEQ) module provides basic circular buffer services for buffers provided by the application.

The module allocates a Queue Control Block (QCB) for each buffer passed to the Open() function. A QCB maintains the buffer's "in" and "out" indexes for adding and removing data from the queue. The Queue Control Blocks can be allocated statically at compile time or dynamically at run time (using malloc). An equate in config.h determines whether they are statically or dynamically created. If they are statically allocated, an additional equate is utilized which specifies the maximum number of buffers to be supported.

There is one control block per buffer. When an R_BYTEQ_Open() is performed, a pointer to the application's buffer and its length are passed in, and a pointer to a QCB is provided. This pointer, which is called a Handle, is then passed to all of the other API functions. The functions then operate on the queue referenced by this Handle. Because there is no global or static data shared between the queues, the API functions are re-entrant for different queues.

This module does not make use of any interrupts. If a queue can be modified at both the interrupt and application level, it is up to the application to ensure that the appropriate related interrupt is disabled whenever the queue is being accessed. Similarly, if the queue is accessed by tasks of different priorities, it is up to the user to prevent task switching or to utilize a mutex or semaphore to reserve the queue.

1.1 Using the BYTEQ Module

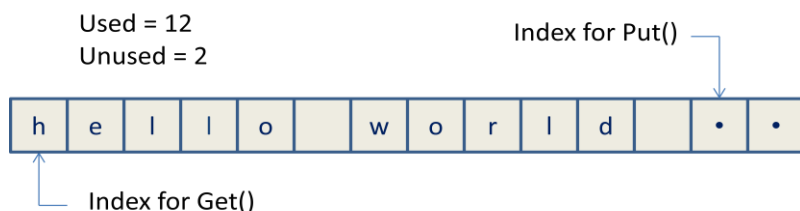
The following illustrates a queue's behavior with API calls:

```
#define BUFSIZE 14

uint8_t      my_buf[BUFSIZE];
byteq_hdl_t  my_que;
byteq_err_t   err;
uint8_t      byte;

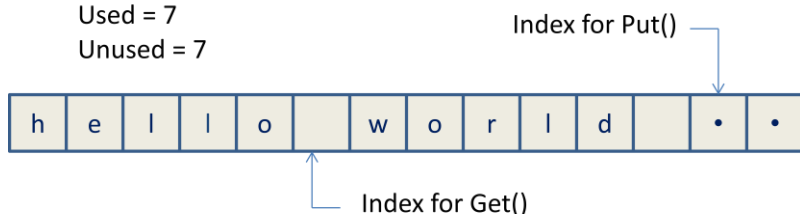
err = R_BYTEQ_Open(my_buf, BUFSIZE, &my_que);

// add 12 bytes to queue
R_BYTEQ_Put(my_que, 'h');
R_BYTEQ_Put(my_que, 'e');
R_BYTEQ_Put(my_que, 'l');
R_BYTEQ_Put(my_que, 'l');
R_BYTEQ_Put(my_que, 'o');
R_BYTEQ_Put(my_que, ' ');
R_BYTEQ_Put(my_que, 'w');
R_BYTEQ_Put(my_que, 'o');
R_BYTEQ_Put(my_que, 'r');
R_BYTEQ_Put(my_que, 'l');
R_BYTEQ_Put(my_que, 'd');
R_BYTEQ_Put(my_que, ' ');
```



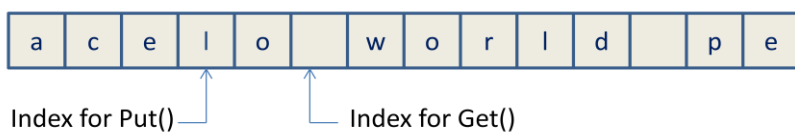
```
// remove 5 bytes from queue
R_BYTEQ_Get(my_que, &byte);    // byte = 'h'
R_BYTEQ_Get(my_que, &byte);    // byte = 'e'
R_BYTEQ_Get(my_que, &byte);    // byte = 'l'
R_BYTEQ_Get(my_que, &byte);    // byte = 'l'
R_BYTEQ_Get(my_que, &byte);    // byte = 'o'
```

Used = 7
Unused = 7



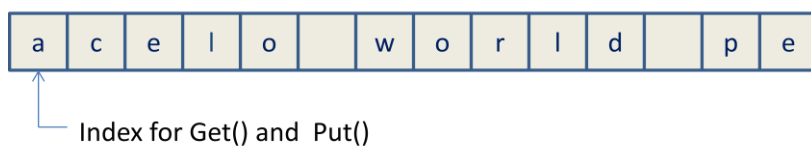
```
// add 5 bytes to queue
R_BYTEQ_Put(my_que, 'p');
R_BYTEQ_Put(my_que, 'e');
R_BYTEQ_Put(my_que, 'a');
R_BYTEQ_Put(my_que, 'c');
R_BYTEQ_Put(my_que, 'e');
```

Used = 12
Unused = 2



```
// flush queue
R_BYTEQ_Flush(my_que);
```

Used = 0
Unused = 14



2. API Information

This Driver API follows the Renesas API naming standards.

2.1 Hardware Requirements

No hardware requirements.

2.2 Software Requirements

This driver is dependent upon the following FIT packages:

- Renesas Board Support Package (r_bsp) v3.10or higher

2.3 Limitations

No software limitations.

2.4 Supported Toolchains

This driver is tested and working with the following toolchains:

- Renesas RX Toolchain v.2.04.01

2.5 Header Files

Compile time configurable options are located in `r_byteq\ref\r_byteq_config_reference.h`. This file should be copied into the `r_config` subdirectory of the project and renamed to `r_byteq_config.h`. It is this renamed file that should be modified if needed and the original kept as a reference.

All API calls and their supporting interface definitions are located in `r_byteq\r_byteq_if.h`. Both this file and `r_byteq_config.h` should be included by the User's application.

2.6 Integer Types

If your toolchain supports C99 then `stdint.h` should be described as shown below. If not, then there should be `typedefs.h` file that is included with your project as defined by the Renesas Coding Standards document.

This project uses ANSI C99 "Exact width integer types" in order to make the code clearer and more portable. These types are defined in `stdint.h`.

2.7 Configuration Overview

All configurable options that can be set at build time are located in the file “r_byteq_config.h”. A summary of these settings are provided in the following table:

Configuration options in <i>r_byteq_config.h</i>	
<pre>#define BYTEQ_CFG_PARAM_CHECKING_ENABLE</pre>	= 1: Include parameter checking in the build. = 0: Omit parameter checking from the build. = BSP_CFG_PARAM_CHECKING_ENABLE (default): Use the system default setting. Note: Code size can be reduced by excluding parameter checking from the build.
<pre>#define BYTEQ_CFG_USE_HEAP_FOR_CTRL_BLKs 0</pre>	A control block is needed for each queue to maintain in/out indexes. By default, these control blocks are allocated at compile time. To dynamically allocate memory at run time, set this equate to 1.
<pre>#define BYTEQ_CFG_MAX_CTRL_BLKs 4</pre>	Specifies how many control blocks to allocate at compile time. This constant is ignored if BYTEQ_CFG_USE_HEAP_FOR_CTRL_BLKs is 1.

2.8 Code Size

The code size is based on optimization level 2 and optimization type set for size for the supported toolchain. The ROM (code and constants) and RAM (global data) sizes are determined by the build-time configuration options set in the module configuration header file.

ROM and RAM code sizes		
	With Parameter Checking	Without Parameter Checking
Using Heap for Control Blocks	ROM: 259 bytes	ROM: 170 bytes
	RAM: 12 bytes for malloc() x Control Blocks	RAM: 12 bytes for malloc() x Control Blocks
Using Allocated Control Blocks	ROM: 300 bytes	ROM: 208 bytes
	RAM: 1 byte + 12 bytes x BYTEQ_CFG_MAX_CTRL_BLKs	RAM: 1 byte + 12bytes x BYTEQ_CFG_MAX_CTRL_BLKs

2.9 Adding the Module to Your Project

The FIT module must be added to each project in the e² studio.

You can use the FIT plug-in to add the FIT module to your project, or the module can be added manually.

It is recommended to use the FIT plug-in as you can add the module to your project easily and also it will automatically update the include file paths for you.

To add the FIT module using the plug-in, refer to chapter 2. “Adding FIT Modules to e² studio Projects Using FIT Plug-In” in the “Adding Firmware Integration Technology Modules to Projects” application note (R01AN1723EU).

To add the FIT module manually, refer to chapter 3. “Adding FIT Modules to e² studio Projects Manually” in the “Adding Firmware Integration Technology Modules to Projects (R01AN1723EU)”

When using the FIT module, the BSP FIT module also needs to be added. For details on the BSP FIT module, refer to the “Board Support Package Module Using Firmware Integration Technology” application note (R01AN1685EJ).

3. API Functions

3.1 Summary

The following functions are included in this design:

Function	Description
R_BYTEQ_Open()	Allocates and initializes a queue control block for a buffer provided by the user. Provides a queue handle for use with other API functions.
R_BYTEQ_Close()	Releases the queue control block associated with the handle.
R_BYTEQ_Put()	Adds a byte of data to the queue.
R_BYTEQ_Get()	Removes the oldest byte of data from the queue.
R_BYTEQ_Flush()	Resets the queue to an empty state.
R_BYTEQ_Used()	Provides the number of bytes used in the queue.
R_BYTEQ_Unused()	Provides the number of bytes unused in the queue.
R_BYTEQ_GetVersion()	Returns at runtime the module version number.

3.2 Return Values

These are the different error codes API functions can return. The enum is found in `r_byteq_if.h` along with the API function declarations.

```
typedef enum _byteq_err          // BYTEQ API error codes
{
    BYTEQ_SUCCESS = 0,
    BYTEQ_ERR_NULL_PTR,          // received null ptr; missing required arg
    BYTEQ_ERR_INVALID_ARG,       // argument is not valid for parameter
    BYTEQ_ERR_MALLOC_FAIL,       // cannot allocate mem for ctrl block;
                                // increase heap
    BYTEQ_ERR_NO_MORE_CTRL_BLKs, // no more ctrl blocks;
                                // increase BYTEQ_MAX_CTRL_BLKs
    BYTEQ_ERR_QUEUE_FULL,        // queue full; cannot add another byte
    BYTEQ_ERR_QUEUE_EMPTY       // queue empty; no byte to fetch
} byteq_err_t;
```

3.3 R_BYTEQ_Open()

This function allocates and initializes a queue control block for a buffer provided by the user. A queue handle is provided for use with other API functions.

Format

```
byteq_err_t R_BYTEQ_Open(uint8_t * const    p_buf,  
                        uint16_t const    size,  
                        byteq_hdl_t * const p_hdl)
```

Parameters

p_buf

Pointer to byte buffer.

size

Buffer size in bytes.

p_hdl

Pointer to a handle for queue (value set here)

Return Values

BYTEQ_SUCCESS:

Successful; queue initialized

BYTEQ_ERR_NULL_PTR:

p_buf is NULL

BYTEQ_ERR_INVALID_ARG:

Size is less than or equal to 1.

BYTEQ_ERR_MALLOC_FAIL:

Cannot allocate control block. Increase heap size.

BYTEQ_ERR_NO_MORE_CTRL_BLKs:

*Cannot assign control block.
Increase BYTEQ_MAX_CTRL_BLKs in config.h.*

Properties

Prototyped in file “r_byteq_if.h”

Description

This function allocates or assigns a queue control block for the buffer pointed to by *p_buf*. Initializes the queue to an empty state and provides a Handle to its control structure in *p_hdl* which is then used as a queue ID for the other API functions.

Reentrant

Function is re-entrant for different buffers.

Example

```
#define BUFSIZE 80  
  
uint8_t      tx_buf[BUFSIZE];  
byteq_hdl_t  tx_que;  
byteq_err_t  byteq_err;  
  
byteq_err = R_BYTEQ_Open(tx_buf, BUFSIZE, &tx_que);
```

Special Notes:

None.

3.4 R_BYTEQ_Close()

This function releases the queue control block associated with a handle.

Format

```
byteq_err_t R_BYTEQ_Close(byteq_hdl_t const hdl)
```

Parameters

hdl

Handle for queue.

Return Values

BYTEQ_SUCCESS: *Successful; control block released.*

BYTEQ_ERR_NULL_PTR: *hdl is NULL.*

Properties

Prototyped in file “r_byteq_if.h”

Description

If the control block associated with this Handle was allocated dynamically at run time (BYTEQ_USE_HEAP_FOR_CTRL_BLKs set to 1 in config.h), then that memory is free()d by this function. If the control block was statically allocated at compile time (BYTEQ_USE_HEAP_FOR_CTRL_BLKs set to 0 in config.h), then this function marks the control block as available for use by another buffer. Nothing is done to the contents of the buffer referenced by this Handle.

Reentrant

Function is re-entrant for different queues.

Example

```
byteq_hdl_t    tx_que;  
byteq_err_t    byteq_err;  
  
byteq_err = R_BYTEQ_Open(tx_buf, BUFSIZE, &tx_que);  
  
byteq_err = R_BYTEQ_Close(tx_que);
```

Special Notes:

None.

3.5 R_BYTEQ_Put()

This function adds a byte of data to the queue.

Format

```
byteq_err_t R_BYTEQ_Put(byteq_hdl_t const hdl,
                        uint8_t const byte)
```

Parameters

hdl

Handle for queue.

byte

Byte to add to queue.

Return Values

BYTEQ_SUCCESS: Successful; byte added to queue

BYTEQ_ERR_NULL_PTR: *hdl* is NULL.

BYTEQ_ERR_QUEUE_FULL Queue full; cannot add byte to queue.

Properties

Prototyped in file “r_byteq_if.h”

Description

This function adds the contents of *byte* to the queue associated with *hdl*.

Reentrant

Function is re-entrant for different queues.

Example

```
byteq_hdl_t tx_que;
byteq_err_t byteq_err;
uint8_t byte = 'A';

byteq_err = R_BYTEQ_Open(tx_buf, BUFSIZE, &tx_que);
byteq_err = R_BYTEQ_Put(tx_que, byte);
```

Special Notes:

If the queue is accessed at both the interrupt and application level, it is up to the user to disable and enable the associated interrupt before and after calling this function from the application level. If the queue is accessed by tasks of different priorities, it is up to the user to prevent task switching or to utilize a mutex or semaphore to reserve the queue.

3.6 R_BYTEQ_Get()

This function removes a byte of data from the queue.

Format

```
byteq_err_t R_BYTEQ_Get(byteq_hdl_t const hdl,  
                        uint8_t * const p_byte)
```

Parameters

hdl

Handle for queue.

p_byte

Pointer to load byte to.

Return Values

<i>BYTEQ_SUCCESS:</i>	<i>Successful; byte removed from queue</i>
<i>BYTEQ_ERR_NULL_PTR:</i>	<i>hdl is NULL.</i>
<i>BYTEQ_ERR_INVALID_ARG:</i>	<i>p_byte is NULL.</i>
<i>BYTEQ_ERR_QUEUE_EMPTY:</i>	<i>Queue empty; no data available to fetch</i>

Properties

Prototyped in file “r_byteq_if.h”

Description

This function removes the oldest byte of data in the queue associated with *hdl* and loads it into the location pointed to by *p_byte*.

Reentrant

Function is re-entrant for different queues.

Example

```
byteq_hdl_t rx_que;  
byteq_err_t byteq_err;  
uint8_t byte;  
  
byteq_err = R_BYTEQ_Open(rx_buf, BUFSIZE, &rx_que);  
  
/* queue filled with data by R_BYTEQ_Put() elsewhere */  
  
byteq_err = R_BYTEQ_Get(rx_que, &byte);
```

Special Notes:

If the queue is accessed at both the interrupt and application level, it is up to the user to disable and enable the associated interrupt before and after calling this function from the application level. If the queue is accessed by tasks of different priorities, it is up to the user to prevent task switching or to utilize a mutex or semaphore to reserve the queue.

3.7 R_BYTEQ_Flush()

This function resets a queue to an empty state.

Format

byteq_err_t R_BYTEQ_Flush(byteq_hdl_t const hdl)

Parameters

hdl

Handle for queue.

Return Values

BYTEQ_SUCCESS: *Successful; queue reset*

BYTEQ_ERR_NULL_PTR: *hdl is NULL.*

Properties

Prototyped in file “r_byteq_if.h”

Description

This function resets the queue identified by *hdl* to an empty state.

Reentrant

Function is re-entrant for different queues.

Example

```
byteq_hdl_t rx_que;
byteq_err_t byteq_err;

byteq_err = R_BYTEQ_Open(rx_buf, BUFSIZE, &rx_que);

/* queue filled with data by R_BYTEQ_Put()elsewhere */

byteq_err = R_BYTEQ_Flush(rx_que);
```

Special Notes:

If the queue is accessed at both the interrupt and application level, it is up to the user to disable and enable the associated interrupt before and after calling this function from the application level. If the queue is accessed by tasks of different priorities, it is up to the user to prevent task switching or to utilize a mutex or semaphore to reserve the queue.

3.8 R_BYTEQ_Used()

This function provides the number of data bytes in the queue.

Format

```
byteq_err_t R_BYTEQ_Used(byteq_hdl_t const hdl,  
                        uint16_t * const p_cnt)
```

Parameter

hdl

Handle for queue.

p_cnt

Pointer to load queue data count to.

Return Values

BYTEQ_SUCCESS: Successful; **p_cnt* loaded with the number of bytes in the queue

BYTEQ_ERR_NULL_PTR: *hdl* is NULL.

BYTEQ_ERR_INVALID_ARG: *p_cnt* is NULL.

Properties

Prototyped in file “r_byteq_if.h”

Description

This function loads the number of bytes in the queue associated with *hdl* and into the location pointed to by *p_cnt*.

Reentrant

Yes.

Example

```
byteq_hdl_t rx_que;  
byteq_err_t byteq_err;  
uint16_t count;  
  
byteq_err = R_BYTEQ_Open(rx_buf, BUFSIZE, &rx_que);  
  
/* queue filled with data by R_BYTEQ_Put() elsewhere */  
  
byteq_err = R_BYTEQ_Used(rx_que, &count);
```

Special Notes:

If the queue is accessed at both the interrupt and application level, it is up to the user to disable and enable the associated interrupt before and after calling this function from the application level. If the queue is accessed by tasks of different priorities, it is up to the user to prevent task switching or to utilize a mutex or semaphore to reserve the queue.

3.9 R_BYTEQ_Unused()

This function provides the number of data bytes available for storage in the queue.

Format

```
byteq_err_t R_BYTEQ_Unused(byteq_hdl_t const hdl,  
                           uint16_t * const p_cnt)
```

Parameters

hdl

Handle for queue.

p_cnt

Pointer to load queue unused byte count to.

Return Values

BYTEQ_SUCCESS: *Successful; *p_cnt loaded with the number of bytes not used in the queue*

BYTEQ_ERR_NULL_PTR: *hdl is NULL.*

BYTEQ_ERR_INVALID_ARG: *p_cnt is NULL.*

Properties

Prototyped in file “r_byteq_if.h”

Description

This function loads the number of unused bytes in the queue associated with *hdl* and into the location pointed to by *p_cnt*.

Reentrant

Yes.

Example

```
byteq_hdl_t tx_que;  
byteq_err_t byteq_err;  
uint16_t count;  
  
byteq_err = R_BYTEQ_Open(tx_buf, BUFSIZE, &tx_que);  
  
/* queue filled with data by R_BYTEQ_Put() elsewhere */  
  
byteq_err = R_BYTEQ_Unused(tx_que, &count);
```

Special Notes:

If the queue is accessed at both the interrupt and application level, it is up to the user to disable and enable the associated interrupt before and after calling this function from the application level. If the queue is accessed by tasks of different priorities, it is up to the user to prevent task switching or to utilize a mutex or semaphore to reserve the queue.

3.10 R_BYTEQ_GetVersion()

This function returns the driver version number at runtime.

Format

uint32_t R_BYTEQ_GetVersion(void)

Parameters

None

Return Values

Version number.

Properties

Prototyped in file “r_byteq_if.h”

Description

Returns the version of this module. The version number is encoded such that the top 2 bytes are the major version number and the bottom 2 bytes are the minor version number.

Reentrant

Yes

Example

```
uint32_t    version;  
  
version = R_BYTEQ_GetVersion();
```

Special Notes:

This function is inlined using the “#pragma inline” directive.

4. Demo Projects

Demo projects are complete stand-alone programs. They include function main() that utilizes the module and its dependent modules (e.g. r_bsp). This FIT module has the following demo projects.

4.1 byteq_demo_rskrx231

The byteq_demo_rskrx71m project demonstrates how to use some of the BYTEQ API calls. The demo project opens and initializes a queue, puts characters into the queue, queries the number of characters in the queue, gets the characters from the queue and prints them to the virtual console. The demo project also prints out the version number of the BYTEQ module. Virtual console can be enabled by selecting Open Console > Renesas Debug Virtual Console.

4.2 byteq_demo_rskrx71m

The byteq_demo_rskrx71m project demonstrates how to use some of the BYTEQ API calls. The demo project opens and initializes a queue, puts characters into the queue, queries the number of characters in the queue, gets the characters from the queue and prints them to the virtual console. The demo project also prints out the version number of the BYTEQ module. Virtual console can be enabled by selecting Open Console > Renesas Debug Virtual Console.

4.3 Adding a Demo to a Workspace

Demo projects are found in the FITDemos subdirectory of the distribution file for this application note. To add a demo project to a workspace, select File > Import > General > Existing Projects into Workspace, then click “Next”. From the Import Projects dialog, choose the “Select archive file” radio button. “Browse” to the FITDemos subdirectory, select the desired demo zip file, then click “Finish”.

Related Technical Updates

This module reflects the content of the following technical updates.

None

Website and Support

Renesas Electronics Website

<http://www.renesas.com/>

Inquiries

<http://www.renesas.com/inquiry>

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	Jul 24, 2013	—	First edition issued
1.10	Jul 21, 2014	—	Updated XML file for new supported MCUs.
1.20	Nov 21, 2014	—	Removed dependency to BSP. Updated XML file for new supported MCUs.
1.30	Jan 22, 2015	—	Updated XML file for new supported MCUs.
1.40	Jun 30, 2015	—	Added support for the RX231 Group.
1.50	Sep 30, 2015	—	Added support for the RX23T Group.
		5	Added r_bsp in Section 2.2 Software Requirements.
		6	Updated code sizes in 2.8 Code Size.
1.60	Jan 29, 2016	6	Updated code sizes in 2.8 Code Size.
		18	Added the section of 4. Demo Projects.
		program	Changed the XML in order not to depend on the series / group / board of the RX Family.
			Fixed the initial setting procedure in the R_BYTEQ_Open function.
			Fixed a program according to the Renesas coding rules.

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

- The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
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