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| **Equivalence Class Design** | | | | | | | | | |
| **Valid EC** |  |  |  |  | | **Invalid EC** | |  |  |
| **EC ID** | **Condition** | | | |  | **EC ID** | **Condition** | | |
| Ec01 | Lock = : 1 <= < 35 | | | |  | Ec01 | Lock = : < 1 | | |
| Ec02 | Stock = : 35 <= <= 70 | | | |  | Ec02 | Lock = : 70 < | | |
| Ec03 | Stock = : 1 <= <= 40 | | | |  | Ec03 | Lock = : < 1 | | |
| Ec04 | Stock = : 40 <= <= 80 | | | |  | Ec04 | Lock = : 80 < | | |
| Ec05 | Barrel = : 1 <= <= 45 | | | |  | Ec05 | Lock = : < 1 | | |
| Ec06 | Barrel = : 45 <= <= 90 | | | |  | Ec06 | Lock = : 90 < | | |
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| **Test Case Design and Test Results** | | | | | | | | | | | | | |
|  |  | | | |  |  |  |  | |  |  | | |
| **Project Name:** | Commission Program | | | | | | | **Project ID:** | | - | | | |
| **Test Strategy:** | Strong Robust Equivalence Class Testing | | | | | | | **Designer:** | | Apichai Tuppoota | | | |
| **Test Environment:** | | **JUnit5** | | | | | | | | | | | |
|  |  | | | |  |  |  |  | |  |  | | |
| **Test Case ID** | **Input** | | | **Expected Result** | | | | | **Actual Result** | | | **Status (Pass/Fail/No run)** |
| Lock | Stock | Barrel |
| Tc01 | -1 | -1 | -1 | -10000 | | | | | -1 | | | Pass |
| Tc02 | -1 | -1 | 1 | -1 | | | | | -1 | | | Pass |
| Tc03 | -1 | -1 | 45 | -1 | | | | | -1 | | | Pass |
| Tc04 | -1 | -1 | 91 | -1 | | | | | -1 | | | Pass |
| Tc05 | -1 | 1 | -1 | -1 | | | | | -1 | | | Pass |
| Tc06 | -1 | 1 | 1 | -1 | | | | | -1 | | | Pass |
| Tc07 | -1 | 1 | 45 | -1 | | | | | -1 | | | Pass |
| Tc08 | -1 | 1 | 91 | -1 | | | | | -1 | | | Pass |
| Tc09 | -1 | 40 | -1 | -1 | | | | | -1 | | | Pass |
| **Test Case ID** | **Input** | | | **Expected Result** | | | | | **Actual Result** | | | **Status (Pass/Fail/No run)** |
|  |  |  |
| Tc10 | -1 | 40 | 1 | -1 | | | | | -1 | | | Pass |
| Tc11 | -1 | 40 | 45 | -1 | | | | | -1 | | | Pass |
| Tc12 | -1 | 40 | 91 | -1 | | | | | -1 | | | Pass |
| Tc13 | -1 | 81 | -1 | -1 | | | | | -1 | | | Pass |
| Tc14 | -1 | 81 | 1 | -1 | | | | | -1 | | | Pass |
| Tc15 | -1 | 81 | 45 | -1 | | | | | -1 | | | Pass |
| Tc16 | -1 | 81 | 91 | -1 | | | | | -1 | | | Pass |
| Tc17 | 1 | -1 | -1 | -1 | | | | | -1 | | | Pass |
| Tc18 | 1 | -1 | 1 | -1 | | | | | -1 | | | Pass |
| Tc19 | 1 | -1 | 45 | -1 | | | | | -1 | | | Pass |
| Tc20 | 1 | -1 | 91 | -1 | | | | | -1 | | | Pass |
| Tc21 | 1 | 1 | -1 | -1 | | | | | -1 | | | Pass |
| Tc22 | 1 | 1 | 1 | 1000 | | | | | 0 | | | Fail |
| Tc23 | 1 | 1 | 45 | 20500 | | | | | 20500 | | | Pass |
| Tc24 | 1 | 1 | 91 | 43500 | | | | | 43500 | | | Pass |

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| **Test Case ID** | **Input** | | | **Expected Result** | **Actual Result** | **Status (Pass/Fail/No run)** |
|  |  |  |
| Tc25 | 1 | 40 | -1 | -1 | -1 | Pass |
| Tc26 | 1 | 40 | 1 | 21900 | 33600.0 | Fail |
| Tc27 | 1 | 40 | 45 | 43900 | 55600.0 | Fail |
| Tc28 | 1 | 40 | 91 | 66900 | 78600.0 | Fail |
| Tc29 | 1 | 81 | -1 | -1 | -1 | Pass |
| Tc30 | 1 | 81 | 1 | 46500 | 70500.0 | Fail |
| Tc31 | 1 | 81 | 45 | 68500 | 92500.0 | Fail |
| Tc32 | 1 | 81 | 91 | 91500 | 115500.0 | Fail |
| Tc33 | 35 | -1 | -1 | -1 | -1 | Pass |
| Tc34 | 35 | -1 | 1 | -1 | -1 | Pass |
| Tc35 | 35 | -1 | 45 | -1 | -1 | Pass |
| Tc36 | 35 | -1 | 91 | -1 | -1 | Pass |
| Tc37 | 35 | 1 | -1 | -1 | -1 | Pass |
| Tc38 | 35 | 1 | 1 | 29100 | 18900.0 | Fail |
| Tc39 | 35 | 1 | 45 | 51100 | 40900.0 | Fail |

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| **Test Case ID** | **Input** | | | **Expected Result** | **Actual Result** | **Status (Pass/Fail/No run)** |
|  |  |  |
| Tc40 | 35 | 1 | 91 | 74100 | 63900.0 | Fail |
| Tc41 | 35 | 40 | -1 | -1 | -1 | Pass |
| Tc42 | 35 | 40 | 1 | 52500 | 54000.0 | Fail |
| Tc43 | 35 | 40 | 45 | 74500 | 76000.0 | Fail |
| Tc44 | 35 | 40 | 91 | 97500 | 99000.0 | Fail |
| Tc45 | 35 | 81 | -1 | -1 | -1 | Pass |
| Tc46 | 35 | 81 | 1 | 77100 | 90900.0 | Fail |
| Tc47 | 35 | 81 | 45 | 99100 | 112900.0 | Fail |
| Tc48 | 35 | 81 | 91 | 122100 | 135900.0 | Fail |
| Tc49 | 71 | -1 | -1 | -1 | -1 | Pass |
| Tc50 | 71 | -1 | 1 | -1 | -1 | Pass |
| Tc51 | 71 | -1 | 45 | -1 | -1 | Pass |
| Tc52 | 71 | -1 | 91 | -1 | -1 | Pass |
| Tc53 | 71 | 1 | -1 | -1 | -1 | Pass |
| Tc54 | 71 | 1 | 1 | 61500 | 40500.0 | Fail |

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| **Test Case ID** | **Input** | | | **Expected Result** | **Actual Result** | **Status (Pass/Fail/No run)** |
|  |  |  |
| Tc55 | 71 | 1 | 45 | 83500 | 62500.0 | Fail |
| Tc56 | 71 | 1 | 91 | 106500 | 85500.0 | Fail |
| Tc57 | 71 | 40 | -1 | -1 | -1 | Pass |
| Tc58 | 71 | 40 | 1 | 84900 | 75600.0 | Fail |
| Tc59 | 71 | 40 | 45 | 106900 | 97600.0 | Fail |
| Tc60 | 71 | 40 | 91 | 129900 | 120600.0 | Fail |
| Tc61 | 71 | 81 | -1 | -1 | -1 | Pass |
| Tc62 | 71 | 81 | 1 | 109500 | 112500.0 | Fail |
| Tc63 | 71 | 81 | 45 | 131500 | 134500.0 | Fail |
| Tc64 | 71 | 81 | 91 | 154500 | 157500.0 | Fail |

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| **Scenario ID** | **Scenario Name** | **Test Case#** | **Pass** | **Fail** | **No run** | **Block** | **Remark** | **Defect ID** |
| EC-Lab5-01 | Commission Program | 64 | 39 | 25 | - | - | - | - |
| **รวม** | | 64 | 39 | 25 | - | - | - | - |