End of Ch # 11 Exercises

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11.5) As discribed in the last, the PCI Express bus consists of 37 'lanes'.

Each lane is capable of a maximum data rate of 500 MB per sec.

Lanes are allocated to a device 1,2,3,8,16 or 32 lanes at a time.

Assume that a PCI-bus: is to be connected to a high definition victeo card that is supporting a 1980 × 1080 true color (3 bytes per pixel)

progressive scan moniter with a refresh rate of 60 frames per second thow many lanes will this video earch require to support the monitor at full capability?

Sol) Each lane has maximum Data rate of 500 HB per sec Lanes are 1,2,3,8,16 or 32

40 video card: 1920x1880 x3 x 60(frame) bytes/sec

: 373, 249,080 bytes/sec

= 355.9580 MB/sec

As I law cun support 500 MB/sec, I law is enough to support 355.9580 MB/sec

- 11.7> How many PCI-Express busses are required to support a 1043 perdec Ethernut cord
- Sol) 10GB = 10,240 MB
 As each lane can handle 500MB, 20.48 bones are required.

 : 32 lanes are used.