Advanced Digital Design

Unit-I

Assignments for Unit-I

1. Install iverilog tools or Xilinx Vivado or any other similar tool and simulate the following designs: -
   a. Fibonacci number circuit
   b. Division circuit
   c. Period Counter
   d. Binary to BCD converter

All designs are available in the textbook: Pong P Chu, "FPGA Prototyping using Verilog Examples"