Hardware Locality (hwloc)
1.11.8rc1

Generated by Doxygen 1.8.13

Contents

1	Hard	ware Locality	1
	1.1	Introduction	1
	1.2	Installation	2
	1.3	Command-line Examples	4
	1.4	Programming Interface	6
		1.4.1 Portability	6
		1.4.2 API Example	9
	1.5	Questions and Bugs	12
	1.6	History / Credits	12
	1.7	Further Reading	12
2	Term	as and Definitions	13
3	Com	mand-Line Tools	17
	3.1	Istopo and Istopo-no-graphics	17
	3.2	hwloc-bind	17
	3.3	hwloc-calc	18
	3.4	hwloc-info	18
	3.5	hwloc-distrib	18
	3.6	hwloc-ps	18
	3.7	hwloc-distances	18
	3.8	hwloc-annotate	19
	3.9	hwloc-diff, hwloc-patch and hwloc-compress-dir	19
	3.10	hwloc-assembler	19
	3.11	hwloc-assembler-remote	19
	3.12	hwloc-dump-hwdata	19
	3.13	hwloc-gather-topology	19

ii CONTENTS

4	Environment Variables 2					
5	CPU	CPU and Memory Binding Overview				
6	I/O D	O Devices				
	6.1	Enabling and requirements	27			
	6.2	I/O objects	27			
	6.3	OS devices	28			
	6.4	PCI devices and bridges	29			
	6.5	Consulting I/O devices and binding	29			
	6.6	Examples	29			
7	Misc	ellaneous objects	33			
	7.1	Misc objects added by hwloc	33			
	7.2	Annotating topologies with Misc objects	33			
8	Multi	i-node Topologies	35			
	8.1	Multi-node Objects Specifities	35			
	8.2	Assembling topologies with command-line tools	36			
	8.3	Assembling topologies with the programming interface	36			
	8.4	Example of assembly with the programming interface	36			
9	Obje	ct attributes	39			
	9.1	Normal attributes	39			
	9.2	Custom string infos	39			
10	Impo	orting and exporting topologies from/to XML files	43			
	10.1	libxml2 and minimalistic XML backends	43			
	10.2	XML import error management	44			
11	Synt	hetic topologies	45			
	11.1	Synthetic description string	45			
	11.2	Loading a synthetic topology	46			
	11.3	Exporting a topology as a synthetic string	46			

CONTENTS

12	Inter	operabi	lity With Other Software	47
13	Thre	ad Safe	ty	49
14	Com	ponent	s and plugins	51
	14.1	Compo	nents enabled by default	51
	14.2	Selection	ng which components to use	52
	14.3	Loading	g components from plugins	52
	14.4	Adding	new discovery components and plugins	52
		14.4.1	Basics of discovery components	53
		14.4.2	Registering a new discovery component	53
	14.5	Existing	g components and plugins	53
15	Emb	edding	hwloc in Other Software	55
	15.1	Using h	nwloc's M4 Embedding Capabilities	56
	15.2	Examp	le Embedding hwloc	57
16	Freq	uently A	Asked Questions	59
	16.1	Concep	ots	59
		16.1.1	I only need binding, why should I use hwloc?	59
		16.1.2	Should I use logical or physical/OS indexes? and how?	59
		16.1.3	hwloc is only a structural model, it ignores performance models, memory bandwidth, etc.? .	60
		16.1.4	hwloc only has a one-dimensional view of the architecture, it ignores distances?	60
		16.1.5	What are these Group objects in my topology?	61
		16.1.6	What happens if my topology is asymmetric?	61
		16.1.7	What happens to my topology if I disable symmetric multithreading, hyper-threading, etc. in the system?	62
		16.1.8	How may I ignore symmetric multithreading, hyper-threading, etc. in hwloc?	62
	16.2	Advanc	ped	63
		16.2.1	I do not want hwloc to rediscover my enormous machine topology every time I rerun a process	63
		16.2.2	How many topologies may I use in my program?	63
		16.2.3	How to avoid memory waste when manipulating multiple similar topologies?	64
		16.2.4	How do I annotate the topology with private notes?	64

iv CONTENTS

	16.3	Caveats		64
		16.3.1 Why	is hwloc slow?	64
		16.3.2 Does	hwloc require privileged access?	65
		16.3.3 What	should I do when hwloc reports "operating system" warnings?	65
		16.3.4 Why	does Valgrind complain about hwloc memory leaks?	66
		16.3.5 How	do I handle ABI breaks and API upgrades?	66
	16.4	Platform-spe	cific	67
		16.4.1 How	do I find the local MCDRAM NUMA node on Intel Knights Landing Xeon Phi?	67
		16.4.2 Why	do I need hwloc-dump-hwdata for memory on Intel Knights Landing Xeon Phi?	68
		16.4.3 How	do I build for Intel Xeon Phi coprocessor?	68
		16.4.4 How	do I build hwloc for BlueGene/Q?	68
		16.4.5 How	do I build hwloc for Windows?	69
		16.4.6 How	to get useful topology information on NetBSD?	69
17	Modu	ıle Index		71
	17.1	Modules		71
18	Data	Structure Inc	dex	73
	18.1	Data Structui	es	73
19	Modu	ıle Documen	tation	75
	19.1	API version		75
		19.1.1 Deta	led Description	75
		19.1.2 Macr	o Definition Documentation	75
		19.1.	2.1 HWLOC_API_VERSION	75
		19.1.	2.2 HWLOC_COMPONENT_ABI	75
		19.1.3 Func	tion Documentation	76
		19.1.	3.1 hwloc_get_api_version()	76
	19.2	Object Sets (hwloc_cpuset_t and hwloc_nodeset_t)	77
		19.2.1 Deta	led Description	77
		19.2.2 Type	def Documentation	77
		19.2.	2.1 hwloc const cpuset t	77

CONTENTS

		19.2.2.2 hwloc_const_nodeset_t	77
		19.2.2.3 hwloc_cpuset_t	77
		19.2.2.4 hwloc_nodeset_t	77
19.3	Object	Types	78
	19.3.1	Detailed Description	78
	19.3.2	Typedef Documentation	78
		19.3.2.1 hwloc_obj_bridge_type_t	78
		19.3.2.2 hwloc_obj_cache_type_t	78
		19.3.2.3 hwloc_obj_osdev_type_t	79
	19.3.3	Enumeration Type Documentation	79
		19.3.3.1 hwloc_compare_types_e	79
		19.3.3.2 hwloc_obj_bridge_type_e	79
		19.3.3.3 hwloc_obj_cache_type_e	79
		19.3.3.4 hwloc_obj_osdev_type_e	80
		19.3.3.5 hwloc_obj_type_t	80
	19.3.4	Function Documentation	81
		19.3.4.1 hwloc_compare_types()	81
19.4	Object	Structure and Attributes	82
	19.4.1	Detailed Description	82
	19.4.2	Typedef Documentation	82
		19.4.2.1 hwloc_obj_t	82
19.5	Topolog	gy Creation and Destruction	83
	19.5.1	Detailed Description	83
	19.5.2	Typedef Documentation	83
		19.5.2.1 hwloc_topology_t	83
	19.5.3	Function Documentation	83
		19.5.3.1 hwloc_topology_check()	83
		19.5.3.2 hwloc_topology_destroy()	84
		19.5.3.3 hwloc_topology_dup()	84
		19.5.3.4 hwloc_topology_init()	84

vi

		19.5.3.5 hwloc_topology_load()	35
19.6	Topolog	gy Detection Configuration and Query	36
	19.6.1	Detailed Description	36
	19.6.2	Enumeration Type Documentation	87
		19.6.2.1 hwloc_topology_flags_e	37
	19.6.3	Function Documentation	38
		19.6.3.1 hwloc_topology_get_flags()	38
		19.6.3.2 hwloc_topology_get_support()	39
		19.6.3.3 hwloc_topology_get_userdata()	39
		19.6.3.4 hwloc_topology_ignore_all_keep_structure()	39
		19.6.3.5 hwloc_topology_ignore_type()	39
		19.6.3.6 hwloc_topology_ignore_type_keep_structure()	39
		19.6.3.7 hwloc_topology_is_thissystem()	90
		19.6.3.8 hwloc_topology_set_custom()	90
		19.6.3.9 hwloc_topology_set_distance_matrix()	90
		19.6.3.10 hwloc_topology_set_flags()	91
		19.6.3.11 hwloc_topology_set_fsroot()	91
		19.6.3.12 hwloc_topology_set_pid()	91
		19.6.3.13 hwloc_topology_set_synthetic()	92
		19.6.3.14 hwloc_topology_set_userdata()	92
		19.6.3.15 hwloc_topology_set_xml()	92
		19.6.3.16 hwloc_topology_set_xmlbuffer()	93
19.7	Object	levels, depths and types	94
	19.7.1	Detailed Description	94
	19.7.2	Enumeration Type Documentation	94
		19.7.2.1 hwloc_get_type_depth_e	94
	19.7.3	Function Documentation	95
		19.7.3.1 hwloc_get_depth_type()	95
		19.7.3.2 hwloc_get_nbobjs_by_depth()	95
		19.7.3.3 hwloc_get_nbobjs_by_type()	95

CONTENTS vii

		19.7.3.4	hwloc_get_next_obj_by_depth()	 	95
		19.7.3.5	hwloc_get_next_obj_by_type()	 	96
		19.7.3.6	hwloc_get_obj_by_depth()	 	96
		19.7.3.7	hwloc_get_obj_by_type()	 	96
		19.7.3.8	hwloc_get_root_obj()	 	96
		19.7.3.9	hwloc_get_type_depth()	 	97
		19.7.3.10	hwloc_get_type_or_above_depth()	 	97
		19.7.3.11	hwloc_get_type_or_below_depth()	 	97
		19.7.3.12	hwloc_topology_get_depth()	 	97
19.8	Conver	ting betwe	en Object Types, Sets and Attributes, and Strings	 	98
	19.8.1	Detailed I	Description	 	98
	19.8.2	Function	Documentation	 	98
		19.8.2.1	hwloc_obj_attr_snprintf()	 	98
		19.8.2.2	hwloc_obj_cpuset_snprintf()	 	98
		19.8.2.3	hwloc_obj_type_snprintf()	 	99
		19.8.2.4	hwloc_obj_type_sscanf()	 	99
		19.8.2.5	hwloc_obj_type_string()	 	100
19.9	Consul	ting and A	dding Key-Value Info Attributes	 	101
	19.9.1	Detailed I	Description	 	101
	19.9.2	Function	Documentation	 	101
		19.9.2.1	hwloc_obj_add_info()	 	101
		19.9.2.2	hwloc_obj_get_info_by_name()	 	101
19.10	CPU bi	nding		 	102
	19.10.1	Detailed I	Description	 	102
	19.10.2	2 Enumerat	tion Type Documentation	 	103
		19.10.2.1	hwloc_cpubind_flags_t	 	103
	19.10.3	3 Function	Documentation	 	104
		19.10.3.1	hwloc_get_cpubind()	 	104
		19.10.3.2	hwloc_get_last_cpu_location()	 	104
		19.10.3.3	hwloc_get_proc_cpubind()	 	104

viii CONTENTS

19.10.3.4 hwloc_get_proc_last_cpu_location()
19.10.3.5 hwloc_get_thread_cpubind()
19.10.3.6 hwloc_set_cpubind()
19.10.3.7 hwloc_set_proc_cpubind()
19.10.3.8 hwloc_set_thread_cpubind()
19.11 Memory binding
19.11.1 Detailed Description
19.11.2 Enumeration Type Documentation
19.11.2.1 hwloc_membind_flags_t
19.11.2.2 hwloc_membind_policy_t
19.11.3 Function Documentation
19.11.3.1 hwloc_alloc()
19.11.3.2 hwloc_alloc_membind()
19.11.3.3 hwloc_alloc_membind_nodeset()
19.11.3.4 hwloc_alloc_membind_policy()
19.11.3.5 hwloc_alloc_membind_policy_nodeset()
19.11.3.6 hwloc_free()
19.11.3.7 hwloc_get_area_membind()
19.11.3.8 hwloc_get_area_membind_nodeset()
19.11.3.9 hwloc_get_area_memlocation()
19.11.3.10hwloc_get_membind()
19.11.3.11hwloc_get_membind_nodeset()
19.11.3.12hwloc_get_proc_membind()
19.11.3.13hwloc_get_proc_membind_nodeset()
19.11.3.14hwloc_set_area_membind()
19.11.3.15hwloc_set_area_membind_nodeset()
19.11.3.16hwloc_set_membind()
19.11.3.17hwloc_set_membind_nodeset()
19.11.3.18hwloc_set_proc_membind()
19.11.3.19hwloc_set_proc_membind_nodeset()

CONTENTS

19.12 Modifying a loaded Topology
19.12.1 Detailed Description
19.12.2 Enumeration Type Documentation
19.12.2.1 hwloc_restrict_flags_e
19.12.3 Function Documentation
19.12.3.1 hwloc_topology_insert_misc_object_by_cpuset()
19.12.3.2 hwloc_topology_insert_misc_object_by_parent()
19.12.3.3 hwloc_topology_restrict()
19.13Building Custom Topologies
19.13.1 Detailed Description
19.13.2 Function Documentation
19.13.2.1 hwloc_custom_insert_group_object_by_parent()
19.13.2.2 hwloc_custom_insert_topology()
19.14Exporting Topologies to XML
19.14.1 Detailed Description
19.14.2 Function Documentation
19.14.2.1 hwloc_export_obj_userdata()
19.14.2.2 hwloc_export_obj_userdata_base64()
19.14.2.3 hwloc_free_xmlbuffer()
19.14.2.4 hwloc_topology_export_xml()
19.14.2.5 hwloc_topology_export_xmlbuffer()
19.14.2.6 hwloc_topology_set_userdata_export_callback()
19.14.2.7 hwloc_topology_set_userdata_import_callback()
19.15Exporting Topologies to Synthetic
19.15.1 Detailed Description
19.15.2 Enumeration Type Documentation
19.15.2.1 hwloc_topology_export_synthetic_flags_e
19.15.3 Function Documentation
19.15.3.1 hwloc_topology_export_synthetic()
19.16Finding Objects inside a CPU set

CONTENTS

19.16.1 Detailed Description	130
19.16.2 Function Documentation	130
19.16.2.1 hwloc_get_first_largest_obj_inside_cpuset()	130
19.16.2.2 hwloc_get_largest_objs_inside_cpuset()	131
19.16.2.3 hwloc_get_nbobjs_inside_cpuset_by_depth()	131
19.16.2.4 hwloc_get_nbobjs_inside_cpuset_by_type()	131
19.16.2.5 hwloc_get_next_obj_inside_cpuset_by_depth()	132
19.16.2.6 hwloc_get_next_obj_inside_cpuset_by_type()	132
19.16.2.7 hwloc_get_obj_index_inside_cpuset()	132
19.16.2.8 hwloc_get_obj_inside_cpuset_by_depth()	133
19.16.2.9 hwloc_get_obj_inside_cpuset_by_type()	133
19.17 Finding Objects covering at least CPU set	134
19.17.1 Detailed Description	134
19.17.2 Function Documentation	134
19.17.2.1 hwloc_get_child_covering_cpuset()	134
19.17.2.2 hwloc_get_next_obj_covering_cpuset_by_depth()	134
19.17.2.3 hwloc_get_next_obj_covering_cpuset_by_type()	135
19.17.2.4 hwloc_get_obj_covering_cpuset()	135
19.18Looking at Ancestor and Child Objects	136
19.18.1 Detailed Description	136
19.18.2 Function Documentation	136
19.18.2.1 hwloc_get_ancestor_obj_by_depth()	136
19.18.2.2 hwloc_get_ancestor_obj_by_type()	136
19.18.2.3 hwloc_get_common_ancestor_obj()	137
19.18.2.4 hwloc_get_next_child()	137
19.18.2.5 hwloc_obj_is_in_subtree()	137
19.19Looking at Cache Objects	138
19.19.1 Detailed Description	138
19.19.2 Function Documentation	138
19.19.2.1 hwloc_get_cache_covering_cpuset()	138

CONTENTS xi

19.19.2.2 hwloc_get_cache_type_depth()	138
19.19.2.3 hwloc_get_shared_cache_covering_obj()	139
19.20 Finding objects, miscellaneous helpers	140
19.20.1 Detailed Description	140
19.20.2 Function Documentation	140
19.20.2.1 hwloc_get_closest_objs()	140
19.20.2.2 hwloc_get_numanode_obj_by_os_index()	141
19.20.2.3 hwloc_get_obj_below_array_by_type()	141
19.20.2.4 hwloc_get_obj_below_by_type()	141
19.20.2.5 hwloc_get_pu_obj_by_os_index()	142
19.21 Distributing items over a topology	143
19.21.1 Detailed Description	143
19.21.2 Enumeration Type Documentation	143
19.21.2.1 hwloc_distrib_flags_e	143
19.21.3 Function Documentation	143
19.21.3.1 hwloc_distrib()	143
19.22CPU and node sets of entire topologies	145
19.22.1 Detailed Description	145
19.22.2 Function Documentation	145
19.22.2.1 hwloc_topology_get_allowed_cpuset()	145
19.22.2.2 hwloc_topology_get_allowed_nodeset()	145
19.22.2.3 hwloc_topology_get_complete_cpuset()	146
19.22.2.4 hwloc_topology_get_complete_nodeset()	146
19.22.2.5 hwloc_topology_get_online_cpuset()	146
19.22.2.6 hwloc_topology_get_topology_cpuset()	147
19.22.2.7 hwloc_topology_get_topology_nodeset()	147
19.23Converting between CPU sets and node sets	148
19.23.1 Detailed Description	148
19.23.2 Function Documentation	148
19.23.2.1 hwloc_cpuset_from_nodeset()	148

xii CONTENTS

19.23.2.2 hwloc_cpuset_from_nodeset_strict()	48
19.23.2.3 hwloc_cpuset_to_nodeset()	49
19.23.2.4 hwloc_cpuset_to_nodeset_strict()	49
19.24Manipulating Distances	50
19.24.1 Detailed Description	50
19.24.2 Function Documentation	50
19.24.2.1 hwloc_get_distance_matrix_covering_obj_by_depth()	50
19.24.2.2 hwloc_get_latency()	50
19.24.2.3 hwloc_get_whole_distance_matrix_by_depth()	51
19.24.2.4 hwloc_get_whole_distance_matrix_by_type()	51
19.25 Finding I/O objects	52
19.25.1 Detailed Description	52
19.25.2 Function Documentation	52
19.25.2.1 hwloc_bridge_covers_pcibus()	52
19.25.2.2 hwloc_get_hostbridge_by_pcibus()	52
19.25.2.3 hwloc_get_next_bridge()	52
19.25.2.4 hwloc_get_next_osdev()	53
19.25.2.5 hwloc_get_next_pcidev()	53
19.25.2.6 hwloc_get_non_io_ancestor_obj()	53
19.25.2.7 hwloc_get_pcidev_by_busid()	53
19.25.2.8 hwloc_get_pcidev_by_busidstring()	53
19.26The bitmap API	54
19.26.1 Detailed Description	55
19.26.2 Macro Definition Documentation	55
19.26.2.1 hwloc_bitmap_foreach_begin	55
19.26.2.2 hwloc_bitmap_foreach_end	56
19.26.3 Typedef Documentation	56
19.26.3.1 hwloc_bitmap_t	56
19.26.3.2 hwloc_const_bitmap_t	56
19.26.4 Function Documentation	56

CONTENTS xiii

19.26.4.1 hwloc_bitmap_allbut()
19.26.4.2 hwloc_bitmap_alloc()
19.26.4.3 hwloc_bitmap_alloc_full()
19.26.4.4 hwloc_bitmap_and()
19.26.4.5 hwloc_bitmap_andnot()
19.26.4.6 hwloc_bitmap_asprintf()
19.26.4.7 hwloc_bitmap_clr()
19.26.4.8 hwloc_bitmap_clr_range()
19.26.4.9 hwloc_bitmap_compare()
19.26.4.10hwloc_bitmap_compare_first()
19.26.4.11hwloc_bitmap_copy()
19.26.4.12hwloc_bitmap_dup()
19.26.4.13hwloc_bitmap_fill()
19.26.4.14hwloc_bitmap_first()
19.26.4.15hwloc_bitmap_free()
19.26.4.16hwloc_bitmap_from_ith_ulong()
19.26.4.17hwloc_bitmap_from_ulong()
19.26.4.18hwloc_bitmap_intersects()
19.26.4.19hwloc_bitmap_isequal()
19.26.4.20hwloc_bitmap_isfull()
19.26.4.21hwloc_bitmap_isincluded()
19.26.4.22hwloc_bitmap_isset()
19.26.4.23hwloc_bitmap_iszero()
19.26.4.24hwloc_bitmap_last()
19.26.4.25hwloc_bitmap_list_asprintf()
19.26.4.26hwloc_bitmap_list_snprintf()
19.26.4.27hwloc_bitmap_list_sscanf()
19.26.4.28hwloc_bitmap_next()
19.26.4.29hwloc_bitmap_not()
19.26.4.30hwloc_bitmap_only()

xiv CONTENTS

19.26.4.31hwloc_bitmap_or()	. 163
19.26.4.32hwloc_bitmap_set()	. 163
19.26.4.33hwloc_bitmap_set_ith_ulong()	. 163
19.26.4.34hwloc_bitmap_set_range()	. 164
19.26.4.35hwloc_bitmap_singlify()	. 164
19.26.4.36hwloc_bitmap_snprintf()	. 164
19.26.4.37hwloc_bitmap_sscanf()	. 164
19.26.4.38hwloc_bitmap_taskset_asprintf()	. 165
19.26.4.39hwloc_bitmap_taskset_snprintf()	. 165
19.26.4.40hwloc_bitmap_taskset_sscanf()	. 165
19.26.4.41hwloc_bitmap_to_ith_ulong()	. 165
19.26.4.42hwloc_bitmap_to_ulong()	. 166
19.26.4.43hwloc_bitmap_weight()	. 166
19.26.4.44hwloc_bitmap_xor()	. 166
19.26.4.45hwloc_bitmap_zero()	. 166
19.27Linux-specific helpers	. 167
19.27.1 Detailed Description	. 167
19.27.2 Function Documentation	. 167
19.27.2.1 hwloc_linux_get_tid_cpubind()	. 167
19.27.2.2 hwloc_linux_get_tid_last_cpu_location()	. 167
19.27.2.3 hwloc_linux_parse_cpumap_file()	. 168
19.27.2.4 hwloc_linux_set_tid_cpubind()	. 168
19.28Interoperability with Linux libnuma unsigned long masks	. 169
19.28.1 Detailed Description	. 169
19.28.2 Function Documentation	. 169
19.28.2.1 hwloc_cpuset_from_linux_libnuma_ulongs()	. 169
19.28.2.2 hwloc_cpuset_to_linux_libnuma_ulongs()	. 170
19.28.2.3 hwloc_nodeset_from_linux_libnuma_ulongs()	. 170
19.28.2.4 hwloc_nodeset_to_linux_libnuma_ulongs()	. 170
19.29 Interoperability with Linux libnuma bitmask	. 171

CONTENTS xv

19.29.1 Detailed Description	71
19.29.2 Function Documentation	71
19.29.2.1 hwloc_cpuset_from_linux_libnuma_bitmask()	71
19.29.2.2 hwloc_cpuset_to_linux_libnuma_bitmask()	71
19.29.2.3 hwloc_nodeset_from_linux_libnuma_bitmask()	72
19.29.2.4 hwloc_nodeset_to_linux_libnuma_bitmask() 1	72
19.30 Interoperability with glibc sched affinity	73
19.30.1 Detailed Description	73
19.30.2 Function Documentation	73
19.30.2.1 hwloc_cpuset_from_glibc_sched_affinity()	73
19.30.2.2 hwloc_cpuset_to_glibc_sched_affinity()	73
19.31 Interoperability with OpenCL	74
19.31.1 Detailed Description	74
19.31.2 Function Documentation	74
19.31.2.1 hwloc_opencl_get_device_cpuset()	74
19.31.2.2 hwloc_opencl_get_device_osdev()	74
19.31.2.3 hwloc_opencl_get_device_osdev_by_index()	75
19.32 Interoperability with the CUDA Driver API	76
19.32.1 Detailed Description	76
19.32.2 Function Documentation	76
19.32.2.1 hwloc_cuda_get_device_cpuset()	76
19.32.2.2 hwloc_cuda_get_device_osdev()	76
19.32.2.3 hwloc_cuda_get_device_osdev_by_index()	77
19.32.2.4 hwloc_cuda_get_device_pci_ids()	77
19.32.2.5 hwloc_cuda_get_device_pcidev()	77
19.33 Interoperability with the CUDA Runtime API	78
19.33.1 Detailed Description	78
19.33.2 Function Documentation	78
19.33.2.1 hwloc_cudart_get_device_cpuset()	78
19.33.2.2 hwloc_cudart_get_device_osdev_by_index()	78

xvi CONTENTS

19.33.2.3 hwloc_cudart_get_device_pci_ids()
19.33.2.4 hwloc_cudart_get_device_pcidev()
19.34Interoperability with the NVIDIA Management Library
19.34.1 Detailed Description
19.34.2 Function Documentation
19.34.2.1 hwloc_nvml_get_device_cpuset()
19.34.2.2 hwloc_nvml_get_device_osdev()
19.34.2.3 hwloc_nvml_get_device_osdev_by_index()
19.35Interoperability with OpenGL displays
19.35.1 Detailed Description
19.35.2 Function Documentation
19.35.2.1 hwloc_gl_get_display_by_osdev()
19.35.2.2 hwloc_gl_get_display_osdev_by_name()
19.35.2.3 hwloc_gl_get_display_osdev_by_port_device()
19.36Interoperability with Intel Xeon Phi (MIC)
19.36.1 Detailed Description
19.36.2 Function Documentation
19.36.2.1 hwloc_intel_mic_get_device_cpuset()
19.36.2.2 hwloc_intel_mic_get_device_osdev_by_index()
19.37Interoperability with OpenFabrics
19.37.1 Detailed Description
19.37.2 Function Documentation
19.37.2.1 hwloc_ibv_get_device_cpuset()
19.37.2.2 hwloc_ibv_get_device_osdev()
19.37.2.3 hwloc_ibv_get_device_osdev_by_name()
19.38Interoperability with Myrinet Express
19.38.1 Detailed Description
19.38.2 Function Documentation
19.38.2.1 hwloc_mx_board_get_device_cpuset()
19.38.2.2 hwloc_mx_endpoint_get_device_cpuset()

CONTENTS xvii

19.39Topology differences
19.39.1 Detailed Description
19.39.2 Typedef Documentation
19.39.2.1 hwloc_topology_diff_obj_attr_type_t
19.39.2.2 hwloc_topology_diff_t
19.39.2.3 hwloc_topology_diff_type_t
19.39.3 Enumeration Type Documentation
19.39.3.1 hwloc_topology_diff_apply_flags_e
19.39.3.2 hwloc_topology_diff_obj_attr_type_e
19.39.3.3 hwloc_topology_diff_type_e
19.39.4 Function Documentation
19.39.4.1 hwloc_topology_diff_apply()
19.39.4.2 hwloc_topology_diff_build()
19.39.4.3 hwloc_topology_diff_destroy()
19.39.4.4 hwloc_topology_diff_export_xml()
19.39.4.5 hwloc_topology_diff_export_xmlbuffer()
19.39.4.6 hwloc_topology_diff_load_xml()
19.39.4.7 hwloc_topology_diff_load_xmlbuffer()
19.40 Components and Plugins: Discovery components
19.40.1 Detailed Description
19.40.2 Typedef Documentation
19.40.2.1 hwloc_disc_component_type_t
19.40.3 Enumeration Type Documentation
19.40.3.1 hwloc_disc_component_type_e
19.41 Components and Plugins: Discovery backends
19.41.1 Detailed Description
19.41.2 Enumeration Type Documentation
19.41.2.1 hwloc_backend_flag_e
19.41.3 Function Documentation
19.41.3.1 hwloc_backend_alloc()

xviii CONTENTS

19.41.3.2 hwloc_backend_enable()	197
19.41.3.3 hwloc_backends_get_obj_cpuset()	197
19.41.3.4 hwloc_backends_notify_new_object()	197
19.42Components and Plugins: Generic components	198
19.42.1 Detailed Description	198
19.42.2 Typedef Documentation	198
19.42.2.1 hwloc_component_type_t	198
19.42.3 Enumeration Type Documentation	198
19.42.3.1 hwloc_component_type_e	198
19.43Components and Plugins: Core functions to be used by components	199
19.43.1 Detailed Description	199
19.43.2 Typedef Documentation	199
19.43.2.1 hwloc_report_error_t	199
19.43.3 Function Documentation	199
19.43.3.1 hwlocinsert_object_by_cpuset()	199
19.43.3.2 hwloc_alloc_setup_object()	200
19.43.3.3 hwloc_fill_object_sets()	200
19.43.3.4 hwloc_hide_errors()	200
19.43.3.5 hwloc_insert_object_by_cpuset()	200
19.43.3.6 hwloc_insert_object_by_parent()	201
19.43.3.7 hwloc_plugin_check_namespace()	201
19.43.3.8 hwloc_report_os_error()	201
19.44Components and Plugins: PCI functions to be used by components	202
19.44.1 Detailed Description	202
19.44.2 Function Documentation	202
19.44.2.1 hwloc_insert_pci_device_list()	202
19.44.2.2 hwloc_pci_find_cap()	202
19.44.2.3 hwloc_pci_find_linkspeed()	202
19.44.2.4 hwloc_pci_prepare_bridge()	202

CONTENTS xix

20	Data	Structu	ire Docum	mentation	203
	20.1	hwloc_	backend S	Struct Reference	. 203
		20.1.1	Detailed I	Description	. 203
		20.1.2	Field Doo	cumentation	. 203
			20.1.2.1	disable	. 204
			20.1.2.2	discover	. 204
			20.1.2.3	flags	. 204
			20.1.2.4	get_obj_cpuset	. 204
			20.1.2.5	is_custom	. 204
			20.1.2.6	is_thissystem	. 204
			20.1.2.7	notify_new_object	. 205
			20.1.2.8	private_data	. 205
	20.2	hwloc_	obj_attr_u	ı::hwloc_bridge_attr_s Struct Reference	. 205
		20.2.1	Detailed I	Description	. 205
		20.2.2	Field Doo	cumentation	. 206
			20.2.2.1	depth	. 206
			20.2.2.2	domain	. 206
			20.2.2.3	downstream	. 206
			20.2.2.4	downstream_type	. 206
			20.2.2.5	pci [1/2]	. 206
			20.2.2.6	pci [2/2]	. 206
			20.2.2.7	secondary_bus	. 206
			20.2.2.8	subordinate_bus	. 207
			20.2.2.9	upstream	. 207
			20.2.2.10	O upstream_type	. 207
	20.3	hwloc_	obj_attr_u:	u::hwloc_cache_attr_s	. 207
		20.3.1	Detailed I	Description	. 207
		20.3.2	Field Doo	cumentation	. 207
			20.3.2.1	associativity	. 207
			20.3.2.2	depth	. 208

CONTENTS

		20.3.2.3	- 1	lir	nes	size	€.	•					•			٠					 •		 	٠	 				208
		20.3.2.4		si	ze	٠																	 		 			 	208
		20.3.2.5	t	ty	'pe)																	 		 			 	208
20.4	hwloc_	componer	nt	S	Stru	uct	Re	fer	er	ice	9												 		 			 	208
	20.4.1	Detailed	D)e:	SCI	ript	ion																 		 			 	209
	20.4.2	Field Doo	cu	ım	nei	nta	tion	1															 		 			 	209
		20.4.2.1	á	al	bi																		 		 			 	209
		20.4.2.2		da	ata	1																	 		 			 	209
		20.4.2.3	f	fir	nal	lize																	 		 			 	209
		20.4.2.4	· f	fla	ags	S .																	 		 			 	209
		20.4.2.5	i	in	ıit																		 		 			 	210
		20.4.2.6	t	ty	'pe)																	 		 			 	210
20.5	hwloc_	disc_comp	po	on	ıer	nt S	Stru	ct	Re	efe	re	nc	е										 		 			 	210
	20.5.1	Detailed	D)e:	SCI	ript	ion																 		 			 	211
	20.5.2	Field Doo	cu	ım	nei	nta	tior	1															 		 			 	211
		20.5.2.1	•	ex	xcl	ude	es																 		 			 	211
		20.5.2.2	i	in	sta	anti	iate)															 		 			 	211
		20.5.2.3	r	na	am	ne .																	 		 			 	211
		20.5.2.4	F	pr	rio	rity																	 		 			 	211
		20.5.2.5	t	ty	'pe)																	 		 			 	212
20.6	hwloc_	distances	s_s	s S	Str	ruct	t Re	efe	re	nc	е												 		 			 	212
	20.6.1	Detailed	D)e:	SCI	ript	ion																 		 			 	212
	20.6.2	Field Doo	cu	um	nei	nta	tior	1															 		 			 	212
		20.6.2.1	ı	la	ıteı	ncy	<i>,</i> .																 		 			 	212
		20.6.2.2	1	la	ıteı	ncy	/_ba	ase	Э.														 		 			 	213
		20.6.2.3	1	la	ıteı	ncy	/_m	ıax															 		 			 	213
		20.6.2.4	r	nk	bol	bjs																	 		 			 	213
		20.6.2.5	r	re	elat	tive	e_d	ept	th														 		 			 	213
20.7	hwloc_	obj_attr_u	u::l	:h\	wlc	oc_	_gro	oup)_a	attı	r_s	s S	Stru	uct	i R	lefe	ere	end	ce				 		 			 	213
	20.7.1	Detailed	l D)e:	SCI	ript	ion																 		 			 	213

CONTENTS xxi

	20.7.2	Field Documentation	14
		20.7.2.1 depth	14
20.8	hwloc_	bj Struct Reference	14
	20.8.1	Detailed Description	15
	20.8.2	Field Documentation	15
		20.8.2.1 allowed_cpuset	15
		20.8.2.2 allowed_nodeset	15
		20.8.2.3 arity	15
		20.8.2.4 attr	16
		20.8.2.5 children	16
		20.8.2.6 complete_cpuset	16
		20.8.2.7 complete_nodeset	16
		20.8.2.8 cpuset	17
		20.8.2.9 depth	17
		20.8.2.10 distances	17
		20.8.2.11 distances_count	17
		20.8.2.12 first_child	17
		20.8.2.13 infos	18
		20.8.2.14 infos_count	18
		20.8.2.15 last_child	18
		20.8.2.16 logical_index	18
		20.8.2.17 memory	18
		20.8.2.18 name	18
		20.8.2.19 next_cousin	19
		20.8.2.20 next_sibling	19
		20.8.2.21 nodeset	19
		20.8.2.22 online_cpuset	19
		20.8.2.23 os_index	20
		20.8.2.24 os_level	20
		20.8.2.25 parent	20

xxii CONTENTS

20.8.2.26 prev_cousin
20.8.2.27 prev_sibling
20.8.2.28 sibling_rank
20.8.2.29 symmetric_subtree
20.8.2.30 type
20.8.2.31 userdata
20.9 hwloc_obj_attr_u Union Reference
20.9.1 Detailed Description
20.9.2 Field Documentation
20.9.2.1 bridge
20.9.2.2 cache
20.9.2.3 group
20.9.2.4 osdev
20.9.2.5 pcidev
20.10hwloc_obj_info_s Struct Reference
20.10.1 Detailed Description
20.10.2 Field Documentation
20.10.2.1 name
20.10.2.2 value
20.11hwloc_obj_memory_s::hwloc_obj_memory_page_type_s Struct Reference
20.11.1 Detailed Description
20.11.2 Field Documentation
20.11.2.1 count
20.11.2.2 size
20.12hwloc_obj_memory_s Struct Reference
20.12.1 Detailed Description
20.12.2 Field Documentation
20.12.2.1 local_memory
20.12.2.2 page_types
20.12.2.3 page_types_len

CONTENTS xxiii

20.12.2.4 total_memory	225
20.13hwloc_obj_attr_u::hwloc_osdev_attr_s Struct Reference	225
20.13.1 Detailed Description	226
20.13.2 Field Documentation	226
20.13.2.1 type	226
20.14hwloc_obj_attr_u::hwloc_pcidev_attr_s Struct Reference	226
20.14.1 Detailed Description	226
20.14.2 Field Documentation	226
20.14.2.1 bus	227
20.14.2.2 class_id	227
20.14.2.3 dev	227
20.14.2.4 device_id	227
20.14.2.5 domain	227
20.14.2.6 func	227
20.14.2.7 linkspeed	227
20.14.2.8 revision	227
20.14.2.9 subdevice_id	228
20.14.2.10subvendor_id	228
20.14.2.11vendor_id	228
20.15hwloc_topology_cpubind_support Struct Reference	228
20.15.1 Detailed Description	228
20.15.2 Field Documentation	229
20.15.2.1 get_proc_cpubind	229
20.15.2.2 get_proc_last_cpu_location	229
20.15.2.3 get_thisproc_cpubind	229
20.15.2.4 get_thisproc_last_cpu_location	229
20.15.2.5 get_thisthread_cpubind	229
20.15.2.6 get_thisthread_last_cpu_location	229
20.15.2.7 get_thread_cpubind	229
20.15.2.8 set_proc_cpubind	230

xxiv CONTENTS

20.15.2.9 set_thisproc_cpubind
20.15.2.10set_thisthread_cpubind
20.15.2.11set_thread_cpubind
20.16hwloc_topology_diff_u::hwloc_topology_diff_generic_s Struct Reference
20.16.1 Field Documentation
20.16.1.1 next
20.16.1.2 type
20.17hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_generic_s Struct Reference 23
20.17.1 Field Documentation
20.17.1.1 type
20.18hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s Struct Reference
20.18.1 Field Documentation
20.18.1.1 diff
20.18.1.2 next
20.18.1.3 obj_depth
20.18.1.4 obj_index
20.18.1.5 type
20.19hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s Struct Reference 23
20.19.1 Detailed Description
20.19.2 Field Documentation
20.19.2.1 name
20.19.2.2 newvalue
20.19.2.3 oldvalue
20.19.2.4 type
20.20 hwloc_topology_diff_obj_attr_u Union Reference
20.20.1 Detailed Description
20.20.2 Field Documentation
20.20.2.1 generic
20.20.2.2 string
20.20.2.3 uint64

CONTENTS xxv

20.21 hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_uint64_s Struct Reference 235
20.21.1 Detailed Description
20.21.2 Field Documentation
20.21.2.1 index
20.21.2.2 newvalue
20.21.2.3 oldvalue
20.21.2.4 type
20.22hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s Struct Reference
20.22.1 Field Documentation
20.22.1.1 next
20.22.1.2 obj_depth
20.22.1.3 obj_index
20.22.1.4 type
20.23hwloc_topology_diff_u Union Reference
20.23.1 Detailed Description
20.23.2 Field Documentation
20.23.2.1 generic
20.23.2.2 obj_attr
20.23.2.3 too_complex
20.24hwloc_topology_discovery_support Struct Reference
20.24.1 Detailed Description
20.24.2 Field Documentation
20.24.2.1 pu
20.25hwloc_topology_membind_support Struct Reference
20.25.1 Detailed Description
20.25.2 Field Documentation
20.25.2.1 alloc_membind
20.25.2.2 bind_membind
20.25.2.3 firsttouch_membind
20.25.2.4 get_area_membind

xxvi CONTENTS

	20.25.2.5 get_area_memlocation
	20.25.2.6 get_proc_membind
	20.25.2.7 get_thisproc_membind
	20.25.2.8 get_thisthread_membind
	20.25.2.9 interleave_membind
	20.25.2.10migrate_membind
	20.25.2.11nexttouch_membind
	20.25.2.12 eplicate_membind
	20.25.2.13set_area_membind
	20.25.2.14set_proc_membind
	20.25.2.15set_thisproc_membind
	20.25.2.16set_thisthread_membind
20.26hwloc_	_topology_support Struct Reference
20.26.	1 Detailed Description
20.26.	2 Field Documentation
	20.26.2.1 cpubind
	20.26.2.2 discovery
	20.26.2.3 membind

Chapter 1

Hardware Locality

Portable abstraction of hierarchical architectures for high-performance computing

1.1 Introduction

hwloc provides command line tools and a C API to obtain the hierarchical map of key computing elements, such as: NUMA memory nodes, shared caches, processor packages, processor cores, processing units (logical processors or "threads") and even I/O devices. hwloc also gathers various attributes such as cache and memory information, and is portable across a variety of different operating systems and platforms. Additionally it may assemble the topologies of multiple machines into a single one so as to let applications consult the topology of an entire fabric or cluster at once.

hwloc primarily aims at helping high-performance computing (HPC) applications, but is also applicable to any project seeking to exploit code and/or data locality on modern computing platforms.

hwloc supports the following operating systems:

- Linux (including old kernels not having sysfs topology information, with knowledge of cpusets, ScaleMP v

 SMP and Kerrighed support, etc.) on all supported hardware, including Intel Xeon Phi (KNL and KNC, either standalone or as a coprocessor) and NumaScale NumaConnect.
- Solaris (with support for processor sets and logical domains)
- AIX
- · Darwin / OS X
- FreeBSD and its variants (such as kFreeBSD/GNU)
- NetBSD
- OSF/1 (a.k.a., Tru64)
- HP-UX
- · Microsoft Windows
- IBM BlueGene/Q Compute Node Kernel (CNK)

2 Hardware Locality

Since it uses standard Operating System information, hwloc's support is mostly independant from the processor type (x86, powerpc, ...) and just relies on the Operating System support. The only exception to this is kFreeBSD, which does not support topology information, and hwloc thus uses an x86-only CPUID-based backend (which can be used for other OSes too, see the Components and plugins section).

To check whether hwloc works on a particular machine, just try to build it and run lstopo or lstopo-no-graphics. If some things do not look right (e.g. bogus or missing cache information), see Questions and Bugs below.

hwloc only reports the number of processors on unsupported operating systems; no topology information is available.

For development and debugging purposes, hwloc also offers the ability to work on "fake" topologies:

- Symmetrical tree of resources generated from a list of level arities, see Synthetic topologies.
- Remote machine simulation through the gathering of topology as XML files, see Importing and exporting topologies from/to XML files.

hwloc can display the topology in a human-readable format, either in graphical mode (X11), or by exporting in one of several different formats, including: plain text, PDF, PNG, and FIG (see Command-line Examples below). Note that some of the export formats require additional support libraries.

hwloc offers a programming interface for manipulating topologies and objects. It also brings a powerful CPU bitmap API that is used to describe topology objects location on physical/logical processors. See the Programming Interface below. It may also be used to binding applications onto certain cores or memory nodes. Several utility programs are also provided to ease command-line manipulation of topology objects, binding of processes, and so on.

Perl bindings are available from Bernd Kallies on CPAN.

Python bindings are available from Guy Streeter:

- Fedora RPM and tarball.
- git tree (html).

1.2 Installation

hwloc (http://www.open-mpi.org/projects/hwloc/) is available under the BSD license. It is hosted as a sub-project of the overall Open MPI project (http://www.open-mpi.org/). Note that hwloc does not require any functionality from Open MPI – it is a wholly separate (and much smaller!) project and code base. It just happens to be hosted as part of the overall Open MPI project.

Nightly development snapshots are available on the web site. Additionally, the code can be directly cloned from Git:

```
shell$ git clone https://github.com/open-mpi/hwloc.git
shell$ cd hwloc
shell$ ./autogen.sh
```

Note that GNU Autoconf >=2.63, Automake >=1.10 and Libtool >=2.2.6 are required when building from a Git clone.

Installation by itself is the fairly common GNU-based process:

1.2 Installation 3

```
shell$ ./configure --prefix=...
shell$ make
shell$ make install
```

The hwloc command-line tool "Istopo" produces human-readable topology maps, as mentioned above. It can also export maps to the "fig" file format. Support for PDF, Postscript, and PNG exporting is provided if the "Cairo" development package (usually cairo-devel or libcairo2-dev) can be found in "Istopo" when hwloc is configured and build.

The hwloc core may also benefit from the following development packages:

- libnuma for memory binding and migration support on Linux (numactl-devel or libnuma-dev package).
- libpciaccess for full I/O device discovery (libpciaccess-devel or libpciaccess-dev package). On Linux, PCI discovery may still be performed (without vendor/device names) even if libpciaccess cannot be used.
- · the AMD OpenCL implementation for OpenCL device discovery.
- · the NVIDIA CUDA Toolkit for CUDA device discovery.
- the NVIDIA Management Library (NVML) for NVML device discovery. It is available within the NVIDIA GPU Deployment Kit from https://developer.nvidia.com/gpu-deployment-kit.
- the NV-CONTROL X extension library (NVCtrl) for NVIDIA display discovery. The relevant development package is usually libXNVCtrl-devel or libxnvctrl-dev. It is also available within nvidia-settings from ftp://download.nvidia.com/XFree86/nvidia-settings/ and https://github.com/NVIDIA/nvidia-settings/.
- libxml2 for full XML import/export support (otherwise, the internal minimalistic parser will only be able to import XML files that were exported by the same hwloc release). See Importing and exporting topologies from/to XML files for details. The relevant development package is usually libxml2-devel or libxml2-dev.
- libudev on Linux for easier discovery of OS device information (otherwise hwloc will try to manually parse udev raw files). The relevant development package is usually libudev-devel or libudev-dev.
- libtool's ltdl library for dynamic plugin loading. The relevant development package is usually libtool-ltdl-devel or libltdl-dev.

PCI and XML support may be statically built inside the main hwloc library, or as separate dynamically-loaded plugins (see the Components and plugins section).

Note that because of the possibility of GPL taint, the pciutils library libpci will not be used (remember that hwloc is BSD-licensed).

Also note that if you install supplemental libraries in non-standard locations, hwloc's configure script may not be able to find them without some help. You may need to specify additional CPPFLAGS, LDFLAGS, or PKG_CONF← IG_PATH values on the configure command line.

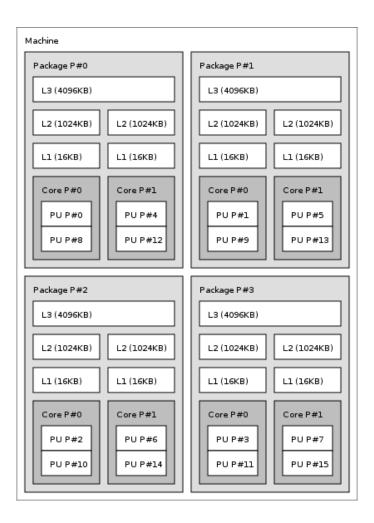
For example, if libpciaccess was installed into /opt/pciaccess, hwloc's configure script may not find it be default. Try adding PKG CONFIG PATH to the ./configure command line, like this:

```
./configure PKG_CONFIG_PATH=/opt/pciaccess/lib/pkgconfig \dots
```

4 Hardware Locality

1.3 Command-line Examples

On a 4-package 2-core machine with hyper-threading, the lstopo tool may show the following graphical output:



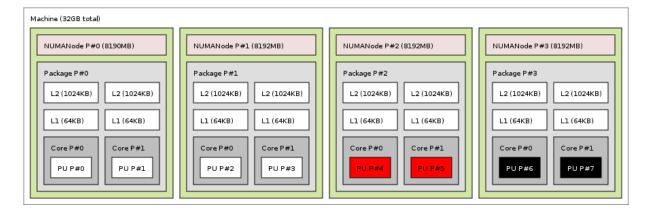
Here's the equivalent output in textual form:

```
Machine (16GB)
  Package L#0 + L3 L#0 (4096KB)
   L2 L#0 (1024KB) + L1 L#0 (16KB) + Core L#0
     PU L#0 (P#0)
     PU L#1 (P#8)
   L2 L#1 (1024KB) + L1 L#1 (16KB) + Core L#1
      PU L#2 (P#4)
     PU L#3 (P#12)
  Package L#1 + L3 L#1 (4096KB)
    L2 L#2 (1024KB) + L1 L#2 (16KB) + Core L#2
     PU L#4 (P#1)
     PU L#5 (P#9)
    L2 L#3 (1024KB) + L1 L#3 (16KB) + Core L#3
     PU L#6 (P#5)
     PU L#7 (P#13)
  Package L#2 + L3 L#2 (4096KB)
    L2 L#4 (1024KB) + L1 L#4 (16KB) + Core L#4
     PU L#8 (P#2)
     PU L#9 (P#10)
    L2 L#5 (1024KB) + L1 L#5 (16KB) + Core L#5
     PU L#10 (P#6)
     PU L#11 (P#14)
  Package L#3 + L3 L#3 (4096KB)
```

```
L2 L#6 (1024KB) + L1 L#6 (16KB) + Core L#6
PU L#12 (P#3)
PU L#13 (P#11)
L2 L#7 (1024KB) + L1 L#7 (16KB) + Core L#7
PU L#14 (P#7)
PU L#15 (P#15)
```

Note that there is also an equivalent output in XML that is meant for exporting/importing topologies but it is hardly readable to human-beings (see Importing and exporting topologies from/to XML files for details).

On a 4-package 2-core Opteron NUMA machine, the 1stopo tool may show the following graphical output:



Here's the equivalent output in textual form:

```
Machine (32GB)

NUMANode L#0 (P#0 8190MB) + Package L#0

L2 L#0 (1024KB) + L1 L#0 (64KB) + Core L#0 + PU L#0 (P#0)

L2 L#1 (1024KB) + L1 L#1 (64KB) + Core L#1 + PU L#1 (P#1)

NUMANode L#1 (P#1 8192MB) + Package L#1

L2 L#2 (1024KB) + L1 L#2 (64KB) + Core L#2 + PU L#2 (P#2)

L2 L#3 (1024KB) + L1 L#3 (64KB) + Core L#3 + PU L#3 (P#3)

NUMANode L#2 (P#2 8192MB) + Package L#2

L2 L#4 (1024KB) + L1 L#4 (64KB) + Core L#4 + PU L#4 (P#4)

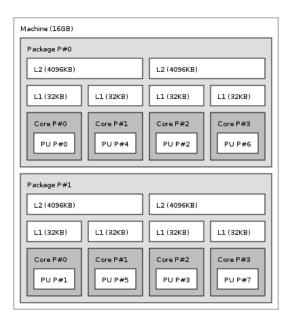
L2 L#5 (1024KB) + L1 L#5 (64KB) + Core L#5 + PU L#5 (P#5)

NUMANode L#3 (P#3 8192MB) + Package L#3

L2 L#6 (1024KB) + L1 L#6 (64KB) + Core L#6 + PU L#6 (P#6)

L2 L#7 (1024KB) + L1 L#6 (64KB) + Core L#6 + PU L#6 (P#6)
```

On a 2-package quad-core Xeon (pre-Nehalem, with 2 dual-core dies into each package):



6 Hardware Locality

Here's the same output in textual form:

```
Machine (16GB)

Package L#0

L2 L#0 (4096KB)

L1 L#1 (32KB) + Core L#0 + PU L#0 (P#0)

L1 L#1 (4096KB)

L1 L#2 (32KB) + Core L#1 + PU L#1 (P#4)

L2 L#1 (4096KB)

L1 L#2 (32KB) + Core L#2 + PU L#2 (P#2)

L1 L#3 (32KB) + Core L#3 + PU L#3 (P#6)

Package L#1

L2 L#2 (4096KB)

L1 L#4 (32KB) + Core L#4 + PU L#4 (P#1)

L1 L#5 (32KB) + Core L#5 + PU L#5 (P#5)

L2 L#3 (4096KB)

L1 L#6 (32KB) + Core L#6 + PU L#6 (P#3)

L1 L#6 (32KB) + Core L#6 + PU L#6 (P#3)
```

1.4 Programming Interface

The basic interface is available in **hwloc.h**. Some higher-level functions are available in **hwloc/helper.h** to reduce the need to manually manipulate objects and follow links between them. Documentation for all these is provided later in this document. Developers may also want to look at hwloc/inlines.h which contains the actual inline code of some **hwloc.h** routines, and at this document, which provides good higher-level topology traversal examples.

To precisely define the vocabulary used by hwloc, a Terms and Definitions section is available and should probably be read first.

Each hwloc object contains a cpuset describing the list of processing units that it contains. These bitmaps may be used for CPU binding and Memory binding. hwloc offers an extensive bitmap manipulation interface in **hwloc/bitmap.h**.

Moreover, hwloc also comes with additional helpers for interoperability with several commonly used environments. See the Interoperability With Other Software section for details.

The complete API documentation is available in a full set of HTML pages, man pages, and self-contained PDF files (formatted for both both US letter and A4 formats) in the source tarball in doc/doxygen-doc/.

NOTE: If you are building the documentation from a Git clone, you will need to have Doxygen and pdflatex installed – the documentation will be built during the normal "make" process. The documentation is installed during "make install" to \$prefix/share/doc/hwloc/ and your systems default man page tree (under \$prefix, of course).

1.4.1 Portability

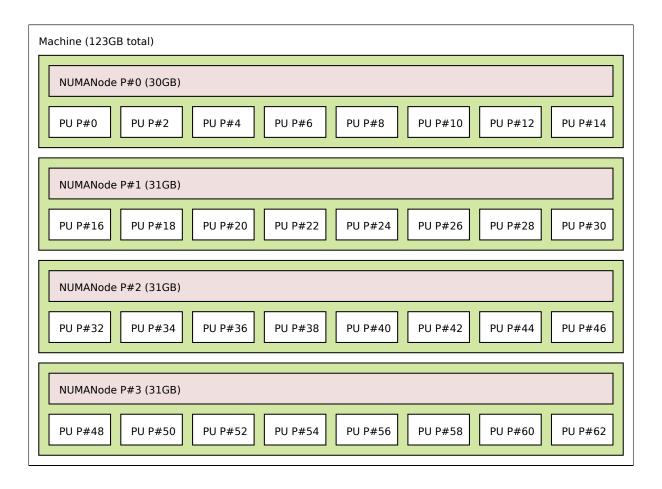
As shown in Command-line Examples, hwloc can obtain information on a wide variety of hardware topologies. However, some platforms and/or operating system versions will only report a subset of this information. For example, on an PPC64-based system with 32 cores (each with 2 hardware threads) running a default 2.6.18-based kernel from RHEL 5.4, hwloc is only able to glean information about NUMA nodes and processor units (PUs). No information about caches, packages, or cores is available.

Similarly, Operating System have varying support for CPU and memory binding, e.g. while some Operating Systems provide interfaces for all kinds of CPU and memory bindings, some others provide only interfaces for a limited number of kinds of CPU and memory binding, and some do not provide any binding interface at all. Hwloc's binding functions would then simply return the ENOSYS error (Function not implemented), meaning that the underlying Operating System does not provide any interface for them. CPU binding and Memory binding provide more information on which hwloc binding functions should be preferred because interfaces for them are usually available on the supported Operating Systems.

Here's the graphical output from Istopo on this platform when Simultaneous Multi-Threading (SMT) is enabled:



And here's the graphical output from Istopo on this platform when SMT is disabled:

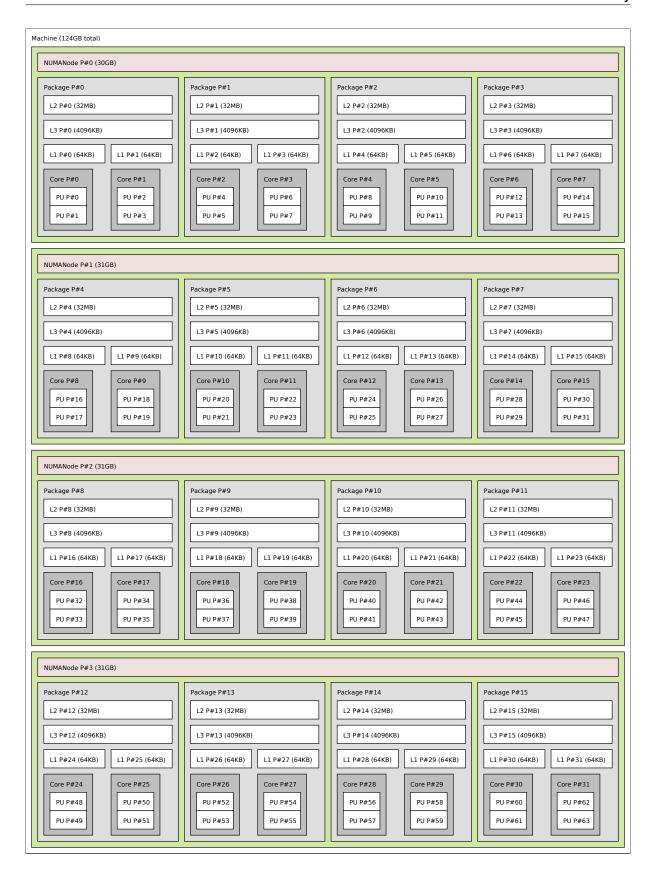


Notice that hwloc only sees half the PUs when SMT is disabled. PU #15, for example, seems to change location from NUMA node #0 to #1. In reality, no PUs "moved" – they were simply re-numbered when hwloc only saw half as many. Hence, PU #15 in the SMT-disabled picture probably corresponds to PU #30 in the SMT-enabled picture.

This same "PUs have disappeared" effect can be seen on other platforms – even platforms / OSs that provide much more information than the above PPC64 system. This is an unfortunate side-effect of how operating systems report information to hwloc.

Note that upgrading the Linux kernel on the same PPC64 system mentioned above to 2.6.34, hwloc is able to discover all the topology information. The following picture shows the entire topology layout when SMT is enabled:

8 Hardware Locality



Developers using the hwloc API or XML output for portable applications should therefore be extremely careful to not make any assumptions about the structure of data that is returned. For example, per the above reported PPC topology, it is not safe to assume that PUs will always be descendants of cores.

Additionally, future hardware may insert new topology elements that are not available in this version of hwloc. Long-lived applications that are meant to span multiple different hardware platforms should also be careful about making

structure assumptions. For example, there may someday be an element "lower" than a PU, or perhaps a new element may exist between a core and a PU.

1.4.2 API Example

The following small C example (available in the source tree as "doc/examples/hwloc-hello.c") prints the topology of the machine and performs some thread and memory binding. More examples are available in the doc/examples/directory of the source tree.

```
/* Example hwloc API program.
 \star See other examples under doc/examples/ in the source tree
 * for more details.
 * Copyright © 2009-2015 Inria. All rights reserved.
 * Copyright © 2009-2011 Université Bordeaux
 * Copyright © 2009-2010 Cisco Systems, Inc. All rights reserved.
 * See COPYING in top-level directory.
 * hwloc-hello.c
#include <hwloc.h>
#include <errno.h>
#include <stdio.h>
#include <string.h>
static void print_children(hwloc_topology_t topology,
      hwloc_obj_t obj,
                           int depth)
    char type[32], attr[1024];
    unsigned i:
    hwloc_obj_type_snprintf(type, sizeof(type), obj, 0);
printf("%*s%s", 2*depth, "", type);
    if (obj->os_index != (unsigned) -1)
  printf("#%u", obj->os_index);
    hwloc_obj_attr_snprintf(attr, sizeof(attr), obj, " ", 0);
    if (*attr)
      printf("(%s)", attr);
    printf("\n");
    for (i = 0; i < obj->arity; i++) {
        print_children(topology, obj->children[i], depth + 1);
}
int main(void)
    int depth;
    unsigned i, n;
    unsigned long size;
    int levels;
    char string[128];
    int topodepth;
    hwloc_topology_t topology;
    hwloc_cpuset_t cpuset;
    hwloc_obj_t obj;
    /* Allocate and initialize topology object. */
    hwloc_topology_init(&topology);
    /\star ... Optionally, put detection configuration here to ignore
       some objects types, define a synthetic topology, etc...
       The default is to detect all the objects of the machine that
       the caller is allowed to access. See Configure Topology
       Detection. */
    /* Perform the topology detection. */
    hwloc_topology_load(topology);
    /* Optionally, get some additional topology information
       in case we need the topology depth later.
    topodepth = hwloc_topology_get_depth(topology);
    /***********************
     * First example:
     * Walk the topology with an array style, from level 0 (always
```

10 Hardware Locality

```
\star the system level) to the lowest level (always the proc level).
for (depth = 0; depth < topodepth; depth++) {</pre>
   printf("*** Objects at level %d\n", depth);
   for (i = 0; i < hwloc_get_nbobjs_by_depth(topology, depth);</pre>
        i++) {
       hwloc_obj_type_snprintf(string, sizeof(string),
                             hwloc_get_obj_by_depth(topology, depth, i), 0);
       printf("Index %u: %s\n", i, string);
   }
}
/*****************
* Second example:
 \star Walk the topology with a tree style.
printf("*** Printing overall tree\n");
print_children(topology, hwloc_get_root_obj(topology), 0);
/*********************
 * Third example:
\star Print the number of packages.
 depth = hwloc_get_type_depth(topology, HWLOC_OBJ_PACKAGE);
if (depth == HWLOC_TYPE_DEPTH_UNKNOWN) {
   printf("*** The number of packages is unknown\n");
} else {
   printf("*** %u package(s)\n",
          hwloc_get_nbobjs_by_depth(topology, depth));
}
* Fourth example:
 \star Compute the amount of cache that the first logical processor
 \star has above it.
 levels = 0;
size = 0;
for (obj = hwloc_get_obj_by_type(topology, HWLOC_OBJ_PU, 0);
    obj;
    obj = obj->parent)
 if (obj->type == HWLOC_OBJ_CACHE) {
   levels++:
   size += obj->attr->cache.size;
printf("*** Logical processor 0 has %d caches totaling %luKB\n",
      levels, size / 1024);
/**********************
* Fifth example:
 * Bind to only one thread of the last core of the machine.
 \star First find out where cores are, or else smaller sets of CPUs if
 \star the OS doesn't have the notion of a "core".
 **********
depth = hwloc_get_type_or_below_depth(topology,
 HWLOC_OBJ_CORE);
/* Get last core. */
obj = hwloc_get_obj_by_depth(topology, depth,
             hwloc_get_nbobjs_by_depth(topology, depth) - 1);
if (obj) {
   /* Get a copy of its cpuset that we may modify. */
   cpuset = hwloc_bitmap_dup(obj->cpuset);
   /\star Get only one logical processor (in case the core is
      SMT/hyper-threaded). */
   hwloc_bitmap_singlify(cpuset);
   /\star And try to bind ourself there. \star/
   if (hwloc_set_cpubind(topology, cpuset, 0)) {
       char *str;
       int error = errno;
       hwloc_bitmap_asprintf(&str, obj->cpuset);
printf("Couldn't bind to cpuset %s: %s\n", str, strerror(error));
       free(str);
   /* Free our cpuset copy */
   hwloc_bitmap_free(cpuset);
 * Sixth example:
 \star Allocate some memory on the last NUMA node, bind some existing
 * memory to the last NUMA node.
 **********************
```

```
/* Get last node. */
n = hwloc_get_nbobjs_by_type(topology,
 HWLOC_OBJ_NUMANODE);
<u>if</u> (n) {
    void *m;
   size = 1024*1024;
    obj = hwloc_get_obj_by_type(topology,
  HWLOC\_OBJ\_NUMANODE, n - 1);
   m = hwloc_alloc_membind_nodeset(topology, size, obj->
  nodeset,
            HWLOC_MEMBIND_BIND, 0);
    hwloc_free(topology, m, size);
   m = malloc(size);
    hwloc_set_area_membind_nodeset(topology, m, size, obj->
  nodeset,
            HWLOC_MEMBIND_BIND, 0);
    free(m);
/\star Destroy topology object. \star/
hwloc_topology_destroy(topology);
return 0;
```

hwloc provides a pkg-config executable to obtain relevant compiler and linker flags. For example, it can be used thusly to compile applications that utilize the hwloc library (assuming GNU Make):

On a machine with 4GB of RAM and 2 processor packages – each package of which has two processing cores – the output from running hwloc-hello could be something like the following:

```
shell$ ./hwloc-hello
*** Objects at level 0
Index 0: Machine(3938MB)
*** Objects at level 1
Index 0: Package#0
Index 1: Package#1
*** Objects at level 2
Index 0: Core#0
Index 1: Core#1
Index 2: Core#3
Index 3: Core#2
*** Objects at level 3
Index 0: PU#0
Index 1: PU#1
Index 2: PU#2
Index 3: PU#3
*** Printing overall tree
Machine (3938MB)
  Package#0
   Core#0
     PU#0
    Core#1
      PU#1
  Package#1
    Core#3
     PII#2
    Core#2
     PU#3
*** 2 package(s)
shell$
```

12 Hardware Locality

1.5 Questions and Bugs

Questions should be sent to the devel mailing list (http://www.open-mpi.org/community/lists/hwloc.
php). Bug reports should be reported in the tracker (https://github.com/open-mpi/hwloc/issues).

If hwloc discovers an incorrect topology for your machine, the very first thing you should check is to ensure that you have the most recent updates installed for your operating system. Indeed, most of hwloc topology discovery relies on hardware information retrieved through the operation system (e.g., via the /sys virtual filesystem of the Linux kernel). If upgrading your OS or Linux kernel does not solve your problem, you may also want to ensure that you are running the most recent version of the BIOS for your machine.

If those things fail, contact us on the mailing list for additional help. Please attach the output of Istopo after having given the --enable-debug option to ./configure and rebuilt completely, to get debugging output. Also attach the /proc + /sys tarball generated by the installed script hwloc-gather-topology when submitting problems about Linux, or send the output of kstat cpu_info in the Solaris case, or the output of sysctl hw in the Darwin or BSD cases.

1.6 History / Credits

hwloc is the evolution and merger of the libtopology (http://runtime.bordeaux.inria.fr/libtopology/) project and the Portable Linux Processor Affinity (PLPA) (http://www.open-mpi.org/projects/plpa/) project. Because of functional and ideological overlap, these two code bases and ideas were merged and released under the name "hwloc" as an Open MPI sub-project.

libtopology was initially developed by the inria Runtime Team-Project (http://runtime.bordeaux. inria.fr/) (headed by Raymond Namyst (http://dept-info.labri.fr/~namyst/). PLPA was initially developed by the Open MPI development team as a sub-project. Both are now deprecated in favor of hwloc, which is distributed as an Open MPI sub-project.

1.7 Further Reading

The documentation chapters include

- · Terms and Definitions
- Command-Line Tools
- · Environment Variables
- · CPU and Memory Binding Overview
- I/O Devices
- · Miscellaneous objects
- Multi-node Topologies
- · Object attributes
- Importing and exporting topologies from/to XML files
- · Synthetic topologies
- · Interoperability With Other Software
- · Thread Safety
- · Components and plugins
- · Embedding hwloc in Other Software
- Frequently Asked Questions

Make sure to have had a look at those too!

Terms and Definitions

Object Interesting kind of part of the system, such as a Core, a Cache, a Memory node, etc. The different types detected by hwloc are detailed in the hwloc_obj_type_t enumeration.

They are topologically sorted by CPU set into a tree.

CPU set The set of logical processors (or processing units) logically included in an object (if it makes sense). They are always expressed using physical logical processor numbers (as announced by the OS). They are implemented as the hww.numbers (as announced by the OS). They are implemented as the <a href="https://hww.numbers.com/hww.numbers.c

Node set The set of NUMA memory nodes logically included in an object (if it makes sense). They are always expressed using physical node numbers (as announced by the OS). They are implemented with the hwloc-bitmap t opaque structure. as bitmaps.

Bitmap A possibly-infinite set of bits used for describing sets of objects such as CPUs (CPU sets) or memory nodes (Node sets). They are implemented with the hwloc bitmap t opaque structure.

Parent object The object logically containing the current object, for example because its CPU set includes the CPU set of the current object.

Ancestor object The parent object, or its own parent object, and so on.

Children object(s) The object (or objects) contained in the current object because their CPU set is included in the CPU set of the current object.

Arity The number of children of an object.

Sibling objects Objects which have the same parent. They usually have the same type (and hence are cousins, as well), but they may not if the topology is asymmetric.

Sibling rank Index to uniquely identify objects which have the same parent, and is always in the range [0, parent ← _arity).

Cousin objects Objects of the same type (and depth) as the current object, even if they do not have the same parent.

Level Set of objects of the same type and depth. All these objects are cousins.

Depth Nesting level in the object tree, starting from the root object. If the topology is symmetric, the depth of a child is equal to the parent depth plus one, and an object depth is also equal to the number of parent/child links between the root object and the given object. If the topology is asymmetric, the difference between some parent and child depths may be larger than one when some intermediate levels (for instance groups) are missing in only some parts of the machine.

14 Terms and Definitions

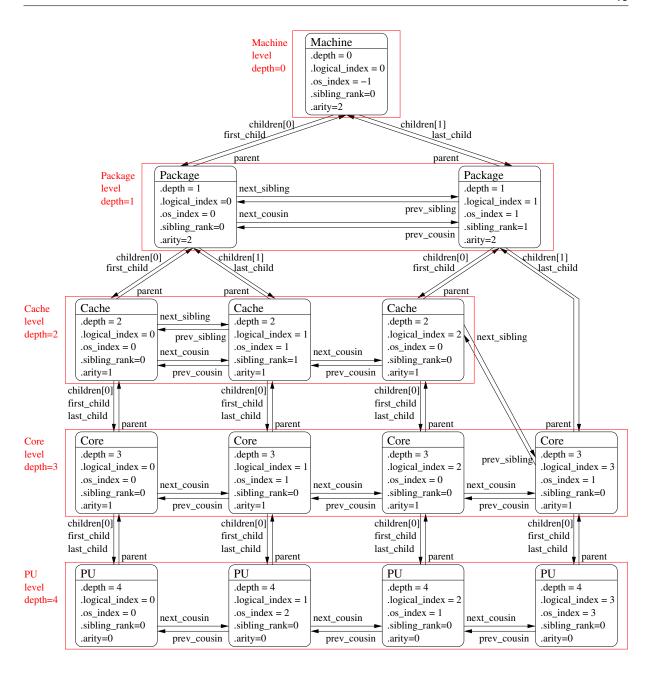
OS or physical index The index that the operating system (OS) uses to identify the object. This may be completely arbitrary, non-unique, non-contiguous, not representative of logical proximity, and may depend on the BIOS configuration. That is why hwloc almost never uses them, only in the default Istopo output (P # x) and cpuset masks. See also Should I use logical or physical/OS indexes? and how?

Logical index Index to uniquely identify objects of the same type and depth, automatically computed by hwloc according to the topology. It expresses logical proximity in a generic way, i.e. objects which have adjacent logical indexes are adjacent in the topology. That is why hwloc almost always uses it in its API, since it expresses logical proximity. They can be shown (as L#x) by lstopo thanks to the -l option. This index is always linear and in the range [0, num_objs_same_type_same_level-1]. Think of it as "cousin rank." The ordering is based on topology first, and then on OS CPU numbers, so it is stable across everything except firmware CPU renumbering. "Logical index" should not be confused with "Logical processor". A "Logical processor" (which in hwloc we rather call "processing unit" to avoid the confusion) has both a physical index (as chosen arbitrarily by BIOS/OS) and a logical index (as computed according to logical proximity by hwloc). See also Should I use logical or physical/OS indexes? and how?.

Processing unit The smallest processing element that can be represented by a hwloc object. It may be a single-core processor, a core of a multicore processor, or a single thread in a SMT processor. hwloc's PU acronym stands for Processing Unit.

Logical processor Synonym of "Processing unit". "Logical processor" should not be confused with "Logical index of a processor".

The following diagram can help to understand the vocabulary of the relationships by showing the example of a machine with two dual core packages (with no hardware threads); thus, a topology with 5 levels. Each box with rounded corner corresponds to one hwloc_obj_t, containing the values of the different integer fields (depth, logical — index, etc.), and arrows show to which other hwloc_obj_t pointers point to (first_child, parent, etc.). The L2 cache of the last core is intentionally missing to show how asymmetric topologies are handled. See What happens if my topology is asymmetric? for more information about such strange topologies.



It should be noted that for PU objects, the logical index – as computed linearly by hwloc – is not the same as the OS index.

16 Terms and Definitions

Command-Line Tools

hwloc comes with an extensive C programming interface and several command line utilities. Each of them is fully documented in its own manual page; the following is a summary of the available command line tools.

3.1 Istopo and Istopo-no-graphics

Istopo (also known as hwloc-ls) displays the hierarchical topology map of the current system. The output may be graphical, ascii-art or textual, and can also be exported to numerous file formats such as PDF, PNG, XML, and others. Advanced graphical outputs require the "Cairo" development package (usually cairo-devel or libcairo2-dev).

Istopo and Istopo-no-graphics accept the same command-line options. However graphical outputs are only available in Istopo. Textual outputs (those that do not depend on heavy external libraries such as Cairo) are supported in both Istopo and Istopo-no-graphics.

This command can also display the processes currently bound to a part of the machine (via the --ps option).

Note that Istopo can read XML files and/or alternate chroot filesystems and display topological maps representing those systems (e.g., use Istopo to output an XML file on one system, and then use Istopo to read in that XML file and display it on a different system).

3.2 hwloc-bind

hwloc-bind binds processes to specific hardware objects through a flexible syntax. A simple example is binding an executable to specific cores (or packages or bitmaps or ...). The hwloc-bind(1) man page provides much more detail on what is possible.

hwloc-bind can also be used to retrieve the current process' binding, or retrieve the last CPU(s) where a process ran, or operate on memory binding.

Just like hwloc-calc, the input locations given to hwloc-bind may be either objects or cpusets (bitmaps as reported by hwloc-calc or hwloc-distrib).

18 Command-Line Tools

3.3 hwloc-calc

hwloc-calc is hwloc's Swiss Army Knife command-line tool for converting things. The input may be either objects or cpusets (bitmaps as reported by another hwloc-calc instance or by hwloc-distrib), that may be combined by addition, intersection or subtraction. The output kinds include:

- a cpuset bitmap: This compact opaque representation of objects is useful for shell scripts etc. It may passed
 to hwloc command-line tools such as hwloc-calc or hwloc-bind, or to hwloc command-line options such as
 lstopo --restrict.
- the amount of the equivalent hwloc objects from a specific type, or the list of their indexes. This is useful for iterating over all similar objects (for instance all cores) within a given part of a platform.
- a hierarchical description of objects, for instance a thread index within a core within a package. This gives a better view of the actual location of an object.

Moreover, input and/or output may be use either physical/OS object indexes or as hwloc's logical object indexes. It eases cooperation with external tools such as taskset or numactl by exporting hwloc specifications into list of processor or NUMA node physical indexes. See also Should I use logical or physical/OS indexes? and how?.

3.4 hwloc-info

hwloc-info dumps information about the given objects, as well as all its specific attributes. It is intended to be used with tools such as grep for filtering certain attribute lines. When no object is specified, or when --topology is passed, hwloc-info prints a summary of the topology. When --support is passed, hwloc-info lists the supported features for the topology.

3.5 hwloc-distrib

hwloc-distrib generates a set of cpuset bitmaps that are uniformly distributed across the machine for the given number of processes. These strings may be used with hwloc-bind to run processes to maximize their memory bandwidth by properly distributing them across the machine.

3.6 hwloc-ps

hwloc-ps is a tool to display the bindings of processes that are currently running on the local machine. By default, hwloc-ps only lists processes that are bound; unbound process (and Linux kernel threads) are not displayed.

3.7 hwloc-distances

hwloc-distances displays all distance matrices attached to the topology. Note that Istopo may also display distance matrices in its verbose textual output. However Istopo only prints matrices that cover the entire topology while hwloc-distances also displays matrices that ignore part of the topology.

3.8 hwloc-annotate 19

3.8 hwloc-annotate

hwloc-annotate may add object attributes such as string information (see Custom string infos for details) or Misc children objects. It reads an input topology from a XML file and outputs the annotated topology as another XML file.

3.9 hwloc-diff, hwloc-patch and hwloc-compress-dir

hwloc-diff computes the difference between two topologies and outputs it to another XML file.

hwloc-patch reads such a difference file and applies to another topology.

hwloc-compress-dir compresses an entire directory of XML files by using hwloc-diff to save the differences between topologies instead of entire topologies.

3.10 hwloc-assembler

hwloc-assembler combines several XML topology files into a single multi-node XML topology. It may then be used later as input with hwloc_topology_set_xml() or with the HWLOC_XMLFILE environment variable. See Multi-node Topologies for details.

3.11 hwloc-assembler-remote

hwloc-assembler-remote is a frontend to hwloc-assembler. It takes care of contacting the given list of remote hosts (through ssh) and retrieving their topologies as XML before assembling them with hwloc-assembler.

3.12 hwloc-dump-hwdata

hwloc-dump-hwdata is a Linux and x86-specific tool that dumps (during boot, privileged) some topology and locality information from raw hardware files (SMBIOS and ACPI tables) to human-readable and world-accessible files that the hwloc library will later reuse.

Currently only used on Intel Knights Landing Xeon Phi platforms. See Why do I need hwloc-dump-hwdata for memory on Intel Knights Landing Xeon Phi?.

See HWLOC_DUMPED_HWDATA_DIR in Environment Variables for details about the location of dumped files.

3.13 hwloc-gather-topology

hwloc-gather-topology is a Linux-specific tool that saves the relevant topology files of the current machine into a tarball (and the corresponding Istopo output).

These files may be used later (possibly offline) for simulating or debugging a machine without actually running on it.

20 Command-Line Tools

Environment Variables

The behavior of the hwloc library and tools may be tuned thanks to the following environment variables.

HWLOC_XMLFILE=/path/to/file.xml enforces the discovery from the given XML file as if hwloc_topology_set_ ← xml() had been called. This file may have been generated earlier with Istopo file.xml. For convenience, this backend provides empty binding hooks which just return success. To have hwloc still actually call OS-specific hooks, HWLOC_THISSYSTEM should be set 1 in the environment too, to assert that the loaded file is really the underlying system. See also Importing and exporting topologies from/to XML files.

HWLOC_XML_VERBOSE=1

- HWLOC_SYNTHETIC_VERBOSE=1 enables verbose messages in the XML or synthetic topology backends. hwloc XML backends (see Importing and exporting topologies from/to XML files) can emit some error messages to the error output stream. Enabling these verbose messages within hwloc can be useful for understanding failures to parse input XML topologies. Similarly, enabling verbose messages in the synthetic topology backend can help understand why the description string is invalid. See also Synthetic topologies.
- HWLOC_FSROOT=/path/to/linux/filesystem-root/ switches to reading the topology from the specified Linux filesystem root instead of the main file-system root, as if hwloc_topology_set_fsroot() had been called. Not using the main file-system root causes hwloc_topology_is_thissystem() to return 0. For convenience, this backend provides empty binding hooks which just return success. To have hwloc still actually call OS-specific hooks, HWLOC_THISSYSTEM should be set 1 in the environment too, to assert that the loaded file is really the underlying system.
- HWLOC_THISSYSTEM=1 enforces the return value of hwloc_topology_is_thissystem(), as if HWLOC_TOP← OLOGY_FLAG_IS_THISSYSTEM was set with hwloc_topology_set_flags(). It means that it makes hwloc assume that the selected backend provides the topology for the system on which we are running, even if it is not the OS-specific backend but the XML backend for instance. This means making the binding functions actually call the OS-specific system calls and really do binding, while the XML backend would otherwise provide empty hooks just returning success. This can be used for efficiency reasons to first detect the topology once, save it to an XML file, and quickly reload it later through the XML backend, but still having binding functions actually do bind. This also enables support for the variable HWLOC_THISSYSTEM_ALLOWED← _RESOURCES.
- HWLOC_THISSYSTEM_ALLOWED_RESOURCES=1 Get the set of allowed resources from the native operating system even if the topology was loaded from XML or synthetic description, as if HWLOC_TOPOLOGY ← _FLAG_THISSYSTEM_ALLOWED_RESOURCES was set with hwloc_topology_set_flags(). This variable requires the topology to match the current system (see the variable HWLOC_THISSYSTEM). This is useful when the topology is not loaded directly from the local machine (e.g. for performance reason) and it comes with all resources, but the running process is restricted to only a part of the machine (for instance because of Linux Cgroup/Cpuset).

22 Environment Variables

HWLOC_HIDE_ERRORS=0 enables or disables verbose reporting of errors. The hwloc library may issue warnings to the standard error stream when it detects a problem during topology discovery, for instance if the operating system (or user) gives contradictory topology information. Setting this environment variable to 1 removes the actual displaying of these error messages.

- HWLOC_GROUPING=1 enables or disables objects grouping based on distances. By default, hwloc uses distance matrices between objects (either read from the OS or given by the user) to find groups of close objects. These groups are described by adding intermediate Group objects in the topology. Setting this environment variable to 0 will disable this grouping. This variable supersedes the obsolete HWLOC_IGNORE_DISTAN← CES variable.
- HWLOC_GROUPING_ACCURACY=0.05 relaxes distance comparison during grouping. By default, objects may be grouped if their distances form a minimal distance graph. When setting this variable to 0.02, these distances do not have to be strictly equal anymore, they may just be equal with a 2% error. If set to try instead of a numerical value, hwloc will try to group with perfect accuracy (0, the default), then with 0.01, 0.02, 0.05 and finally 0.1. Numbers given in this environment variable should always use a dot as a decimal mark (for instance 0.01 instead of 0,01).
- **HWLOC_GROUPING_VERBOSE=0** enables or disables some verbose messages during grouping. If this variable is set to 1, some debug messages will be displayed during distance-based grouping of objects even if debug was not specific at configure time. This is useful when trying to find an interesting distance grouping accuracy.

HWLOC_<type>_DISTANCES=index,...:X*Y

HWLOC_<type>_DISTANCES=begin-end:X*Y*Z

HWLOC_<type>_DISTANCES=index,...:distance,... sets a distance matrix for objects of the given type and physical indexes. The type should be given as its case-sensitive stringified value (e.g. NUMANode, Package, Cache, Core, PU). If another distance matrix already exists for the given type, either because the user specified it or because the OS offers it, it will be replaced by the given one.

If the variable value is none, the existing distance matrix for the given type is removed. Otherwise, the variable value first consists in a list of physical indexes that may be specified as a comma-separated list (e.g. 0, 2, 4, 1, 3, 5) or as a range of consecutive indexes (0-5). It is followed by a colon and the corresponding distances:

- If X*Y is given, X groups of Y close objects are specified.
- If X*Y*Z is given, X groups of Y groups of Z close objects are specified.
- Otherwise, the comma-separated list of distances should be given. If N objects are considered, the i*N+j-th value gives the distance from the i-th object to the j-th object. These distance values must use a dot as a decimal separator.

Note that distances are ignored in multi-node topologies.

- HWLOC_PCI_<domain>_<bus>_LOCALCPUS=<cpuset> changes the locality of I/O devices behind the specified PCI hostbridge. If no I/O locality information is available or if the BIOS reports incorrect information, it is possible to move a I/O device tree (the entire set of objects behind a host bridge) near a custom set of processors. domain and bus are the PCI domain and primary bus of the corresponding host bridge.
- **HWLOC_PLUGINS_PATH=/path/to/hwloc/plugins/:...** changes the default search directory for plugins. By default, \$libdir/hwloc is used. The variable may contain several colon-separated directories.
- **HWLOC_PLUGINS_VERBOSE=1** displays verbose information about plugins. List which directories are scanned, which files are loaded, and which components are successfully loaded.
- **HWLOC_PLUGINS_BLACKLIST=filename1,filename2,...** prevents plugins from being loaded if their filename (without path) is listed. Plugin filenames may be found in verbose messages outputted when HWLOC_PL ∪GINS_VERBOSE=1.
- HWLOC_DUMPED_HWDATA_DIR=/path/to/dumped/files/ loads files dumped by hwloc-dump-hwdata (on Linux) from the given directory. The default dump/load directory is configured during build based on --runstatedir, --localstatedir, and --prefix options. It usually points to /var/run/hwloc/ in Linux distribution packages, but it may also point to \$prefix/var/run/hwloc/ when manually installing and only specifying --prefix.

- HWLOC_COMPONENTS=list,of,components forces a list of components to enable or disable. Enable or disable the given comma-separated list of components (if they do not conflict with each other). Component names prefixed with are disabled. Once the end of the list is reached, hwloc falls back to enabling the remaining components (sorted by priority) that do not conflict with the already enabled ones, and unless explicitly disabled in the list. If stop is met, the enabling loop immediately stops, no more component is enabled. If the variable is set to an empty string, no specific component is loaded first, all components are loaded in priority order, this is strictly identical to not specifying any variable. The xml component name may be followed by a XML file to load (xml=file.xml). The synthetic component may be followed by a basic synthetic topology description (synthetic=node:2 pu:3, see Synthetic topologies). This variable does not take precedence over the application selecting components with functions such as hwloc_topology_set_xml(). See Components and plugins for details.
- **HWLOC_COMPONENTS_VERBOSE=1** displays verbose information about components. Display messages when components are registered or enabled. This is the recommended way to list the available components with their priority (all of them are *registered* at startup).
- **HWLOC_DEBUG_VERBOSE=0** disables all verbose messages that are enabled by default when <code>-enable-debug</code> is passed to configure.

24 Environment Variables

CPU and Memory Binding Overview

Some operating systems do not systematically provide separate functions for CPU and memory binding. This means that CPU binding functions may have have effects on the memory binding policy. Likewise, changing the memory binding policy may change the CPU binding of the current thread. This is often not a problem for applications, so by default hwloc will make use of these functions when they provide better binding support.

If the application does not want the CPU binding to change when changing the memory policy, it needs to use the HWLOC_MEMBIND_NOCPUBIND flag to prevent hwloc from using OS functions which would change the CPU binding. Additionally, HWLOC_CPUBIND_NOMEMBIND can be passed to CPU binding function to prevent hwloc from using OS functions would change the memory binding policy. Of course, using these flags will reduce hwloc's overall support for binding, so their use is discouraged.

One can avoid using these flags but still closely control both memory and CPU binding by allocating memory, touching each page in the allocated memory, and then changing the CPU binding. The already-really-allocated memory will then be "locked" to physical memory and will not be migrated. Thus, even if the memory binding policy gets changed by the CPU binding order, the already-allocated memory will not change with it. When binding and allocating further memory, the CPU binding should be performed again in case the memory binding altered the previously-selected CPU binding.

Not all operating systems support the notion of a "current" memory binding policy for the current process, but such operating systems often still provide a way to allocate data on a given node set. Conversely, some operating systems support the notion of a "current" memory binding policy and do not permit allocating data on a specific node set without changing the current policy and allocate the data. To provide the most powerful coverage of these facilities, hwloc provides:

- functions that set/get the current memory binding policies (if supported): hwloc_set/get_membind_*() and hwloc_set/get_proc_membind()
- functions that allocate memory bound to specific node set without changing the current memory binding policy (if supported): hwloc_alloc_membind() and hwloc_alloc_membind_nodeset().
- helpers which, if needed, change the current memory binding policy of the process in order to obtain memory binding: hwloc_alloc_membind_policy() and hwloc_alloc_membind_policy_nodeset()

An application can thus use the two first sets of functions if it wants to manage separately the global process binding policy and directed allocation, or use the third set of functions if it does not care about the process memory binding policy.

See CPU binding and Memory binding for hwloc's API functions regarding CPU and memory binding, respectively. There are some examples under doc/examples/ in the source tree.

I/O Devices

hwloc usually manipulates processing units and memory but it can also discover I/O devices and report their locality as well. This is useful for placing I/O intensive applications on cores near the I/O devices they use, or for gathering information about all platform components.

6.1 Enabling and requirements

I/O discovery is disabled by default (except in Istopo) so as not to break legacy application by adding unexpected I/O objects to the topology. It can be enabled by passing flags such as HWLOC_TOPOLOGY_FLAG_IO_DEVICES to hwloc_topology_set_flags() before loading the topology.

Note that I/O discovery requires significant help from the operating system. The pciaccess library (the development package is usually <code>libpciaccess-devel</code> or <code>libpciaccess-dev</code>) is needed to fully detect PCI devices and bridges. On Linux, PCI discovery may still be performed even if <code>libpciaccess</code> cannot be used. But it misses PCI device names. Moreover, some operating systems require privileges for probing PCI devices, see <code>Does hwloc require privileged access?</code> for details.

The actual locality of I/O devices is only currently detected on Linux. Other operating system will just reported I/O devices as being attached to the topology root object.

6.2 I/O objects

When I/O discovery is enabled and supported, some additional objects are added to the topology. The corresponding I/O object types are:

- HWLOC_OBJ_OS_DEVICE describes an operating-system-specific handle such as the *sda* drive or the *eth0* network interface. See OS devices.
- HWLOC_OBJ_PCI_DEVICE and HWLOC_OBJ_BRIDGE build up a PCI hierarchy made of devices and bridges. See PCI devices and bridges.
- HWLOC_OBJ_MISC describes miscellaneous devices such as memory modules (DIMMs). These are neither PCI nor OS handles, therefore they appear as Misc objects. See Misc objects added by hwloc.

hwloc tries to attach these new objects to normal objects (usually NUMA nodes) to match their actual physical location. For instance, if a I/O Hub is physically connected to a package, the corresponding hwloc bridge object (and its PCI bridges and devices children) is inserted as a child of the corresponding hwloc Package object.

I/O objects also have neither CPU sets nor node sets (NULL pointers) because they are not directly usable by the user applications for binding. Moreover I/O hierarchies may be highly complex (asymmetric trees of bridges). So I/O objects are placed in specific levels with custom depths. Their lists may still be traversed with regular helpers such as hwloc_get_next_obj_by_type(). However, hwloc offers some dedicated helpers such as hwloc_get_next_pcidev() and hwloc_get_next_osdev() for convenience (see Finding I/O objects).

28 I/O Devices

6.3 OS devices

Although each PCI device is uniquely identified by its bus ID (e.g. 0000:01:02.3), a user-space application can hardly find out which PCI device it is actually using. Applications rather use software handles (such as the *eth0* network interface, the *sda* hard drive, or the *mlx4_0* OpenFabrics HCA). Therefore hwloc tries to add software devices (HWLOC_OBJ_OS_DEVICE, also known as OS devices) below their PCI objects.

hwloc first tries to discover OS devices from the operating system, e.g. *eth0*, *sda* or *mlx4_0*. However, this ability is currently only available on Linux for some classes of devices.

hwloc then tries to discover software devices through additional I/O components using external libraries. For instance proprietary graphics drivers do not expose any named OS device, but hwloc may still create one OS object per software handle when supported. For instance the opencl and cuda components may add some opencl0d0 and cuda0 OS device objects.

Here is a list of OS device objects commonly created by hwloc components when I/O discovery is enabled and supported.

- Hard disks (HWLOC_OBJ_OSDEV_BLOCK)
 - sda (Linux component)
- Network interfaces (HWLOC OBJ OSDEV NETWORK)
 - eth0, wlan0, ib0 (Linux component)
- OpenFabrics (InfiniBand, Omni-Path, usNIC, etc) HCAs (HWLOC OBJ OSDEV OPENFABRICS)
 - mlx5_0, hfi1_0, qib0, usnic_0 (Linux component)
- GPUs (HWLOC_OBJ_OSDEV_GPU)
 - nvml0 for the first NVML device (NVML component, using the NVIDIA Management Library)
 - :0.0 for the first display (GL component, using the NV-CONTROL X extension library, NVCtrl)
- Co-Processors (HWLOC_OBJ_OSDEV_COPROC)
 - opencl0d0 for the first device of the first OpenCL platform, opencl1d3 for the fourth device of the second OpenCL platform (OpenCL component)
 - cuda0 for the first NVIDIA CUDA device (CUDA component, using the NVIDIA CUDA Library)
 - mic0 for the first Intel Xeon Phi (MIC) coprocessor (Linux component)
- DMA engine channel (HWLOC_OBJ_OSDEV_DMA)
 - dma0chan0 (Linux component) when full I/O discovery is enabled (HWLOC_TOPOLOGY_FLAG_W
 HOLE_IO)

When none of the above strategies is supported and enabled, hwloc cannot place any OS object inside PCI objects. Note that some PCI devices may contain multiple software devices (see the example below).

See also Interoperability With Other Software for managing these devices without considering them as hwloc objects.

6.4 PCI devices and bridges

A PCI hierarchy is usually organized as follows: A hostbridge object (<a href="https://hww.nobe.ncb/h

Each hostbridge contains one or several children, either other bridges (usually PCI to PCI) or PCI devices (H WLOC_OBJ_PCI_DEVICE). The number of bridges between the hostbridge and a PCI device depends on the machine and on the topology flags.

6.5 Consulting I/O devices and binding

I/O devices may be consulted by traversing the topology manually (with usual routines such as hwloc_get_obj_by ← _type()) or by using dedicated helpers (such as hwloc_get_pcidev_by_busid(), see Finding I/O objects).

I/O objects do not actually contain any locality information because their CPU sets and node sets are NULL. Their locality must be retrieved by walking up the object tree (through the parent link) until an non-I/O object is found (see <a href="https://hww.non-null.com/null

Command-line tools are also aware of I/O devices. Istopo displays the interesting ones by default (passing --no-io disables it).

hwloc-calc and hwloc-bind may manipulate I/O devices specified by PCI bus ID or by OS device name.

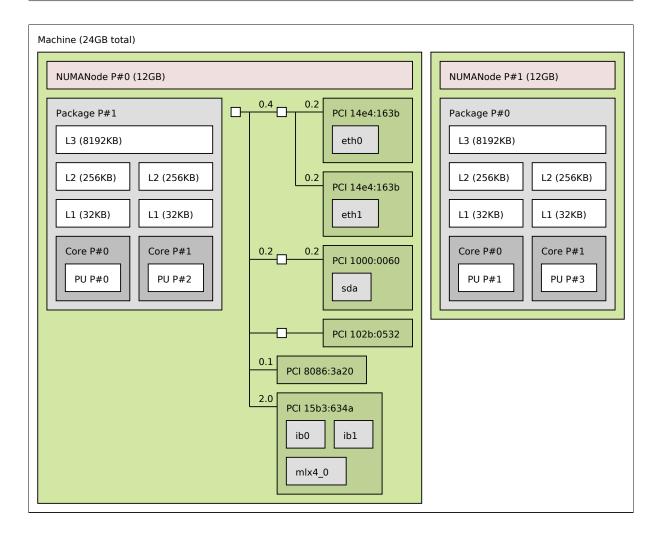
- pci=0000:02:03.0 is replaced by the set of CPUs that are close to the PCI device whose bus ID is given.
- os=eth0 is replaced by CPUs that are close to the I/O device whose software handle is called eth0.

This enables easy binding of I/O-intensive applications near the device they use.

6.6 Examples

The following picture shows a dual-package dual-core host whose PCI bus is connected to the first package and NUMA node.

30 I/O Devices



Six interesting PCI devices were discovered. However hwloc found some corresponding software devices (*eth0*, *eth1*, *sda*, *mlx4_0*, *ib0*, and *ib1*) for only four of these physical devices. The other ones (*PCI 102b:0532* and *PCI 8086:3a20*) are an unused IDE controller (no disk attached) and a graphic card (no corresponding software device reported to the user by the operating system).

On the contrary, it should be noted that three different software devices were found for the last PCI device ($P \leftarrow CI \ 15b3:634a$). Indeed this OpenFabrics HCA PCI device object contains one one OpenFabrics software device ($mlx4_0$) and two virtual network interface software devices (ib0 and ib1).

PCI link speed is also reported for some bridges and devices because Istopo was privileged when it discovered the topology.

Here is the corresponding textual output:

```
Machine (24GB)

NUMANode L#0 (P#0 12GB)

Package L#0 + L3 L#0 (8192KB)

L2 L#0 (256KB) + L1 L#0 (32KB) + Core L#0 + PU L#0 (P#0)

L2 L#1 (256KB) + L1 L#1 (32KB) + Core L#1 + PU L#1 (P#2)

HostBridge

PCIBridge

PCI 14e4:163b

Net "eth0"

PCI 14e4:163b

Net "eth1"

PCIBridge

PCI 1000:0060

Block "sda"
```

6.6 Examples 31

```
PCIBridge
    PCI 102b:0532
PCI 8086:3a20
PCI 15b3:634a
    Net "ib0"
    Net "ib1"
    Net "mlx4_0"

NUMANode L#1 (P#1 12GB) + Package L#1 + L3 L#1 (8192KB)
L2 L#2 (256KB) + L1 L#2 (32KB) + Core L#2 + PU L#2 (P#1)
L2 L#3 (256KB) + L1 L#3 (32KB) + Core L#3 + PU L#3 (P#3)
```

32 I/O Devices

Miscellaneous objects

hwloc topologies may be annotated with Misc objects (of type HWLOC_OBJ_MISC) either automatically or by the user. This is an flexible way to annotate topologies with large sets of information since Misc objects may be inserted anywhere in the topology (to annotate specific objects or parts of the topology), even below other Misc objects, and each of them may contain multiple attributes (ee also How do I annotate the topology with private notes?).

These Misc objects may have a Type info attribute to replace Misc with something else in the Istopo output.

7.1 Misc objects added by hwloc

hwloc only uses Misc objects when other object types are not sufficient. This currently includes:

• Memory modules (DIMMs), on Linux when privileged and when dmi-sysfs is supported by the kernel, and when I/O discovery is enabled. These objects have a Type info attribute of value MemoryModule. They are currently always attached to the root object. Their attributes describe the DIMM vendor, model, etc. lstopo -v displays them as:

```
Misc(MemoryModule) (P#1 Type=MemoryModule DeviceLocation="Bottom-Slot 2(right)" BankLocation="BANK 2" Vendor=Elpida SerialNumber=21733667 AssetTag=9876543210 PartNumber="EBJ81UG8EFU0-GN-F")
```

• Displaying process binding in lstopo —top. These objects have a Type info attribute of value Process and a name attribute made of their PID and program name. They are attached below the object they are bound to. The textual lstopo displays them as:

```
PU L#0 (P#0)
Misc(Process) 4445 myprogram
```

7.2 Annotating topologies with Misc objects

The user may annotate hwloc topologies with its own Misc objects. A Misc object may be inserted anywhere in the topology by specifying its CPU set (using hwloc_topology_insert_misc_object_by_cpuset()). Or it may be inserted as a leaf of the topology by specifying its parent (with hwloc_topology_insert_misc object_by_parent()).

Multi-node Topologies

hwloc is usually used for consulting and manipulating single machine topologies. This includes large systems as long as a single instance of the operating system manages the entire system. However it is sometimes desirable to have multiple independent hosts inside the same topology, for instance when applying algorithms to an entire cluster topology. hwloc therefore offers the ability to agregate multiple host topologies into a single global one.

8.1 Multi-node Objects Specifities

A multi-node topology contains several single-node topologies. Those are assembled by making their own root objects (usually Machine object) children of higher objects. These higher objects include at least the root of the global topology (usually a System object). Some intermediate objects may also exists, for instance to represent switches in a large fabric.

There are actually three possible types of objects that have different properties with respect to cpusets, nodesets and binding. Indeed those cpusets and nodesets were designed for execution and memory binding within a single operating system. Binding on another system or across several different systems would be meaningless.

Local objects Any object that corresponds to the local machine may be manipulated as usual. Obviously, if the multi-node topology does not contain the local machine topology, no such local object exists.

Objects from other nodes Any object that comes from inside another node is represented as usual but its cpusets and nodesets should not be used for binding since binding on another system makes no sense.

Objects above single nodes Any object above single-node topologies does not have any cpuset or nodeset pointer because binding across multiple systems makes no sense. This includes the glocal root object of a multi-node topology and possibly some intermediate objects between this global root and the local root of single-node topologies.

It is important to keep this in mind before binding using multi-node topologies. To make sure binding on an object is possible, one should first check that its cpuset or nodeset pointer is not NULL. Then, one should check whether the object is indeed local.

To find out which machine a given object corresponds to, one may look at the info attributes of the parent Machine object. The HostName info is usually available in Machine objects, it may be retrieved with the following code:

```
hwloc_obj_t machine_obj;
obj = hwloc_get_ancestor_obj_by_type(topology, HWLOC_OBJ_MACHINE, obj);
if (machine_obj)
  return hwloc_obj_get_info_by_name(machine_obj, "HostName");
else
  return NULL;
```

The hwloc assembler scripts (see below) also add AssemblerName and AssemblerIndex info attributes to the Machine objects to identify the corresponding host name and index during assembly.

8.2 Assembling topologies with command-line tools

One way to manipulate multinode topologies is to retrieve other nodes' topologies as XML files and combine them as a global XML topology. It may then be loaded with hwloc_topology_set_xml() or with the HWLOC_XMLFILE environment variable.

The hwloc-assembler and hwloc-assembler-remote utilities offer the ability to combine XML topologies or remote nodes' topologies (see Command-Line Tools).

8.3 Assembling topologies with the programming interface

The hwloc programming interface offers the ability to build multinode topologies using the *custom* interface. A new multinode topology has to be initialized with hwloc_topology_init() and then set to custom with hwloc_topology_\iff set_custom(). Topologies and objects mat then be assembled. Later, the custom topology is finalized as usual with hwloc_topology_load().

A custom topology starts with a single root object of type System. It may be modified by inserting a new child object with hwloc_custom_insert_group_object_by_parent() or by duplicating another topology with hwloc_custom—insert_topology(). Both of these operations require to specify the parent object in the custom topology where the insertion will take place. This parent may be either the root (returned by hwloc_get_root_obj()) or an already-inserted object (returned by hwloc_custom_insert_group_object_by_parent()).

Ideally, any existing object in the custom topology could be the parent. However, special care should be taken when traversing the topology to find such an object because most links between objects (children, siblings, cousins) are not setup until hwloc topology load() is invoked.

8.4 Example of assembly with the programming interface

If the topologies of two hosts have been previously gathered in XML files host1.xml and host2.xml, the global topology may be assembled with the following code.

```
hwloc_topology_t host1, host2, global;
/* initialize global topology */
hwloc_topology_init(&global);
hwloc_topology_set_custom(global);
/* insert host1 entire topology below the global topology root */
hwloc_topology_init(&host1);
hwloc_topology_load(host1);
hwloc_custom_insert_topology(global, hwloc_get_root_obj(global),
                              host1, NULL);
hwloc_topology_destroy(host1);
/\star insert host2 entire topology below the global topology root \star/
hwloc_topology_init(&host2);
hwloc_topology_load(host2);
hwloc_custom_insert_topology(global, hwloc_get_root_obj(global),
                              host2, NULL);
hwloc_topology_destroy(host2);
/\star load and play with the global topology \star/
hwloc_topology_load(global);
```

If a intermediate object such as a switch should be inserted above one of the host topologies:

Object attributes

9.1 Normal attributes

hwloc objects have many attributes. The hwloc_obj structure contains a common set of attributes that are available for object types, for instance their type or logical_index.

Each object also contains an attr field that, if non NULL, points to a union hwloc_obj_attr_u of type-specific attribute structures. For instance, a Cache object obj contains cache-specific information in obj->attr->cache, such as its size and associativity. See hwloc_obj_attr_u for details.

9.2 Custom string infos

Aside from the name field of each object, hwloc annotates many objects with string attributes that are made of a key and a value. Each object contains a list of such pairs that may be consulted manually (looking at the object infos array field) or using the hwloc_obj_get_info_by_name(). The user may additionally add new key-value pairs to any object using hwloc_obj_add_info() or the hwloc-annotate program.

Here is a non-exhaustive list of attributes that may be automatically added by hwloc (with the usual corresponding object in parentheses). Note that these attributes heavily depend on the ability of the operating system to report them. Many of them will therefore be missing on some OS.

- **OSName, OSRelease, OSVersion, HostName, Architecture (Machine object)** The operating system name, release, version, the hostname and the architecture name, as reported by the Unix uname command.
- **Backend (Machine object or topology root object)** The name of the hwloc backend/component that filled the topology. If several components were combined, multiple Backend keys may exist, with different values, for instance x86, Linux and pci.
- LinuxCgroup (Machine object) The name the Linux control group where the calling process is placed.
- **SyntheticDescription (topology root object)** The description string that was given to hwloc to build this synthetic topology.
- **CPUModel (Package or Machine)** The processor model name. Usually added to Package objects, but can be in Machine instead if hwloc failed to discover any package.
- CPUType (Package) A Solaris-specific general processor type name, such as "i86pc".

40 Object attributes

CPUVendor, CPUModelNumber, CPUFamilyNumber, CPUStepping (Package or Machine) The processor vendor name, model number, family number, and stepping number. Currently available for x86 and Xeon Phi processors on most systems, and for ia64 processors on Linux (except CPUStepping). Usually added to Package objects, but can be in Machine instead if hwloc failed to discover any package.

- **CPURevision (Package)** A POWER/PowerPC-specific general processor revision number, currently only available on Linux.
- PlatformName, PlatformModel, PlatformVendor, PlatformBoardID, PlatformRevision,
- **SystemVersionRegister, ProcessorVersionRegister (Machine)** Some POWER/PowerPC-specific attributes describing the platform and processor. Currently only available on Linux. Usually added to Package objects, but can be in Machine instead if hwloc failed to discover any package.
- MemoryMode, ClusterMode (topology root object) Intel Knights Landing configuration modes, only available if hwloc-dump-hwdata was used (see Why do I need hwloc-dump-hwdata for memory on Intel Knights Landing Xeon Phi?). The memory mode may be Cache, Flat, Hybrid50 (half the MCDRAM is used as a cache) or Hybrid25 (25% of MCDRAM as cache). The cluster mode may be Quadrant, Hemisphere, All2All, SN← C2 or SNC4. See doc/examples/get-knl-modes.c in the source directory for an example of retrieving these attributes.
- **Inclusive (Caches)** The inclusiveness of a cache (1 if inclusive, 0 otherwise). Currently only available on x86 processors.
- **SolarisProcessorGroup (Group)** The Solaris kstat processor group name that was used to build this Group object.
- PCIVendor, PCIDevice (PCI devices and bridges) The vendor and device names of the PCI device.
- **PCISIot** The name/number of the physical slot where the PCI device is plugged.
- **Vendor, Model, Revision, SerialNumber** The vendor and model names, revision, and serial number of a Block OS device.
- LinuxDeviceID The major/minor device number such as 8:0 on Linux for a Block OS device.
- **CoProcType (Co-Processor OS devices)** The type of co-processor, for instance "MIC", "CUDA" or "Open ← CL".
- GPUVendor, GPUModel (GPU or Co-Processor OS devices) The vendor and model names of the GPU device.
- OpenCLDeviceType, OpenCLPlatformIndex,
- **OpenCLPlatformName, OpenCLPlatformDeviceIndex (OpenCL GPU OS devices)** The type of OpenCL device, the OpenCL platform index and name, and the index of the device within the platform.
- **OpenCLComputeUnits, OpenCLGlobalMemorySize** The number of compute units and global memory size (in kB) of a OpenCL device.
- NVIDIAUUID, NVIDIASerial (NVML GPU OS devices) The UUID and Serial of NVIDIA GPUs.
- CUDAMultiProcessors, CUDACoresPerMP,
- CUDAGlobalMemorySize, CUDAL2CacheSize, CUDASharedMemorySizePerMP (CUDA OS devices) The number of shared multiprocessors, the number of cores per multiprocessor, the global memory size, the (global) L2 cache size, and size of the shared memory in each multiprocessor of a CUDA device. Sizes are in kB.
- MICSerialNumber The serial number of an Intel Xeon Phi (MIC) coprocessor. hwloc may run either inside the coprocessor itself, or on the host processor. That attribute is set in both cases, so that the exact same coprocessor may be identified from both point of views, even if there are multiple nodes with multiple MICs. When running hwloc on the host, each hwloc OS device object that corresponds to a Xeon Phi gets such an attribute. When running hwloc inside a Xeon Phi coprocessor, the root object of the topology gets this attribute.

- **MICFamily, MICSKU, MICActiveCores, MICMemorySize** The family, SKU (model), number of active cores, and memory size (in kB) of an Intel Xeon Phi (MIC) coprocessor.
- **DMIBoardVendor, DMIBoardName, etc. (Machine object)** DMI hardware information such as the motherboard and chassis models and vendors, the BIOS revision, etc., as reported by Linux under /sys/class/dmi/id/.
- **Address, Port (Network interface OS devices)** The MAC address and the port number of a software network interface, such as eth4 on Linux.
- NodeGUID, SysImageGUID, Port1State, Port2LID, Port2LMC, Port3GID1 (OpenFabrics OS devices) The node GUID and GUID mask, the state of a port #1 (value is 4 when active), the LID and LID mask count of port #2, and GID #1 of port #3.
- **Type** A better type name than the usual one. This may be used to specify where Groups come from. For instance Linux S/390 *books* appear as Groups of type *Book* (see also What are these Group objects in my topology?). Block OS devices may have a Type of "Disk", "Tape", "Removable Media Device" or "Other". The Type attribute value is displayed instead of the default object type name in Istopo.
- **Vendor, AssetTag, PartNumber, DeviceLocation, BankLocation (MemoryModule Misc objects)** Information about memory modules (DIMMs) extracted from SMBIOS.
- **hwlocVersion** The version number of the hwloc library that was used to generate the topology. If the topology was loaded from XML, this is not the hwloc version that loaded it, but rather the first hwloc instance that exported the topology to XML earlier.
- **ProcessName** The name of the process that contains the hwloc library that was used to generate the topology. If the topology was from XML, this is not the hwloc version that loaded it, but rather the first process that exported the topology to XML earlier.

Here is a non-exhaustive list of user-provided info attributes that have a special meaning:

IstopoStyle Enforces the style of an object (background and text colors) in the graphical output of Istopo. See CUSTOM COLORS in the Istopo(1) manpage for details.

42 Object attributes

Importing and exporting topologies from/to XML files

hwloc offers the ability to export topologies to XML files and reload them later. This is for instance useful for loading topologies faster (see I do not want hwloc to rediscover my enormous machine topology every time I rerun a process), manipulating other nodes' topology, or avoiding the need for privileged processes (see Does hwloc require privileged access?).

Topologies may be exported to XML files thanks to hwloc_topology_export_xml(), or to a XML memory buffer with hwloc_topology_export_xmlbuffer(). The Istopo program can also serve as a XML topology export tool.

XML topologies may then be reloaded later with hwloc_topology_set_xml() and hwloc_topology_set_xmlbuffer(). The HWLOC_XMLFILE environment variable also tells hwloc to load the topology from the given XML file.

Note

Loading XML topologies disables binding because the loaded topology may not correspond to the physical machine that loads it. This behavior may be reverted by asserting that loaded file really matches the underlying system with the HWLOC_THISSYSTEM environment variable or the HWLOC_TOPOLOGY_FLAG_IS_TH ISSYSTEM topology flag.

The topology flag HWLOC_TOPOLOGY_FLAG_THISSYSTEM_ALLOWED_RESOURCES may be used to load a XML topology that contains the entire machine and restrict it to the part that is actually available to the current process (e.g. when Linux Cgroup/Cpuset are used to restrict the set of resources).

hwloc also offers the ability to export/import Topology differences.

XML topology files are not localized. They use a dot as a decimal separator. Therefore any exported topology can be reloaded on any other machine without requiring to change the locale.

XML exports contain all details about the platform. It means that two very similar nodes still have different XML exports (e.g. some serial numbers or MAC addresses are different). If a less precise exporting/importing is required, one may want to look at Synthetic topologies instead.

10.1 libxml2 and minimalistic XML backends

hwloc offers two backends for importing/exporting XML.

First, it can use the libxml2 library for importing/exporting XML files. It features full XML support, for instance when those files have to be manipulated by non-hwloc software (e.g. a XSLT parser). The libxml2 backend is enabled by default if libxml2 development headers are available (the relevant development package is usually libxml2-devel or libxml2-dev).

If libxml2 is not available at configure time, or if $-\mathtt{disable-libxml2}$ is passed, hwloc falls back to a custom backend. Contrary to the aforementioned full XML backend with libxml2, this minimalistic XML backend cannot be guaranteed to work with external programs. It should only be assumed to be compatible with the same hwloc release (even if using the libxml2 backend). Its advantage is however to always be available without requiring any external dependency.

If libxml2 is available but the core hwloc library should not directly depend on it, the libxml2 support may be built as a dynamicall-loaded plugin. One should pass <code>--enable-plugins</code> to enable plugin support (when supported) and build as plugins all component that support it. Or pass <code>--enable-plugins=xml_libxml</code> to only build this libxml2 support as a plugin.

10.2 XML import error management

Importing XML files can fail at least because of file access errors, invalid XML syntax or non-hwloc-valid XML contents.

Both backend cannot detect all these errors when the input XML file or buffer is selected (when hwloc_topology _ _set_xml() or hwloc_topology_set_xmlbuffer() is called). Some errors such non-hwloc-valid contents can only be detected later when loading the topology with hwloc_topology_load().

It is therefore strongly recommended to check the return value of both hwloc_topology_set_xml() (or hwloc_copology_set_xmlbuffer()) and hwloc_topology_load() to handle all these errors.

Synthetic topologies

hwloc may load fake or remote topologies so as to consult them without having the underlying hardware available. Aside from loading XML topologies, hwloc also enables the building of *synthetic* topologies that are described by a single string listing the arity of each levels.

For instance, Istopo may create a topology made of 2 NUMA nodes, containing a single package each, with one cache above two single-threaded cores:

```
$ lstopo -i "node:2 pack:1 cache:1 core:2 pu:1" -
Machine (2048MB)

NUMANode L#0 (P#0 1024MB) + Package L#0 + L2 L#0 (4096KB)
Core L#0 + PU L#0 (P#0)
Core L#1 + PU L#1 (P#1)

NUMANode L#1 (P#1 1024MB) + Package L#1 + L2 L#1 (4096KB)
Core L#2 + PU L#2 (P#2)
Core L#3 + PU L#3 (P#3)
```

Replacing - with file.xml in this command line will export this topology to XML as usual.

Note

Synthetic topologies offer a very basic way to export a topology and reimport it on another machine. It is a lot less precise than XML but may still be enough when only the hierarchy of resources matters.

11.1 Synthetic description string

Each item in the description string gives the type of the level and the number of such children under each object of the previous level. That is why the above topology contains 4 cores (2 cores times 2 nodes).

These type names must be written as machine, node, package, core, cache, pu, group. They do not need to be written case-sensitively, nor entirely (as long as there is no ambiguity, 2 characters such as ma select a Machine level). Type-specific attributes may also be given such as L2iCache (hwloc_obj_type_sscanf() is used for parsing the type names). Note that I/O and Misc objects are not available.

The root object does not appear in the string. A Machine object is used by default, and a System object replaces it if a Machine level is specified in the string.

Cache level depths are automatically chosen by hwloc (only a L2 first, then a L1 under it, then L3 above, then L4 etc.) unless they are specified. Memory and cache sizes are also automatically chosen if needed.

Each item may be followed parentheses containing a list of space-separated attributes. For instance:

46 Synthetic topologies

• L2iCache: 2 (size=32kB) specifies 2 children of 32kB level-2 instruction caches. The size may be specified in bytes (without any unit suffix) or as TB, GB, MB or kB.

- NUMANode: 3 (memory=16MB) specifies 3 NUMA nodes with 16MB each. The size may be specified in bytes (without any unit suffix) or as TB, GB, MB or kB.
- PU:2 (indexes=0, 2, 1, 3) specifies 2 PU children and the full list of OS indexes among the entire set of 4 PU objects.
- PU:2 (indexes=numa:core) specifies 2 PU children whose OS indexes are interleaved by NUMA node
 first and then by package.
- · Attributes in parentheses at the very beginning of the description apply to the root object.

11.2 Loading a synthetic topology

Aside from Istopo, the hwloc programming interface offers the same ability by passing the synthetic description string to hwloc topology set synthetic() before hwloc topology load().

Synthetic topologies are created by the synthetic component. This component may be enabled by force by setting the HWLOC_COMPONENTS environment variable to something such as synthetic="node:2 core:3 pu:4".

Loading a synthetic topology disables binding support since the topology usually does not match the underlying hardware. Binding may be reenabled as usual by setting HWLOC_THISSYSTEM=1 in the environment or by setting the HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM topology flag.

11.3 Exporting a topology as a synthetic string

The function hwloc_topology_export_synthetic() may export a topology as a synthetic string. It offers a convenient way to quickly describe the contents of a machine. The Istopo tool may also perform such an export by forcing the output format.

```
$ lstopo --of synthetic --no-io
Package:1 Cache:1 Cache:2 Cache:1 Cache:1 PU:2
```

The exported string may be passed back to hwloc for recreating another similar topology. The entire tree will be similar, but some attributes such as the processor model will be missing.

Such an export is only possible if the topology is totally symmetric, which means the $symmetric_subtree$ field of the root object is set. This usually implies that I/O objects are disabled since attaching I/O busses often cause the topology to become asymmetric. Passing -no-io to Istopo is therefore often useful to make synthetic export work (as well as not passing any I/O topology flag before exporting with hwloc topology export synthetic()).

Interoperability With Other Software

Although hwloc offers its own portable interface, it still may have to interoperate with specific or non-portable libraries that manipulate similar kinds of objects. hwloc therefore offers several specific "helpers" to assist converting between those specific interfaces and hwloc.

Some external libraries may be specific to a particular OS; others may not always be available. The hwloc core therefore generally does not explicitly depend on these types of libraries. However, when a custom application uses or otherwise depends on such a library, it may optionally include the corresponding hwloc helper to extend the hwloc interface with dedicated helpers.

Most of these helpers use structures that are specific to these external libraries and only meaningful on the local machine. If so, the helper requires the input topology to match the current machine. Some helpers also require I/O device discovery to be supported and enabled for the current topology.

- **Linux specific features** hwloc/linux.h offers Linux-specific helpers that utilize some non-portable features of the Linux system, such as binding threads through their thread ID ("tid") or parsing kernel CPU mask files.
- **Linux libnuma** hwloc/linux-libnuma.h provides conversion helpers between hwloc CPU sets and libnuma-specific types, such as bitmasks. It helps you use libnuma memory-binding functions with hwloc CPU sets.
- **Glibc** hwloc/glibc-sched.h offers conversion routines between Glibc and hwloc CPU sets in order to use hwloc with functions such as sched_getaffinity() or pthread_attr_setaffinity_np().
- **OpenFabrics Verbs** hwloc/openfabrics-verbs.h helps interoperability with the OpenFabrics Verbs interface. For example, it can return a list of processors near an OpenFabrics device. It may also return the corresponding OS device hwloc object for further information (if I/O device discovery is enabled).
- **Myrinet Express** hwloc/myriexpress.h offers interoperability with the Myrinet Express interface. It can return the list of processors near a Myrinet board managed by the MX driver. Note that if I/O device discovery is enabled, such boards may also appear as PCI objects in the topology.
- Intel Xeon Phi (MIC) hwloc/intel-mic.h helps interoperability with Intel Xeon Phi (MIC) coprocessors by returning the list of processors near these devices. It may also return the corresponding OS device hwloc object for further information (if I/O device discovery is enabled).
- AMD OpenCL hwloc/opencl.h enables interoperability with the OpenCL interface. Only the AMD implementation currently offers locality information. It may return the list of processors near an AMD/ATI GPU given as a cl_device_id. It may also return the corresponding OS device hwloc object for further information (if I/O device discovery is enabled).
- **NVIDIA CUDA hwloc/cuda.h** and **hwloc/cudart.h** enable interoperability with NVIDIA CUDA Driver and Runtime interfaces. For instance, it may return the list of processors near NVIDIA GPUs. It may also return the corresponding OS device hwloc object for further information (if I/O device discovery is enabled).

- **NVIDIA Management Library (NVML) hwloc/nvml.h** enables interoperability with the NVIDIA NVML interface. It may return the list of processors near a NVIDIA GPU given as a nvmlDevice_t. It may also return the corresponding OS device hwloc object for further information (if I/O device discovery is enabled).
- **NVIDIA displays hwloc/gl.h** enables interoperability with NVIDIA displays using the NV-CONTROL X extension (NVCtrl library). If I/O device discovery is enabled, it may return the OS device hwloc object that corresponds to a display given as a name such as :0.0 or given as a port/device pair (server/screen).
- **Taskset command-line tool** The taskset command-line tool is widely used for binding processes. It manipulates CPU set strings in a format that is slightly different from hwloc's one (it does not divide the string in fixed-size subsets and separates them with commas). To ease interoperability, hwloc offers routines to convert hwloc CPU sets from/to taskset-specific string format. Most hwloc command-line tools also support the --taskset option to manipulate taskset-specific strings.

Thread Safety

Like most libraries that mainly fill data structures, hwloc is not thread safe but rather reentrant: all state is held in a hwloc_topology_t instance without mutex protection. That means, for example, that two threads can safely operate on and modify two different hwloc_topology_t instances, but they should not simultaneously invoke functions that modify the *same* instance. Similarly, one thread should not modify a hwloc_topology_t instance while another thread is reading or traversing it. However, two threads can safely read or traverse the same hwloc_topology_t instance concurrently.

When running in multiprocessor environments, be aware that proper thread synchronization and/or memory coherency protection is needed to pass hwloc data (such as hwloc_topology_t pointers) from one processor to another (e.g., a mutex, semaphore, or a memory barrier). Note that this is not a hwloc-specific requirement, but it is worth mentioning.

For reference, hwloc topology t modification operations include (but may not be limited to):

Creation and destruction hwloc_topology_init(), hwloc_topology_load(), hwloc_← topology_destroy() (see Topology Creation and Destruction) imply major modifications of the structure, including freeing some objects. No other thread cannot access the topology or any of its objects at the same time.

Also references to objects inside the topology are not valid anymore after these functions return.

Runtime topology modifications hwloc_topology_insert_misc_object_by_* (see Modifying a loaded Topology) may modify the topology significantly by adding objects inside the tree, changing the topology depth, etc. hwloc_topology_restrict() modifies the topology even more dramatically by removing some objects.

Although references to former objects *may* still be valid after insertion or restriction, it is strongly advised to not rely on any such guarantee and always re-consult the topology to reacquire new instances of objects.

Locating topologies hwloc_topology_ignore*, hwloc_topology_set* (see Topology Detection Configuration and Query) do not modify the topology directly, but they do modify internal structures describing the behavior of the upcoming invocation of hwloc_topology_load(). Hence, all of these functions should not be used concurrently.

50 Thread Safety

Components and plugins

hwloc is organized in components that are responsible for discovering objects. Depending on the topology configuration, some components will be used, some will be ignored. The usual default is to enable the native operating system component, (e.g. linux or solaris) and the pci miscellaneous component. If available, an architecture-specific component (such as x86) may also improve the topology detection.

If a XML topology is loaded, the xml discovery component will be used instead of all other components. It internally uses a specific class of components for the actual XML import/export routines (xml_libxml and xml_\circ nolibxml) but these will not be discussed here (see libxml2 and minimalistic XML backends).

14.1 Components enabled by default

The hwloc core contains a list of components sorted by priority. Each one is enabled as long as it does not conflict with the previously enabled ones. This includes native operating system components, architecture-specific ones, and if available, I/O components such as pci.

Usually the native operating system component (when it exists, e.g. linux or aix) is enabled first. Then hwloc looks for an architecture specific component (e.g. x86). Finally there also exist a basic component (no_os) that just tries to discover the number of PUs in the system.

Each component discovers as much topology information as possible. Most of them, including most native OS components, do nothing unless the topology is still empty. Some others, such as x86 and pci, can complete and annotate what other backends found earlier.

Default priorities ensure that clever components are invoked first. Native operating system components have higher priorities, and are therefore invoked first, because they likely offer very detailed topology information. If needed, it will be later extended by architecture-specific information (e.g. from the x86 component).

If any configuration function such as hwloc_topology_set_xml() is used before loading the topology, the corresponding component is enabled first. Then, as usual, hwloc enables any other component (based on priorities) that does not conflict.

Certain components that manage a virtual topology, for instance XML topology import, synthetic topology description, or custom building, conflict with all other components. Therefore, one of them may only be loaded (e.g. with $hwloc_topology_set_xml()$) if no other component is enabled.

The environment variable HWLOC_COMPONENTS_VERBOSE may be set to get verbose messages about component registration (including their priority) and enabling.

14.2 Selecting which components to use

Once topology configuration functions such as hwloc_topology_set_custom() have been taken care of, the priority order of the remaining components may be changed through the HWLOC_COMPONENTS environment variable (component names must be separated by commas).

Specifying x86 in this variable will cause the x86 component to take precedence over any other component, including the native operating system component. It is therefore loaded first, before hwloc tries to load all remaining non-conflicting components. In this case, x86 would take care of discovering everything it supports, instead of only completing what the native OS information. This may be useful if the native component is buggy on some platforms.

It is possible to prevent some components from being loaded by prefixing their name with – in the list. For instance x86, -pci will load the x86 component, then let hwloc load all the usual components except pci.

It is possible to prevent all remaining components from being loaded by placing stop in the environment variable. Only the components listed before this keyword will be enabled.

Certain component names (xml and synthetic) accept an argument (e.g. xml=file.xml). These arguments behave exactly as if the corresponding string had been passed to hwloc_topology_set_xml() or hwloc_topology_set_synthetic().

14.3 Loading components from plugins

Components may optionally be built as plugins so that the hwloc core library does not directly depend on their dependencies (for instance the libpciaccess library). Plugin support may be enabled with the --enable-plugins configure option. All components buildable as plugins will then be built as plugins. The configure option may be given a comma-separated list of component names to specify the exact list of components to build as plugins.

Plugins are built as independent dynamic libraries that are installed in \$libdir/hwloc. All plugins found in this directory are loaded during topology_init() (unless blacklisted in HWLOC_PLUGINS_BLACKLIST, see Environment Variables). A specific list of directories (colon-separated) to scan may be specified in the HWLOC_P LUGINS PATH environment variable.

Note that loading a plugin just means that the corresponding component is registered to the hwloc core. Components are then only enabled if the topology configuration requests it, as explained in the previous sections.

Also note that plugins should carefully be enabled and used when embedding hwloc in another project, see Embedding hwloc in Other Software for details.

14.4 Adding new discovery components and plugins

The types and functions cited below are declared in the hwloc/plugins.h header. Components are supposed to only use hwloc public headers (hwloc.h and anything under the include/hwloc subdirectory) and nothing from the include/private subdirectory in the source tree.

14.4.1 Basics of discovery components

Each discovery component is defined by a hwloc_disc_component structure which contains an instantiate() callback. This function is invoked when this component is actually used by a topology. It fills a new hwloc_backend structure that usually contains discover() and/or notify_new_object() callbacks taking care of the actual topology discovery.

Note

If two discovery components have the same name, only the highest priority one is actually made available. This offers a way for third-party plugins to override existing components.

14.4.2 Registering a new discovery component

Registering components to the hwloc core relies on a hwloc_component structure. Its data field points to the previously defined hwloc_disc_component structure while its type should be HWLOC_COMPONENT_T \leftarrow YPE_DISC. This structure should be named hwloc_<name>_component.

The configure script should be modified to add <name> to its hwloc_components shell variable so that the component is actually available.

Note

The symbol name of the hwloc_component structure is independent of the name of the discovery component mentioned in the previous section.

When the component is statically built inside the hwloc library, the symbol hwloc_<name>_component is added by configure to the src/static-components.h. The core then registers all components listed in this file.

If the new component may be built as a plugin, the configure script should also define the shell variable hwloc-_<name>_component_maybeplugin=1. When the configure script actually enables the component as a plugin, it will set the variable hwloc_<name>_component to plugin. The build system may then use this variable to change the way the component is built. It should create a hwloc_<name>.so shared object. All these files are loaded in alphabetic order, and the components they contain are registered to the hwloc core.

14.5 Existing components and plugins

All components distributed within hwloc are listed below. The list of actually available components may be listed at running with the HWLOC_COMPONENTS_VERBOSE environment variable (see Environment Variables).

- aix, darwin, freebsd, hpux, linux, netbsd, osf, solaris, windows Each officially supported operating system has its own native component, which is statically built when supported, and which is used by default.
- **x86** The x86 architecture (either 32 or 64 bits) has its own component that may complete or replace the previously-found CPU information. It is statically built when supported.
- bgq This component is specific to IBM BlueGene/Q compute node (running CNK). It is built and enabled by default when --host=powerpc64-bgq-linux is passed to configure (see How do I build hwloc for Blue← Gene/Q?).
- **no_os** A basic component that just tries to detect the number of processing units in the system. It mostly serves on operating systems that are not natively supported. It is always statically built.

- pci PCI object discovery uses the external pciaccess library (aka libpciaccess); see I/O Devices. It may be built as a plugin.
- **linuxpci** This component can probe PCI devices on Linux without the help of external libraries such as libpciaccess. Its priority is lower than the pci component because it misses device names.
- **opencl** The OpenCL component creates co-processor OS device objects such as *opencl0d0* (first device of the first OpenCL platform) or *opencl1d3* (fourth device of the second platform). Only the AMD OpenCL implementation currently offers locality information. **It may be built as a plugin**.
- **cuda** This component creates co-processor OS device objects such as *cuda0* that correspond to NVIDIA GPUs used with CUDA library. **It may be built as a plugin**.
- **nvml** Probing the NVIDIA Management Library creates OS device objects such as *nvml0* that are useful for batch schedulers. It also detects the actual PCIe link bandwidth without depending on power management state and without requiring administrator privileges. **It may be built as a plugin**.
- **gl** Probing the NV-CONTROL X extension (NVCtrl library) creates OS device objects such as :0.0 corresponding to NVIDIA displays. They are useful for graphical applications that need to place computation and/or data near a rendering GPU. **It may be built as a plugin**.
- synthetic Synthetic topology support (see Synthetic topologies) is always built statically.
- custom Custom topology support (see Multi-node Topologies) is always built statically.
- **xml** XML topology import (see Importing and exporting topologies from/to XML files) is always built statically. It internally uses one of the XML backends (see libxml2 and minimalistic XML backends).
 - xml nolibxml is a basic and hwloc-specific XML import/export. It is always statically built.
 - xml_libxml relies on the external libxml2 library for provinding a feature-complete XML import/export.
 It may be built as a plugin.

fake A dummy plugin that does nothing but is used for debugging plugin support.

Embedding hwloc in Other Software

It can be desirable to include hwloc in a larger software package (be sure to check out the LICENSE file) so that users don't have to separately download and install it before installing your software. This can be advantageous to ensure that your software uses a known-tested/good version of hwloc, or for use on systems that do not have hwloc pre-installed.

When used in "embedded" mode, hwloc will:

- · not install any header files
- · not build any documentation files
- · not build or install any executables or tests
- not build libhwloc.* instead, it will build libhwloc_embedded.*

There are two ways to put hwloc into "embedded" mode. The first is directly from the configure command line:

```
shell$ ./configure --enable-embedded-mode ...
```

The second requires that your software project uses the GNU Autoconf / Automake / Libtool tool chain to build your software. If you do this, you can directly integrate hwloc's m4 configure macro into your configure script. You can then invoke hwloc's configuration tests and build setup by calling an m4 macro (see below).

Although hwloc dynamic shared object plugins may be used in embedded mode, the embedder project will have to manually setup libltdl in its build system so that hwloc can load its plugins at run time. Also, embedders should be aware of complications that can arise due to public and private linker namespaces (e.g., if the embedder project is loaded into a private namespace and then hwloc tries to dynamically load its plugins, such loading may fail since the hwloc plugins can't find the hwloc symbols they need). The embedder project is **strongly** advised not to use hwloc's dynamically loading plugins / libltdl capability.

15.1 Using hwloc's M4 Embedding Capabilities

Every project is different, and there are many different ways of integrating hwloc into yours. What follows is *one* example of how to do it.

If your project uses recent versions Autoconf, Automake, and Libtool to build, you can use hwloc's embedded m4 capabilities. We have tested the embedded m4 with projects that use Autoconf 2.65, Automake 1.11.1, and Libtool 2.2.6b. Slightly earlier versions of may also work but are untested. Autoconf versions prior to 2.65 are almost certain to not work.

You can either copy all the config/hwloc*m4 files from the hwloc source tree to the directory where your project's m4 files reside, or you can tell aclocal to find more m4 files in the embedded hwloc's "config" subdirectory (e.g., add "-lpath/to/embedded/hwloc/config" to your Makefile.am's ACLOCAL_AMFLAGS).

The following macros can then be used from your configure script (only HWLOC_SETUP_CORE *must* be invoked if using the m4 macros):

• HWLOC_SETUP_CORE(config-dir-prefix, action-upon-success, action-upon-failure, print_banner_or_not) ← : Invoke the hwloc configuration tests and setup the hwloc tree to build. The first argument is the prefix to use for AC_OUTPUT files − it's where the hwloc tree is located relative to \$top_srcdir. Hence, if your embedded hwloc is located in the source tree at contrib/hwloc, you should pass [contrib/hwloc] as the first argument. If HWLOC_SETUP_CORE and the rest of configure completes successfully, then "make" traversals of the hwloc tree with standard Automake targets (all, clean, install, etc.) should behave as expected. For example, it is safe to list the hwloc directory in the SUBDIRS of a higher-level Makefile.am. The last argument, if not empty, will cause the macro to display an announcement banner that it is starting the hwloc core configuration tests.

HWLOC_SETUP_CORE will set the following environment variables and AC_SUBST them: HWLOC_EM⇔ BEDDED_CFLAGS, HWLOC_EMBEDDED_CPPFLAGS, and HWLOC_EMBEDDED_LIBS. These flags are filled with the values discovered in the hwloc-specific m4 tests, and can be used in your build process as relevant. The _CFLAGS, _CPPFLAGS, and _LIBS variables are necessary to build libhwloc (or libhwloc_⇔ embedded) itself.

HWLOC_SETUP_CORE also sets HWLOC_EMBEDDED_LDADD environment variable (and AC_SUBSTs it) to contain the location of the libhwloc_embedded.la convenience Libtool archive. It can be used in your build process to link an application or other library against the embedded hwloc library.

NOTE: If the HWLOC_SET_SYMBOL_PREFIX macro is used, it must be invoked *before* HWLOC_SE ← TUP_CORE.

- HWLOC_BUILD_STANDALONE: HWLOC_SETUP_CORE defaults to building hwloc in an "embedded" mode (described above). If HWLOC_BUILD_STANDALONE is invoked *before* HWLOC_SETUP_CO← RE, the embedded definitions will not apply (e.g., libhwloc.la will be built, not libhwloc_embedded.la).
- HWLOC_SET_SYMBOL_PREFIX(foo_): Tells the hwloc to prefix all of hwloc's types and public symbols with "foo_"; meaning that function hwloc_init() becomes foo_hwloc_init(). Enum values are prefixed with an uppercase translation if the prefix supplied; HWLOC_OBJ_SYSTEM becomes FOO_HWLOC_OBJ_SYSTEM. This is recommended behavior if you are including hwloc in middleware it is possible that your software will be combined with other software that links to another copy of hwloc. If both uses of hwloc utilize different symbol prefixes, there will be no type/symbol clashes, and everything will compile, link, and run successfully. If you both embed hwloc without changing the symbol prefix and also link against an external hwloc, you may get multiple symbol definitions when linking your final library or application.
- HWLOC_SETUP_DOCS, HWLOC_SETUP_UTILS, HWLOC_SETUP_TESTS: These three macros only apply when hwloc is built in "standalone" mode (i.e., they should NOT be invoked unless HWLOC_BUILD_S

 TANDALONE has already been invoked).

HWLOC_DO_AM_CONDITIONALS: If you embed hwloc in a larger project and build it conditionally with Automake (e.g., if HWLOC_SETUP_CORE is invoked conditionally), you must unconditionally invoke HWL← OC_DO_AM_CONDITIONALS to avoid warnings from Automake (for the cases where hwloc is not selected to be built). This macro is necessary because hwloc uses some AM_CONDITIONALs to build itself, and AM_CONDITIONALs cannot be defined conditionally. Note that it is safe (but unnecessary) to call HWLO← C_DO_AM_CONDITIONALS even if HWLOC_SETUP_CORE is invoked unconditionally. If you are not using Automake to build hwloc, this macro is unnecessary (and will actually cause errors because it invoked AM_* macros that will be undefined).

NOTE: When using the HWLOC_SETUP_CORE m4 macro, it may be necessary to explicitly invoke AC_CANO NICAL_TARGET (which requires config.sub and config.guess) and/or AC_USE_SYSTEM_EXTENSIONS macros early in the configure script (e.g., after AC_INIT but before AM_INIT_AUTOMAKE). See the Autoconf documentation for further information.

Also note that hwloc's top-level configure.ac script uses exactly the macros described above to build hwloc in a standalone mode (by default). You may want to examine it for one example of how these macros are used.

15.2 Example Embedding hwloc

Here's an example of integrating with a larger project named sandbox that already uses Autoconf, Automake, and Libtool to build itself:

```
# First, cd into the sandbox project source tree
shell$ cd sandbox
shell$ cp -r /somewhere/else/hwloc-<version> my-embedded-hwloc
shell$ edit Makefile.am
 1. Add "-Imy-embedded-hwloc/config" to ACLOCAL_AMFLAGS
 2. Add "my-embedded-hwloc" to SUBDIRS
 3. Add "$(HWLOC_EMBEDDED_LDADD)" and "$(HWLOC_EMBEDDED_LIBS)" to
    sandbox's executable's LDADD line. The former is the name of the
    Libtool convenience library that hwloc will generate. The latter
     is any dependent support libraries that may be needed by
    $ (HWLOC_EMBEDDED_LDADD) .
 4. Add "$(HWLOC_EMBEDDED_CFLAGS)" to AM_CFLAGS
 5. Add "$(HWLOC_EMBEDDED_CPPFLAGS)" to AM_CPPFLAGS
shell$ edit configure.ac
 1. Add "HWLOC_SET_SYMBOL_PREFIX(sandbox_hwloc_)" line
 2. Add "HWLOC_SETUP_CORE([my-embedded-hwloc], [happy=yes], [happy=no])" line
 3. Add error checking for happy=no case
shell$ edit sandbox.c
 1. Add #include <hwloc.h>
 2. Add calls to sandbox_hwloc_init() and other hwloc API functions
```

Now you can bootstrap, configure, build, and run the sandbox as normal – all calls to "sandbox_hwloc_*" will use the embedded hwloc rather than any system-provided copy of hwloc.

Frequently Asked Questions

16.1 Concepts

16.1.1 I only need binding, why should I use hwloc?

hwloc is its portable API that works on a variety of operating systems. It supports binding of threads, processes and memory buffers (see CPU binding and Memory binding). Even if some features are not supported on some systems, using hwloc is much easier than reimplementing your own portability layer.

Moreover, hwloc provides knowledge of cores and hardware threads. It offers easy ways to bind tasks to individual hardware threads, or to entire multithreaded cores, etc. See How may I ignore symmetric multithreading, hyperthreading, etc. in hwloc?. Most alternative software for binding do not even know whether each core is single-threaded, multithreaded or hyper-threaded. They would bind to individual threads without any way to know whether multiple tasks are in the same physical core.

However, using hwloc comes with an overhead since a topology must be loaded before gathering information and binding tasks or memory. This overhead may be reduced by filtering useless information out of the topology. For instance the following code builds a topology that may only contain Cores, hardware threads (PUs), and NUMA nodes (Packages, Caches and Group objects are ignored).

```
hwloc_topology_t topology;
hwloc_topology_init(&topology);
hwloc_topology_ignore_type(topology, HWLOC_OBJ_PACKAGE);
hwloc_topology_ignore_type(topology, HWLOC_OBJ_CACHE);
hwloc_topology_ignore_type(topology, HWLOC_OBJ_GROUP);
hwloc_topology_load(topology);
```

16.1.2 Should I use logical or physical/OS indexes? and how?

One of the original reasons why hwloc was created is that **physical/OS indexes** (obj->os_index) are often crazy and unpredictable: logical processors numbers are usually non-contiguous (processors 0 and 1 are not physically close), they vary from one machine to another, and may even change after a BIOS or system update. This numbers make task placement hardly portable. Moreover some objects have no physical/OS numbers (caches), and some objects have non-unique numbers (core numbers are only unique within a socket). Physical/OS indexes are only guaranteed to exist and be unique for PU and NUMA nodes.

hwloc therefore introduces **logical indexes** (obj->logical_index) which are portable, contiguous and logically ordered (based on the resource organization in the locality tree). In general, one should only use logical indexes and just let hwloc do the internal conversion when really needed (when talking to the OS and hardware).

hwloc developers recommends that users do not use physical/OS indexes unless they really know what they are doing. The main reason for still using physical/OS indexes is when interacting with non-hwloc tools such as numactl or taskset, or when reading hardware information from raw sources such as /proc/cpuinfo.

Istopo options -1 and -p may be used to switch between logical indexes (prefixed with L#) and physical/OS indexes (P#). Converting one into the other may also be achieved with hwloc-calc which may manipulate either logical or physical indexes as input or output. See also hwloc-calc.

```
# Convert PU with physical number 3 into logical number
$ hwloc-calc -I pu --physical-input --logical-output pu:3
5

# Convert a set of NUMA nodes from logical to physical
# (beware that the output order may not match the input order)
$ hwloc-calc -I numa --logical-input --physical-output numa:2-3 numa:7
0.2.5
```

16.1.3 hwloc is only a structural model, it ignores performance models, memory bandwidth, etc.?

hwloc is indeed designed to provide applications with a structural model of the platform. This is an orthogonal approach to describing the machine with performance models, for instance using memory bandwidth or latencies measured by benchmarks. We believe that both approaches are important for helping application make the most of the hardware.

For instance, on a dual-processor host with four cores each, hwloc clearly shows which four cores are together. Latencies between all pairs of cores of the same processor are likely identical, and also likely lower than the latency between cores of different processors. However the structural model cannot guarantee such implementation details. On the other side, performance models would reveal such details without always clearly identifying which cores are in the same processor.

The focus of hwloc is mainly of the structural modeling side. However, hwloc lets user adds performance information to the topology through distances (see hwloc_distances_s and hwloc_topology_set_distance_matrix()) or even custom annotations (see How do I annotate the topology with private notes?). hwloc may also use such distance information for grouping objects together (see hwloc only has a one-dimensional view of the architecture, it ignores distances? and What are these Group objects in my topology?).

16.1.4 hwloc only has a one-dimensional view of the architecture, it ignores distances?

hwloc places all objects in a tree. Each level is a one-dimensional view of a set of similar objects. All children of the same object (siblings) are assumed to be equally interconnected (same distance between any of them), while the distance between children of different objects (cousins) is supposed to be larger.

Modern machines exhibit complex hardware interconnects, so this tree may miss some information about the actual physical distances between objects. The hwloc topology may therefore be annotated with distance information that may be used to build a more realistic representation (multi-dimensional) of each level. For instance, the root object may contain a distance matrix that represents the latencies between any pairs of NUMA nodes if the BIOS and/or operating system reports them.

For more information about the distance API, see hwloc_distances_s and hwloc_topology_set_distance_matrix().

16.1 Concepts 61

16.1.5 What are these Group objects in my topology?

hwloc comes with a set of predefined object types (Core, Package, NUMA node, Caches) that match the vast majority of hardware platforms. The HWLOC_OBJ_GROUP type was designed for cases where this set is not sufficient. Groups may be used anywhere to add more structure information to the topology, for instance to show that 2 out of 4 NUMA nodes are actually closer than the others. When applicable, the Type info attribute describes why a Group was actually added (see also Custom string infos).

hwloc currently uses Groups for the following reasons:

- AMD dual-core compute units (Type=ComputeUnit, in the x86 backend), but these objects are usually merged with the L2 caches.
- Intel x2APIC non-core and non-package levels (in the x86 backend).
- · Windows processor groups.
- IBM S/390 "Books" on Linux (Type=Book).
- · AIX unknown hierarchy levels.
- · Distance-based groups made of close objects.
- I/O parents when I/O locality does not match any existing object.

16.1.6 What happens if my topology is asymmetric?

hwloc supports asymmetric topologies even if most platforms are usually symmetric. For example, there could be different types of processors in a single machine, each with different numbers of cores, symmetric multithreading, or levels of caches.

In practice, asymmetric topologies mostly appear when intermediate groups are added for I/O affinity: on a 4-package machine, an I/O bus may be connected to 2 packages. These packages are below an additional Group object, while the other packages are not (see also What are these Group objects in my topology?).

Before hwloc v2.0, hwloc_topology_ignore_type_keep_structure() and hwloc_topology_ignore_all_keep_structure() may also make topologies assymetric by removing parts of levels, especially when part of the machine is disallowed by administrator restrictions (e.g. Linux cgroups).

To understand how hwloc manages such cases, one should first remember the meaning of levels and cousin objects. All objects of the same type are gathered as horizontal levels with a given depth. They are also connected through the cousin pointers of the hwloc_obj structure. Some types, such as Caches or Groups, are annotated with a depth or level attribute (for instance L2 cache or Group1). Moreover caches have a type attribute (for instance L1i or L1d). Such attributes are also taken in account when gathering objects as horizontal levels. To be clear: there will be one level for L1i caches, another level for L1d caches, another one for L2, etc.

If the topology is asymmetric (e.g., if a group is missing above some processors), a given horizontal level will still exist if there exist any objects of that type. However, some branches of the overall tree may not have an object located in that horizontal level. Note that this specific hole within one horizontal level does not imply anything for other levels. All objects of the same type are gathered in horizontal levels even if their parents or children have different depths and types.

See the diagram in Terms and Definitions for a graphical representation of such topologies.

Moreover, it is important to understand that a same parent object may have children of different types (and therefore, different depths). These children are therefore siblings (because they have the same parent), but they are not cousins (because they do not belong to the same horizontal level).

16.1.7 What happens to my topology if I disable symmetric multithreading, hyper-threading, etc. in the system?

hwloc creates one PU (processing unit) object per hardware thread. If your machine supports symmetric multithreading, for instance Hyper-Threading, each Core object may contain multiple PU objects:

```
$ lstopo -
...
Core L#0
PU L#0 (P#0)
PU L#1 (P#2)
Core L#1
PU L#2 (P#1)
PU L#3 (P#3)
```

x86 machines usually offer the ability to disable hyper-threading in the BIOS. Or it can be disabled on the Linux kernel command-line at boot time, or later by writing in sysfs virtual files.

If you do so, the hwloc topology structure does not significantly change, but some PU objects will not appear anymore. No level will disappear, you will see the same number of Core objects, but each of them will contain a single PU now. The PU level does not disappear either (remember that hwloc topologies always contain a PU level at the bottom of the topology) even if there is a single PU object per Core parent.

```
$ lstopo -
...
Core L#0
PU L#0 (P#0)
Core L#1
PU L#1 (P#1)
```

16.1.8 How may I ignore symmetric multithreading, hyper-threading, etc. in hwloc?

First, see What happens to my topology if I disable symmetric multithreading, hyper-threading, etc. in the system? for more information about multithreading.

If you need to ignore symmetric multithreading in software, you should likely manipulate hwloc Core objects directly:

Whenever you want to bind a process or thread to a core, make sure you singlify its cpuset first, so that the task is actually bound to a single thread within this core (to avoid useless migrations).

```
/* bind on the second core */
hwloc_obj_t core = hwloc_get_obj_by_type(topology, HWLOC_OBJ_CORE, 1);
hwloc_cpuset_t set = hwloc_bitmap_dup(core->cpuset);
hwloc_bitmap_singlify(set);
hwloc_set_cpubind(topology, set, 0);
hwloc_bitmap_free(set);
```

With hwloc-calc or hwloc-bind command-line tools, you may specify that you only want a single-thread within each core by asking for their first PU object:

16.2 Advanced 63

```
$ hwloc-calc core:4-7
0x0000ff00
$ hwloc-calc core:4-7.pu:0
0x00005500
```

When binding a process on the command-line, you may either specify the exact thread that you want to use, or ask hwloc-bind to singlify the cpuset before binding

```
$ hwloc-bind core:3.pu:0 -- echo "hello from first thread on core #3"
hello from first thread on core #3
...
$ hwloc-bind core:3 --single -- echo "hello from a single thread on core #3"
hello from a single thread on core #3
```

16.2 Advanced

16.2.1 I do not want hwloc to rediscover my enormous machine topology every time I rerun a process

Although the topology discovery is not expensive on common machines, its overhead may become significant when multiple processes repeat the discovery on large machines (for instance when starting one process per core in a parallel application). The machine topology usually does not vary much, except if some cores are stopped/restarted or if the administrator restrictions are modified. Thus rediscovering the whole topology again and again may look useless.

For this purpose, hwloc offers XML import/export features. It lets you save the discovered topology to a file (for instance with the Istopo program) and reload it later by setting the HWLOC_XMLFILE environment variable. The HWLOC_THISSYSTEM environment variable should also be set to 1 to assert that loaded file is really the underlying system.

Loading a XML topology is usually much faster than querying multiple files or calling multiple functions of the operating system. It is also possible to manipulate such XML files with the C programming interface, and the import/export may also be directed to memory buffer (that may for instance be transmitted between applications through a package). See also Importing and exporting topologies from/to XML files.

Note

The environment variable HWLOC_THISSYSTEM_ALLOWED_RESOURCES may be used to load a XML topology that contains the entire machine and restrict it to the part that is actually available to the current process (e.g. when Linux Cgroup/Cpuset are used to restrict the set of resources). See Environment Variables.

16.2.2 How many topologies may I use in my program?

hwloc lets you manipulate multiple topologies at the same time. However these topologies consume memory and system resources (for instance file descriptors) until they are destroyed. It is therefore discouraged to open the same topology multiple times.

Sharing a single topology between threads is easy (see Thread Safety) since the vast majority of accesses are read-only.

If multiple topologies of different (but similar) nodes are needed in your program, have a look at How to avoid memory waste when manipulating multiple similar topologies?.

16.2.3 How to avoid memory waste when manipulating multiple similar topologies?

hwloc does not share information between topologies. If multiple similar topologies are loaded in memory, for instance the topologies of different identical nodes of a cluster, lots of information will be duplicated.

hwloc/diff.h (see also Topology differences) offers the ability to compute topology differences, apply or unapply them, or export/import to/from XML. However this feature is limited to basic differences such as attribute changes. It does not support complex modifications such as adding or removing some objects.

16.2.4 How do I annotate the topology with private notes?

Each hwloc object contains a userdata field that may be used by applications to store private pointers. This field is only valid during the lifetime of these container object and topology. It becomes invalid as soon the topology is destroyed, or as soon as the object disappears, for instance when restricting the topology. The userdata field is not exported/imported to/from XML by default since hwloc does not know what it contains. This behavior may be changed by specifying application-specific callbacks with hwloc_topology_set_userdata_export callback() and hwloc_topology_set_userdata_import_callback().

Each object may also contain some *info* attributes (key name and value) that are setup by hwloc during discovery and that may be extended by the user with hwloc_obj_add_info() (see also Object attributes). Contrary to the userdata field which is unique, multiple info attributes may exist for each object, even with the same name. These attributes are always exported to XML. However only character strings may be used as key names and values.

It is also possible to insert Misc objects with a custom name anywhere as a leaf of the topology (see Miscellaneous objects). And Misc objects may have their own userdata and info attributes just like any other object.

The hwloc-annotate command-line tool may be used for adding Misc objects and info attributes.

There is also a topology-specific userdata pointer that can be used to recognize different topologies by storing a custom pointer. It may be manipulated with $hwloc_topology_set_userdata()$ and $hwloc_topology_det_userdata()$.

16.3 Caveats

16.3.1 Why is hwloc slow?

Building a hwloc topology on a large machine may be slow because the discovery of hundreds of hardware cores or threads takes time (especially when reading thousands of sysfs files on Linux). One should consider using XML (see I do not want hwloc to rediscover my enormous machine topology every time I rerun a process) to work around such issues.

Additionally, Istopo enables most hwloc objects and discovery flags by default so that the output topology is as precise as possible (while hwloc disables many of them by default). This includes I/O device discovery through PCI libraries as well as external libraries such as NVML. To speed up Istopo, you may disable such features with command-line options such as -no-io.

When NVIDIA GPU probing is enabled with CUDA or NVML, one should make sure that the *Persistent* mode is enabled (with nvidia-smi -pm 1) to avoid significant GPU initialization overhead.

When AMD GPU discovery is enabled with OpenCL and hwloc is used remotely over ssh, some spurious round-trips on the network may significantly increase the discovery time. Forcing the DISPLAY environment variable to the remote X server display (usually:0) instead of only setting the COMPUTE variable may avoid this.

Also remember that these components may be disabled at build-time with configure flags such as --disable-opencl, --disable-cuda or --disable-nvml, and at runtime with the environment variable HWLOC_COMPONENTS=-opencl, cuda, nvml.

16.3 Caveats 65

16.3.2 Does hwloc require privileged access?

hwloc discovers the topology by querying the operating system. Some minor features may require privileged access to the operation system. For instance memory module and PCI link speed discovery on Linux is reserved to root, and the entire PCI discovery on Solaris and BSDs requires access to some special files that are usually restricted to root (/dev/pci* or /devices/pci*).

To workaround this limitation, it is recommended to export the topology as a XML file generated by the administrator (with the Istopo program) and make it available to all users (see Importing and exporting topologies from/to XML files). It will offer all discovery information to any application without requiring any privileged access anymore. Only the necessary hardware characteristics will be exported, no sensitive information will be disclosed through this XML export.

This XML-based model also has the advantage of speeding up the discovery because reading a XML topology is usually much faster than querying the operating system again.

The utility hwloc-dump-hwdata is also involved in gathering privileged information at boot time and making it available to non-privileged users (note that this may require a specific SELinux MLS policy module). However it only applies to Intel Knights Landing Xeon Phi for now (see Why do I need hwloc-dump-hwdata for memory on Intel Knights Landing Xeon Phi?). See also HWLOC_DUMPED_HWDATA_DIR in Environment Variables for details about the location of dumped files.

16.3.3 What should I do when hwloc reports "operating system" warnings?

When the operating system reports invalid locality information (because of either software or hardware bugs), hwloc may fail to insert some objects in the topology because they cannot fit in the already built tree of resources. If so, hwloc will report a warning like the following. The object causing this error is ignored, the discovery continues but the resulting topology will miss some objects and may be asymmetric (see also What happens if my topology is asymmetric?).

```
*********

* hwloc has encountered what looks like an error from the operating system.

* L3 (cpuset 0x000003f0) intersects with NUMANode (P#0 cpuset 0x0000003f) without inclusion!

* Error occurred in topology.c line 940

* Please report this error message to the hwloc user's mailing list,

* along with the output from the hwloc-gather-topology script.
```

These errors are common on large AMD platforms because of BIOS and/or Linux kernel bugs causing invalid L3 cache information. In the above example, the hardware reports a L3 cache that is shared by 2 cores in the first NUMA node and 4 cores in the second NUMA node. That's wrong, it should actually be shared by all 6 cores in a single NUMA node. The resulting topology will miss some L3 caches.

If your application does not care about cache sharing, or if you do not plan to request cache-aware binding in your process launcher, you may likely ignore this error (and hide it by setting HWLOC_HIDE_ERRORS=1 in your environment).

Some platforms report similar warnings about conflicting Packages and NUMANodes.

On x86 hosts, passing $\texttt{HWLOC_COMPONENTS} = x86$ in the environment may workaround some of these issues by switching to a different way to discover the topology.

Upgrading the BIOS and/or the operating system may help. Otherwise, as explained in the message, reporting this issue to the hwloc developers (by sending the tarball that is generated by the hwloc-gather-topology script on this platform) is a good way to make sure that this is a software (operating system) or hardware bug (BIOS, etc).

16.3.4 Why does Valgrind complain about hwloc memory leaks?

If you are debugging your application with Valgrind, you want to avoid memory leak reports that are caused by hwloc and not by your program.

hwloc itself is often checked with Valgrind to make sure it does not leak memory. However some global variables in hwloc dependencies are never freed. For instance libz allocates its global state once at startup and never frees it so that it may be reused later. Some libxml2 global state is also never freed because hwloc does not know whether it can safely ask libxml2 to free it (the application may also be using libxml2 outside of hwloc).

These unfreed variables cause leak reports in Valgrind. hwloc installs a Valgrind *suppressions* file to hide them. You should pass the following command-line option to Valgrind to use it:

```
--suppressions=/path/to/hwloc-valgrind.supp
```

16.3.5 How do I handle ABI breaks and API upgrades?

The hwloc interface is extended with every new major release. Any application using the hwloc API should be prepared to check at compile-time whether some features are available in the currently installed hwloc distribution.

For instance, to check whether the hwloc version is at least 1.10, you should use:

```
#include <hwloc.h>
#if HWLOC_API_VERSION >= 0x00010a00
...
#endif
```

To check for the API of release X.Y.Z at build time, you may compare HWLOC_API_VERSION with (X>>16)+(Y>>8)+Z.

For supporting older releases that do not have <code>HWLOC_OBJ_NUMANODE</code> and <code>HWLOC_OBJ_PACKAGE</code> yet, you may use:

```
#include <hwloc.h>
#if HWLOC_API_VERSION < 0x00010b00
#define HWLOC_OBJ_NUMANODE HWLOC_OBJ_NODE
#define HWLOC_OBJ_PACKAGE HWLOC_OBJ_SOCKET
#endif</pre>
```

The hwloc interface will be deeply modified in release 2.0 to fix several issues of the 1.x interface. The ABI will be broken, which means applications must be recompiled against the new 2.0 interface.

To check that you are not mixing old/recent headers with a recent/old runtime library, check the major revision number in the API version:

To specifically detect v2.0 issues:

16.4 Platform-specific 67

```
#include <hwloc.h>
#if HWLOC_API_VERSION >= 0x00020000
   /* headers are recent */
   if (hwloc_get_api_version() < 0x20000)
    ... error out, the hwloc runtime library is older than 2.0 ...
#else
   /* headers are pre-2.0 */
   if (hwloc_get_api_version() >= 0x20000)
    ... error out, the hwloc runtime library is more recent than 2.0 ...
#endif
```

You should not try to remain compatible with very old releases such as 1.1.x or earlier because HWLOC_API_ \leftarrow VERSION was added in 1.0.0 and hwloc_get_api_version() came only in 1.1.1. Also do not use the old cpuset API since it was deprecated and superseded by the bitmap API in 1.1, and later removed in 1.5.

16.4 Platform-specific

16.4.1 How do I find the local MCDRAM NUMA node on Intel Knights Landing Xeon Phi?

Intel Knights Landing Xeon Phi processors introduce a new memory architecture by possibly having two distinct local memories: some normal memory (DDR) and some high-bandwidth on-package memory (MCDRAM). Processors can be configured in various clustering modes to have up to 4 *Clusters*. Moreover, each *Cluster* (quarter, half or whole processor) of the processor may have its own local parts of the DDR and of the MCDRAM. This memory and clustering configuration may be probed by looking at MemoryMode and ClusterMode attributes, see Custom string infos and doc/examples/get-knl-modes.c in the source directory.

The upcoming hwloc 2.0 will address this new architecture by presenting memory in an improved way. For now, starting with 1.11.2, hwloc releases use the following approximate representation:

If a cluster only contains DDR or MCDRAM but not both, that memory is available as a local NUMA node above cores as usual.

If a cluster contains both, two distinct NUMA nodes appear. They are sibling children of a Group object of type Cluster (or sibling children of the Package object for non-clustered processors).

The DDR memory is the local NUMA node above cores as usual. Allocating memory from one core to its local NUMA node will therefore actually allocate it on the normal memory by default.

The local high-bandwidth MCDRAM is the second NUMA node (without any Core or PU below it). It is the next sibling of the local DDR NUMA node below the same parent object. To allocate on the faster MCDRAM, one should first find the local NUMA node (the DDR memory, by looking up parent objects), and then take the next sibling to reach the local MCDRAM NUMA node (if any).

The MCDRAM NUMA nodes may also be identified thanks to the Type info attribute which is set to MCDRAM.

Command-line tools such as hwloc-bind may bind memory on the MCDRAM by using the *hbm* keyword. For instance, to bind on the first MCDRAM NUMA node:

```
$ hwloc-bind --membind --hbm numa:0 -- myprogram
$ hwloc-bind --membind numa:0 -- myprogram
```

16.4.2 Why do I need hwloc-dump-hwdata for memory on Intel Knights Landing Xeon Phi?

Intel Knights Landing Xeon Phi processors may use the on-package memory (MCDRAM) as either memory or a memory-side cache (currently reported as a L3 cache by hwloc). There are also several clustering modes that significantly affect the memory organization (see How do I find the local MCDRAM NUMA node on Intel Knights Landing Xeon Phi? for more information about these modes). Details about these are currently only available to privileged users.

The hwloc-dump-hwdata utility may be used to dump this privileged binary information into human-readable and world-accessible files that the hwloc library will later load. The utility should usually run as root once during boot, in order to update dumped information (stored under /var/run/hwloc by default) in case the MCDRAM or clustering configuration changed between reboots.

When SELinux MLS policy is enabled, a specific hwloc policy module may be required so that all users get access to the dumped files (in /var/run/hwloc by default). One may use hwloc policy files from the SELinux Reference Policy at https://github.com/TresysTechnology/refpolicy-contrib (see also the documentation at https://github.com/TresysTechnology/refpolicy/wiki/GettingStarted).

hwloc-dump-hwdata requires dmi-sysfs kernel module loaded.

The utility is currently unneeded on non-KNL platforms.

See HWLOC DUMPED HWDATA DIR in Environment Variables for details about the location of dumped files.

16.4.3 How do I build for Intel Xeon Phi coprocessor?

Note

This section does not apply to standalone Intel Knights Landing Xeon Phi.

Intel Knights Corner Xeon Phi coprocessors usually runs a Linux environment but cross-compiling from the host is required. hwloc uses standard autotools options for cross-compiling. For instance, to build for a *Knights Corner* (*KNC*) coprocessor:

If building with icc:

```
./configure CC="icc -mmic" --host=x86_64-k1om-linux --build=x86_64-unknown-linux-gnu
```

If building with the Xeon Phi-specific GCC that comes with the MPSS environment, for instance /usr/linux-klom-4.7/bin/x8_64-klom-linux-gcc:

```
export PATH=$PATH:/usr/linux-k1om-4.7/bin/
./configure --host=x86_64-k1om-linux --build=x86_64-unknown-linux-gnu
```

16.4.4 How do I build hwloc for BlueGene/Q?

IBM BlueGene/Q machines run a standard Linux on the login/frontend nodes and a custom CNK (*Compute Node Kernel*) on the compute nodes.

To discover the topology of a login/frontend node, hwloc should be configured as usual, without any BlueGene/Q-specific option.

However, one would likely rather discover the topology of the compute nodes where parallel jobs are actually running. If so, hwloc must be cross-compiled with the following configuration line:

CPPFLAGS may have to be updated if your platform headers are installed in a different directory.

16.4 Platform-specific 69

16.4.5 How do I build hwloc for Windows?

hwloc releases are available as pre-built ZIPs for Windows on both 32bits and 64bits x86 platforms. They are built using MSYS2 and MinGW on a Windows host. Such an environment allows using the Unix-like configure, make and make install steps without having to tweak too many variables or options. One may look at contrib/ci.inria.fr/job-3-mingw.sh in the hwloc repository for an example used for nightly testing.

hwloc releases also contain a basic Microsoft Visual Studio solution under contrib/windows/.

16.4.6 How to get useful topology information on NetBSD?

The NetBSD (and FreeBSD) backend uses x86-specific topology discovery (through the x86 component). This implementation requires CPU binding so as to query topology information from each individual logical processor. This means that hwloc cannot find any useful topology information unless user-level process binding is allowed by the NetBSD kernel. The security.models.extensions.user_set_cpu_affinity sysctl variable must be set to 1 to do so. Otherwise, only the number of logical processors will be detected.

Module Index

17.1 Modules

Here is a list of all modules:

API version
Object Sets (hwloc_cpuset_t and hwloc_nodeset_t)
Object Types
Object Structure and Attributes
Topology Creation and Destruction
Topology Detection Configuration and Query
Object levels, depths and types
Converting between Object Types, Sets and Attributes, and Strings
Consulting and Adding Key-Value Info Attributes
CPU binding
Memory binding
Modifying a loaded Topology
Building Custom Topologies
Exporting Topologies to XML
Exporting Topologies to Synthetic
Finding Objects inside a CPU set
Finding Objects covering at least CPU set
Looking at Ancestor and Child Objects
Looking at Cache Objects
Finding objects, miscellaneous helpers
Distributing items over a topology
CPU and node sets of entire topologies
Converting between CPU sets and node sets
Manipulating Distances
Finding I/O objects
The bitmap API
Linux-specific helpers
Interoperability with Linux libnuma unsigned long masks
Interoperability with Linux libnuma bitmask
Interoperability with glibc sched affinity
Interoperability with OpenCL
Interoperability with the CUDA Driver API
Interoperability with the CUDA Runtime API
Interoperability with the NVIDIA Management Library
Interoperability with OpenGL displays 182

72 Module Index

Interoperability with Intel Xeon Phi (MIC)	84
Interoperability with OpenFabrics	85
Interoperability with Myrinet Express	87
Topology differences	88
Components and Plugins: Discovery components	95
Components and Plugins: Discovery backends	96
Components and Plugins: Generic components	98
Components and Plugins: Core functions to be used by components	99
Components and Plugins: PCI functions to be used by components	วกว

Data Structure Index

18.1 Data Structures

Here are the data structures with brief descriptions:

hwloc_backend	
Discovery backend structure	203
hwloc_obj_attr_u::hwloc_bridge_attr_s	
Bridge specific Object Attribues	205
hwloc_obj_attr_u::hwloc_cache_attr_s	
Cache-specific Object Attributes	207
hwloc_component	
Generic component structure	208
hwloc_disc_component	
·	210
hwloc_distances_s	
·	212
hwloc_obj_attr_u::hwloc_group_attr_s	
• •	213
hwloc_obj	
	214
hwloc_obj_attr_u	
Object type-specific Attributes	221
hwloc_obj_info_s	
Object info	222
hwloc_obj_memory_s::hwloc_obj_memory_page_type_s	
Array of local memory page types, NULL if no local memory and page_types is 0	223
hwloc_obj_memory_s	
Object memory	224
hwloc_obj_attr_u::hwloc_osdev_attr_s	
OS Device specific Object Attributes	225
hwloc_obj_attr_u::hwloc_pcidev_attr_s	
·	226
hwloc_topology_cpubind_support	
	228
- 1 01 1 01	230
_ 1 0/ /	231
hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s	231
hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s	
String attribute modification with an optional name	232

74 Data Structure Index

hwloc_topology_diff_obj_attr_u	
One object attribute difference	233
hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_uint64_s	
Integer attribute modification with an optional index	235
hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s	236
hwloc_topology_diff_u	
One element of a difference list between two topologies	236
hwloc_topology_discovery_support	
Flags describing actual discovery support for this topology	237
hwloc_topology_membind_support	
Flags describing actual memory binding support for this topology	238
hwloc_topology_support	
Set of flags describing actual support for this topology	241

Module Documentation

19.1 API version

Macros

- #define HWLOC_API_VERSION 0x00010b00
- #define HWLOC_COMPONENT_ABI 4

Functions

unsigned hwloc_get_api_version (void)

19.1.1 Detailed Description

19.1.2 Macro Definition Documentation

19.1.2.1 HWLOC_API_VERSION

```
#define HWLOC_API_VERSION 0x00010b00
```

Indicate at build time which hwloc API version is being used.

This number is updated to (X>>16)+(Y>>8)+Z when a new release X.Y.Z actually modifies the API.

Users may check for available features at build time using this number (see How do I handle ABI breaks and API upgrades?).

19.1.2.2 HWLOC_COMPONENT_ABI

```
#define HWLOC_COMPONENT_ABI 4
```

Current component and plugin ABI version (see hwloc/plugins.h)

76 Module Documentation

19.1.3 Function Documentation

19.1.3.1 hwloc_get_api_version()

Indicate at runtime which hwloc API version was used at build time.

Should be HWLOC_API_VERSION if running on the same version.

19.2 Object Sets (hwloc_cpuset_t and hwloc_nodeset_t)

Typedefs

- typedef hwloc_bitmap_t hwloc_cpuset_t
- typedef hwloc_const_bitmap_t hwloc_const_cpuset_t
- typedef hwloc_bitmap_t hwloc_nodeset_t
- · typedef hwloc const bitmap thwloc const nodeset t

19.2.1 Detailed Description

Hwloc uses bitmaps to represent two distinct kinds of object sets: CPU sets (hwloc_cpuset_t) and NUMA node sets (hwloc_nodeset_t). These types are both typedefs to a common back end type (hwloc_bitmap_t), and therefore all the hwloc bitmap functions are applicable to both hwloc cpuset t and hwloc nodeset t (see The bitmap API).

The rationale for having two different types is that even though the actions one wants to perform on these types are the same (e.g., enable and disable individual items in the set/mask), they're used in very different contexts: one for specifying which processors to use and one for specifying which NUMA nodes to use. Hence, the name difference is really just to reflect the intent of where the type is used.

19.2.2 Typedef Documentation

```
19.2.2.1 hwloc_const_cpuset_t

typedef hwloc_const_bitmap_t hwloc_const_cpuset_t

A non-modifiable hwloc_cpuset_t.

19.2.2.2 hwloc_const_nodeset_t

typedef hwloc_const_bitmap_t hwloc_const_nodeset_t

A non-modifiable hwloc_nodeset_t.

19.2.2.3 hwloc_cpuset_t

typedef hwloc_bitmap_t hwloc_cpuset_t

A CPU set is a bitmap whose bits are set according to CPU physical OS indexes.

It may be consulted and modified with the bitmap API as any hwloc_bitmap_t (see hwloc/bitmap.h).
```

```
typedef hwloc_bitmap_t hwloc_nodeset_t
```

A node set is a bitmap whose bits are set according to NUMA memory node physical OS indexes.

Each bit may be converted into a PU object using hwloc_get_pu_obj_by_os_index().

It may be consulted and modified with the bitmap API as any hwloc_bitmap_t (see hwloc/bitmap.h). Each bit may be converted into a NUMA node object using hwloc_get_numanode_obj_by_os_index().

When binding memory on a system without any NUMA node (when the whole memory is considered as a single memory bank), the nodeset may be either empty (no memory selected) or full (whole system memory selected).

See also Converting between CPU sets and node sets.

19.2.2.4 hwloc_nodeset_t

78 Module Documentation

19.3 Object Types

Typedefs

- typedef enum hwloc_obj_cache_type_e hwloc_obj_cache_type_t
- typedef enum hwloc_obj_bridge_type_e hwloc_obj_bridge_type_t
- typedef enum hwloc obj osdev type e hwloc obj osdev type t

Enumerations

```
    enum hwloc_obj_type_t {
        HWLOC_OBJ_SYSTEM, HWLOC_OBJ_MACHINE, HWLOC_OBJ_NUMANODE, HWLOC_OBJ_PACKA
        GE,
        HWLOC_OBJ_CACHE, HWLOC_OBJ_CORE, HWLOC_OBJ_PU, HWLOC_OBJ_GROUP,
```

- HWLOC_OBJ_MISC, HWLOC_OBJ_BRIDGE, HWLOC_OBJ_PCI_DEVICE, HWLOC_OBJ_OS_DEVICE}
- enum hwloc_obj_cache_type_e { HWLOC_OBJ_CACHE_UNIFIED, HWLOC_OBJ_CACHE_DATA, HWL
 OC_OBJ_CACHE_INSTRUCTION }
- enum hwloc_obj_bridge_type_e { HWLOC_OBJ_BRIDGE_HOST, HWLOC_OBJ_BRIDGE_PCI }
- enum hwloc_obj_osdev_type_e {
 HWLOC_OBJ_OSDEV_BLOCK, HWLOC_OBJ_OSDEV_GPU, HWLOC_OBJ_OSDEV_NETWORK, HW
 LOC_OBJ_OSDEV_OPENFABRICS,
 HWLOC_OBJ_OSDEV_DMA, HWLOC_OBJ_OSDEV_COPROC }
- enum hwloc_compare_types_e { HWLOC_TYPE_UNORDERED }

Functions

- int hwloc_compare_types (hwloc_obj_type_t type1, hwloc_obj_type_t type2)
- 19.3.1 Detailed Description
- 19.3.2 Typedef Documentation

```
19.3.2.1 hwloc_obj_bridge_type_t
```

```
typedef enum hwloc_obj_bridge_type_e hwloc_obj_bridge_type_t
```

Type of one side (upstream or downstream) of an I/O bridge.

```
19.3.2.2 hwloc_obj_cache_type_t
```

```
typedef enum hwloc_obj_cache_type_e hwloc_obj_cache_type_t
```

Cache type.

19.3 Object Types 79

19.3.2.3 hwloc_obj_osdev_type_t

typedef enum hwloc_obj_osdev_type_e hwloc_obj_osdev_type_t

Type of a OS device.

19.3.3 Enumeration Type Documentation

19.3.3.1 hwloc_compare_types_e

enum hwloc_compare_types_e

Enumerator

19.3.3.2 hwloc_obj_bridge_type_e

enum hwloc_obj_bridge_type_e

Type of one side (upstream or downstream) of an I/O bridge.

Enumerator

HWLOC_OBJ_BRIDGE_HOST	Host-side of a bridge, only possible upstream.
HWLOC_OBJ_BRIDGE_PCI	PCI-side of a bridge.

19.3.3.3 hwloc_obj_cache_type_e

enum hwloc_obj_cache_type_e

Cache type.

Enumerator

HWLOC_OBJ_CACHE_UNIFIED	Unified cache.
HWLOC_OBJ_CACHE_DATA	Data cache.
HWLOC_OBJ_CACHE_INSTRUCTION	Instruction cache. Only used when the
	HWLOC_TOPOLOGY_FLAG_ICACHES topology flag is set.

80 Module Documentation

19.3.3.4 hwloc_obj_osdev_type_e

enum hwloc_obj_osdev_type_e

Type of a OS device.

Enumerator

HWLOC_OBJ_OSDEV_BLOCK	Operating system block device. For instance "sda" on Linux.
HWLOC_OBJ_OSDEV_GPU	Operating system GPU device. For instance ":0.0" for a GL display, "card0" for a Linux DRM device.
HWLOC_OBJ_OSDEV_NETWORK	Operating system network device. For instance the "eth0" interface on Linux.
HWLOC_OBJ_OSDEV_OPENFABRICS	Operating system openfabrics device. For instance the "mlx4_0" InfiniBand HCA, or "hfi1_0" Omni-Path interface on Linux.
HWLOC_OBJ_OSDEV_DMA	Operating system dma engine device. For instance the "dma0chan0" DMA channel on Linux.
HWLOC_OBJ_OSDEV_COPROC	Operating system co-processor device. For instance "mic0" for a Xeon Phi (MIC) on Linux, "opencl0d0" for a OpenCL device, "cuda0" for a CUDA device.

19.3.3.5 hwloc_obj_type_t

enum hwloc_obj_type_t

Type of topology object.

Note

Enumerator

HWLOC_OBJ_SYSTEM	Whole system (may be a cluster of machines). The whole system that is accessible to hwloc. That may comprise several machines in SSI systems like Kerrighed.
HWLOC_OBJ_MACHINE	Machine. The typical root object type. A set of processors and memory with cache coherency.
HWLOC_OBJ_NUMANODE	NUMA node. A set of processors around memory which the processors can directly access.
HWLOC_OBJ_PACKAGE	Physical package, what goes into a socket. In the physical meaning, i.e. that you can add or remove physically.
HWLOC_OBJ_CACHE	Cache. Can be L1i, L1d, L2, L3,
HWLOC_OBJ_CORE	Core. A computation unit (may be shared by several logical processors).

19.3 Object Types 81

Enumerator

HWLOC_OBJ_PU	Processing Unit, or (Logical) Processor. An execution unit (may share a core with some other logical processors, e.g. in the case of an SMT core). Objects of this kind are always reported and can thus be used as fallback when others are not.
HWLOC_OBJ_GROUP	Group objects. Objects which do not fit in the above but are detected by hwloc and are useful to take into account for affinity. For instance, some operating systems expose their arbitrary processors aggregation this way. And hwloc may insert such objects to group NUMA nodes according to their distances. See also What are these Group objects in my topology?. These objects are ignored when they do not bring any structure.
HWLOC_OBJ_MISC	Miscellaneous objects. Objects without particular meaning, that can e.g. be added by the application for its own use, or by hwloc for miscellaneous objects such as MemoryModule (DIMMs).
HWLOC_OBJ_BRIDGE	Bridge. Any bridge that connects the host or an I/O bus, to another I/O bus. Bridge objects have neither CPU sets nor node sets. They are not added to the topology unless I/O discovery is enabled with hwloc_topology_set_flags().
HWLOC_OBJ_PCI_DEVICE	PCI device. These objects have neither CPU sets nor node sets. They are not added to the topology unless I/O discovery is enabled with hwloc_topology_set_flags().
HWLOC_OBJ_OS_DEVICE	Operating system device. These objects have neither CPU sets nor node sets. They are not added to the topology unless I/O discovery is enabled with hwloc_topology_set_flags().

19.3.4 Function Documentation

19.3.4.1 hwloc_compare_types()

```
int hwloc_compare_types (
                hwloc_obj_type_t type1,
                 hwloc_obj_type_t type2 )
```

Compare the depth of two object types.

Types shouldn't be compared as they are, since newer ones may be added in the future. This function returns less than, equal to, or greater than zero respectively if type1 objects usually include type2 objects, are the same as type2 objects, or are included in type2 objects. If the types can not be compared (because neither is usually contained in the other), HWLOC_TYPE_UNORDERED is returned. Object types containing CPUs can always be compared (usually, a system contains machines which contain nodes which contain packages which contain caches, which contain cores, which contain processors).

Note

HWLOC_OBJ_PU will always be the deepest.

This does not mean that the actual topology will respect that order: e.g. as of today cores may also contain caches, and packages may also contain nodes. This is thus just to be seen as a fallback comparison method.

19.4 Object Structure and Attributes

Data Structures

- struct hwloc_obj_memory_s
- struct hwloc_obj
- union hwloc_obj_attr_u
- struct hwloc_distances_s
- struct hwloc_obj_info_s

Typedefs

- typedef struct hwloc_obj_t
- 19.4.1 Detailed Description
- 19.4.2 Typedef Documentation

```
19.4.2.1 hwloc_obj_t

typedef struct hwloc_obj* hwloc_obj_t
```

Convenience typedef; a pointer to a struct hwloc_obj.

19.5 Topology Creation and Destruction

Typedefs

typedef struct hwloc_topology * hwloc_topology_t

Functions

- int hwloc_topology_init (hwloc_topology_t *topologyp)
- int hwloc_topology_load (hwloc_topology_t topology)
- void hwloc_topology_destroy (hwloc_topology_t topology)
- int hwloc_topology_dup (hwloc_topology_t *newtopology, hwloc_topology_t oldtopology)
- void hwloc_topology_check (hwloc_topology_t topology)

19.5.1 Detailed Description

19.5.2 Typedef Documentation

```
19.5.2.1 hwloc_topology_t
typedef struct hwloc_topology* hwloc_topology_t
```

Topology context.

To be initialized with hwloc topology init() and built with hwloc topology load().

19.5.3 Function Documentation

19.5.3.1 hwloc_topology_check()

Run internal checks on a topology structure.

The program aborts if an inconsistency is detected in the given topology.

Parameters

topology is the topology to be checked

Note

This routine is only useful to developers.

The input topology should have been previously loaded with hwloc_topology_load().

19.5.3.2 hwloc_topology_destroy()

Terminate and free a topology context.

Parameters

```
topology is the topology to be freed
```

19.5.3.3 hwloc_topology_dup()

Duplicate a topology.

The entire topology structure as well as its objects are duplicated into a new one.

This is useful for keeping a backup while modifying a topology.

Note

Object userdata is not duplicated since hwloc does not know what it point to. The objects of both old and new topologies will point to the same userdata.

19.5.3.4 hwloc_topology_init()

Allocate a topology context.

Parameters

out topologyp is assigned a pointer to the new allocated contex	a pointer to the new allocated context.	topologyp	out
---	---	-----------	-----

Returns

0 on success, -1 on error.

19.5.3.5 hwloc_topology_load()

Build the actual topology.

Build the actual topology once initialized with hwloc_topology_init() and tuned with Topology Detection Configuration and Query routines. No other routine may be called earlier using this topology context.

Parameters

topology	is the topology to be loaded with objects.
----------	--

Returns

0 on success, -1 on error.

Note

On failure, the topology is reinitialized. It should be either destroyed with hwloc_topology_destroy() or configured and loaded again.

This function may be called only once per topology.

The binding of the current thread or process may temporarily change during this call but it will be restored before it returns.

See also

Topology Detection Configuration and Query

19.6 Topology Detection Configuration and Query

Data Structures

- · struct hwloc_topology_discovery_support
- struct hwloc_topology_cpubind_support
- · struct hwloc_topology_membind_support
- · struct hwloc topology support

Enumerations

enum hwloc_topology_flags_e {
 HWLOC_TOPOLOGY_FLAG_WHOLE_SYSTEM, HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM, HWL
 OC_TOPOLOGY_FLAG_IO_DEVICES, HWLOC_TOPOLOGY_FLAG_IO_BRIDGES,
 HWLOC_TOPOLOGY_FLAG_WHOLE_IO, HWLOC_TOPOLOGY_FLAG_ICACHES, HWLOC_TOPOLO
 GY_FLAG_THISSYSTEM_ALLOWED_RESOURCES }

Functions

- int hwloc_topology_ignore_type (hwloc_topology_t topology, hwloc_obj_type_t type)
- int hwloc_topology_ignore_type_keep_structure (hwloc_topology_t topology, hwloc_obj_type_t type)
- int hwloc_topology_ignore_all_keep_structure (hwloc_topology_t topology)
- int hwloc_topology_set_flags (hwloc_topology_t topology, unsigned long flags)
- unsigned long hwloc topology get flags (hwloc topology t topology)
- int hwloc_topology_set_pid (hwloc_topology_t restrict topology, hwloc_pid_t pid)
- int hwloc topology set fsroot (hwloc topology t restrict topology, const char *restrict fsroot path)
- int hwloc_topology_set_synthetic (hwloc_topology_t restrict topology, const char *restrict description)
- int hwloc_topology_set_xml (hwloc_topology_t restrict topology, const char *restrict xmlpath)
- int hwloc topology set xmlbuffer (hwloc topology t restrict topology, const char *restrict buffer, int size)
- int hwloc_topology_set_custom (hwloc_topology_t topology)
- int hwloc_topology_set_distance_matrix (hwloc_topology_t restrict topology, hwloc_obj_type_t type, unsigned nbobjs, unsigned *os_index, float *distances)
- int hwloc_topology_is_thissystem (hwloc_topology_t restrict topology)
- const struct hwloc topology support * hwloc topology get support (hwloc topology t restrict topology)
- void hwloc topology set userdata (hwloc topology t topology, const void *userdata)
- void * hwloc_topology_get_userdata (hwloc_topology_t topology)

19.6.1 Detailed Description

Several functions can optionally be called between hwloc_topology_load()) to configure how the detection should be performed, e.g. to ignore some objects types, define a synthetic topology, etc.

If none of them is called, the default is to detect all the objects of the machine that the caller is allowed to access.

This default behavior may also be modified through environment variables if the application did not modify it already. Setting HWLOC_XMLFILE in the environment enforces the discovery from a XML file as if hwloc_topology_set __xml() had been called. HWLOC_FSROOT switches to reading the topology from the specified Linux filesystem root as if hwloc_topology_set_fsroot() had been called. Finally, HWLOC_THISSYSTEM enforces the return value of hwloc_topology_is_thissystem().

19.6.2 Enumeration Type Documentation

19.6.2.1 hwloc_topology_flags_e

enum hwloc_topology_flags_e

Flags to be set onto a topology context before load.

Flags should be given to hwloc_topology_set_flags(). They may also be returned by hwloc_topology_get_flags().

Enumerator

HWLOC_TOPOLOGY_FLAG_WHOLE_SYSTEM	Detect the whole system, ignore reservations and offline settings. Gather all resources, even if some were disabled by the administrator. For instance, ignore Linux Cgroup/Cpusets and gather all processors and memory nodes, and ignore the fact that some resources may be offline. When this flag is not set, PUs that are disallowed are not added to the topology. Parent objects (package, core, cache, etc.) are added only if some of their children are allowed. NUMA nodes are always added but their available memory is set to 0 when disallowed. If the current topology is exported to XML and reimported later, this flag should be set again in the reimported as well.
HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM	Assume that the selected backend provides the topology for the system on which we are running. This forces hwloc_topology_is_thissystem() to return 1, i.e. makes hwloc assume that the selected backend provides the topology for the system on which we are running, even if it is not the OS-specific backend but the XML backend for instance. This means making the binding functions actually call the OS-specific system calls and really do binding, while the XML backend would otherwise provide empty hooks just returning success. Setting the environment variable HWLOC_THISSYSTEM may also result in the same behavior. This can be used for efficiency reasons to first detect the topology once, save it to an XML file, and quickly reload it later through the XML backend, but still having binding functions actually do bind.
HWLOC_TOPOLOGY_FLAG_IO_DEVICES	Detect PCI devices. By default, I/O devices are ignored. This flag enables I/O device detection using the pci backend. Only the common PCI devices (GPUs, NICs, block devices,) and host bridges (objects that connect the host objects to an I/O subsystem) will be added to the topology. Additionally it also enables MemoryModule misc objects. Uncommon devices and other bridges (such as PCI-to-PCI bridges) will be ignored.

Enumerator

HWLOC_TOPOLOGY_FLAG_IO_BRIDGES	Detect PCI bridges. This flag should be combined with HWLOC_TOPOLOGY_FLAG_IO_DEVICES to enable the detection of both common devices and of all useful bridges (bridges that have at least one device behind them).
HWLOC_TOPOLOGY_FLAG_WHOLE_IO	Detect the whole PCI hierarchy. This flag enables detection of all I/O devices (even the uncommon ones such as DMA channels) and bridges (even those that have no device behind them) using the pci backend. This implies HWLOC_TOPOLOGY_FLAG_IO_DEVICES.
HWLOC_TOPOLOGY_FLAG_ICACHES	Detect instruction caches. This flag enables detection of Instruction caches, instead of only Data and Unified caches.
HWLOC_TOPOLOGY_FLAG_THISSYSTEM_ALL↔ OWED_RESOURCES	Get the set of allowed resources from the local operating system even if the topology was loaded from XML or synthetic description. If the topology was loaded from XML or from a synthetic string, restrict it by applying the current process restrictions such as Linux Cgroup/Cpuset. This is useful when the topology is not loaded directly from the local machine (e.g. for performance reason) and it comes with all resources, while the running process is restricted to only parts of the machine. This flag is ignored unless HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM is also set since the loaded topology must match the underlying machine where restrictions will be gathered from. Setting the environment variable HWLOC_THISSYSTEM_ALLOWED_RESOURCES would result in the same behavior.

19.6.3 Function Documentation

19.6.3.1 hwloc_topology_get_flags()

Get OR'ed flags of a topology.

Get the OR'ed set of hwloc_topology_flags_e of a topology.

Returns

the flags previously set with hwloc_topology_set_flags().

19.6.3.2 hwloc_topology_get_support()

```
\label{location} \mbox{const struct $h$wloc_topology_support* $h$wloc_topology_get_support (} \\ \mbox{$h$wloc_topology_t restrict $topology$ )}
```

Retrieve the topology support.

Each flag indicates whether a feature is supported. If set to 0, the feature is not supported. If set to 1, the feature is supported, but the corresponding call may still fail in some corner cases.

These features are also listed by hwloc-info --support

19.6.3.3 hwloc_topology_get_userdata()

Retrieve the topology-specific userdata pointer.

Retrieve the application-given private data pointer that was previously set with hwloc topology set userdata().

19.6.3.4 hwloc_topology_ignore_all_keep_structure()

Ignore all objects that do not bring any structure.

Ignore all objects that do not bring any structure: This is equivalent to calling hwloc_topology_ignore_type_keep __structure() for all object types.

19.6.3.5 hwloc_topology_ignore_type()

Ignore an object type.

Ignore all objects from the given type. The bottom-level type HWLOC_OBJ_PU may not be ignored. The top-level object of the hierarchy will never be ignored, even if this function succeeds. Group objects are always ignored if they do not bring any structure since they are designed to add structure to the topology. I/O objects may not be ignored, topology flags should be used to configure their discovery instead.

19.6.3.6 hwloc_topology_ignore_type_keep_structure()

Ignore an object type if it does not bring any structure.

Ignore all objects from the given type as long as they do not bring any structure: Each ignored object should have a single children or be the only child of its parent. The bottom-level type HWLOC_OBJ_PU may not be ignored. I/O objects may not be ignored, topology flags should be used to configure their discovery instead.

19.6.3.7 hwloc_topology_is_thissystem()

```
int hwloc_topology_is_thissystem ( {\tt hwloc\_topology\_t\ restrict\ } topology\ )
```

Does the topology context come from this system?

Returns

1 if this topology context was built using the system running this program.

0 instead (for instance if using another file-system root, a XML topology file, or a synthetic topology).

19.6.3.8 hwloc_topology_set_custom()

Prepare the topology for custom assembly.

The topology then contains a single root object. It must then be built by inserting other topologies with hwloc_custom_insert_topology() or single objects with hwloc_custom_insert_group_object_by_parent(). hwloc topology load() must be called to finalize the new topology as usual.

Note

If nothing is inserted in the topology, hwloc topology load() will fail with errno set to EINVAL.

The cpuset and nodeset of the root object are NULL because these sets are meaningless when assembling multiple topologies.

On success, the custom component replaces the previously enabled component (if any), but the topology is not actually modified until <a href="https://doi.org/10.2016/nc.2016/

19.6.3.9 hwloc_topology_set_distance_matrix()

```
int hwloc_topology_set_distance_matrix (
          hwloc_topology_t restrict topology,
          hwloc_obj_type_t type,
          unsigned nbobjs,
          unsigned * os_index,
          float * distances )
```

Provide a distance matrix.

Provide the matrix of distances between a set of objects of the given type. nbobjs must be at least 2. The set may or may not contain all the existing objects of this type. The objects are specified by their OS/physical index in the os_index array. The distances matrix follows the same order. The distance from object i to object j in the i*nbobjs+j.

A single latency matrix may be defined for each type. If another distance matrix already exists for the given type, either because the user specified it or because the OS offers it, it will be replaced by the given one. If nbobjs is 0, os_index is NULL and distances is NULL, the existing distance matrix for the given type is removed.

Note

Distance matrices are ignored in multi-node topologies.

19.6.3.10 hwloc_topology_set_flags()

Set OR'ed flags to non-yet-loaded topology.

Set a OR'ed set of hwloc topology flags e onto a topology that was not yet loaded.

If this function is called multiple times, the last invokation will erase and replace the set of flags that was previously

The flags set in a topology may be retrieved with hwloc_topology_get_flags()

19.6.3.11 hwloc_topology_set_fsroot()

Change the file-system root path when building the topology from sysfs/procfs.

On Linux system, use sysfs and procfs files as if they were mounted on the given fsroot_path instead of the main file-system root. Setting the environment variable HWLOC_FSROOT may also result in this behavior. Not using the main file-system root causes hwloc_topology_is_thissystem() to return 0.

Note that this function does not actually load topology information; it just tells hwloc where to load it from. You'll still need to invoke hwloc_topology_load() to actually load the topology information.

Returns

- -1 with errno set to ENOSYS on non-Linux and on Linux systems that do not support it.
- -1 with the appropriate errno if fsroot_path cannot be used.

Note

For convenience, this backend provides empty binding hooks which just return success. To have hwloc still actually call OS-specific hooks, the HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM has to be set to assert that the loaded file is really the underlying system.

On success, the Linux component replaces the previously enabled component (if any), but the topology is not actually modified until hwloc_topology_load().

19.6.3.12 hwloc_topology_set_pid()

Change which process the topology is viewed from.

On some systems, processes may have different views of the machine, for instance the set of allowed CPUs. By default, hwloc exposes the view from the current process. Calling hwloc_topology_set_pid() permits to make it expose the topology of the machine from the point of view of another process.

Note

```
hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.
```

-1 is returned and errno is set to ENOSYS on platforms that do not support this feature.

19.6.3.13 hwloc_topology_set_synthetic()

Enable synthetic topology.

Gather topology information from the given description, a space-separated string of numbers describing the arity of each level. Each number may be prefixed with a type and a colon to enforce the type of a level. If only some level types are enforced, hwloc will try to choose the other types according to usual topologies, but it may fail and you may have to specify more level types manually. See also the Synthetic topologies.

If description was properly parsed and describes a valid topology configuration, this function returns 0. Otherwise -1 is returned and errno is set to EINVAL.

Note that this function does not actually load topology information; it just tells hwloc where to load it from. You'll still need to invoke hwloc topology load() to actually load the topology information.

Note

For convenience, this backend provides empty binding hooks which just return success. On success, the synthetic component replaces the previously enabled component (if any), but the topology is not actually modified until <a href="https://hww.ncbeneuron.com/hw

19.6.3.14 hwloc_topology_set_userdata()

Set the topology-specific userdata pointer.

Each topology may store one application-given private data pointer. It is initialized to \mathtt{NULL} . hwloc will never modify it

Use it as you wish, after hwloc_topology_init() and until hwloc_topolog_destroy().

This pointer is not exported to XML.

19.6.3.15 hwloc_topology_set_xml()

```
int hwloc_topology_set_xml (
          hwloc_topology_t restrict topology,
          const char *restrict xmlpath )
```

Enable XML-file based topology.

Gather topology information from the XML file given at xmlpath. Setting the environment variable HWLOC_XM LFILE may also result in this behavior. This file may have been generated earlier with hwloc_topology_export_xml() or Istopo file.xml.

Note that this function does not actually load topology information; it just tells hwloc where to load it from. You'll still need to invoke hwloc_topology_load() to actually load the topology information.

Returns

-1 with errno set to EINVAL on failure to read the XML file.

Note

See also hwloc_topology_set_userdata_import_callback() for importing application-specific object userdata. For convenience, this backend provides empty binding hooks which just return success. To have hwloc still actually call OS-specific hooks, the HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM has to be set to assert that the loaded file is really the underlying system.

On success, the XML component replaces the previously enabled component (if any), but the topology is not actually modified until hwloc topology load().

19.6.3.16 hwloc_topology_set_xmlbuffer()

```
int hwloc_topology_set_xmlbuffer (
          hwloc_topology_t restrict topology,
          const char *restrict buffer,
          int size )
```

Enable XML based topology using a memory buffer (instead of a file, as with hwloc_topology_set_xml()).

Gather topology information from the XML memory buffer given at buffer and of length size. This buffer may have been filled earlier with hwloc_topology_export_xmlbuffer().

Note that this function does not actually load topology information; it just tells hwloc where to load it from. You'll still need to invoke hwloc_topology_load() to actually load the topology information.

Returns

-1 with errno set to EINVAL on failure to read the XML buffer.

Note

See also hwloc_topology_set_userdata_import_callback() for importing application-specific object userdata. For convenience, this backend provides empty binding hooks which just return success. To have hwloc still actually call OS-specific hooks, the HWLOC_TOPOLOGY_FLAG_IS_THISSYSTEM has to be set to assert that the loaded file is really the underlying system.

On success, the XML component replaces the previously enabled component (if any), but the topology is not actually modified until hwloc_topology_load().

19.7 Object levels, depths and types

Enumerations

enum hwloc_get_type_depth_e {
 HWLOC_TYPE_DEPTH_UNKNOWN, HWLOC_TYPE_DEPTH_MULTIPLE, HWLOC_TYPE_DEPTH_BR
 IDGE, HWLOC_TYPE_DEPTH_PCI_DEVICE,
 HWLOC_TYPE_DEPTH_OS_DEVICE }

Functions

- unsigned hwloc_topology_get_depth (hwloc_topology_t restrict topology)
- int hwloc_get_type_depth (hwloc_topology_t topology, hwloc_obj_type_t type)
- static int hwloc get type or below depth (hwloc topology t topology, hwloc obj type t type)
- static int hwloc_get_type_or_above_depth (hwloc_topology_t topology, hwloc_obj_type_t type)
- hwloc_obj_type_t hwloc_get_depth_type (hwloc_topology_t topology, unsigned depth)
- unsigned hwloc_get_nbobjs_by_depth (hwloc_topology_t topology, unsigned depth)
- static int hwloc_get_nbobjs_by_type (hwloc_topology_t topology, hwloc_obj_type_t type)
- static hwloc_obj_t hwloc_get_root_obj (hwloc_topology_t topology)
- · hwloc obj t hwloc get obj by depth (hwloc topology t topology, unsigned depth, unsigned idx)
- static hwloc_obj_t hwloc_get_obj_by_type (hwloc_topology_t topology, hwloc_obj_type_t type, unsigned idx)
- static hwloc_obj_t hwloc_get_next_obj_by_depth (hwloc_topology_t topology, unsigned depth, hwloc_obj_t prev)
- static hwloc_obj_t hwloc_get_next_obj_by_type (hwloc_topology_t topology, hwloc_obj_type_t type, hwloc
 obj_t prev)

19.7.1 Detailed Description

Be sure to see the figure in Terms and Definitions that shows a complete topology tree, including depths, child/sibling/cousin relationships, and an example of an asymmetric topology where one package has fewer caches than its peers.

19.7.2 Enumeration Type Documentation

19.7.2.1 hwloc_get_type_depth_e

enum hwloc_get_type_depth_e

Enumerator

HWLOC_TYPE_DEPTH_UNKNOWN	No object of given type exists in the topology.
HWLOC_TYPE_DEPTH_MULTIPLE	Objects of given type exist at different depth in the topology.
HWLOC_TYPE_DEPTH_BRIDGE	Virtual depth for bridge object level.
HWLOC_TYPE_DEPTH_PCI_DEVICE	Virtual depth for PCI device object level.
HWLOC_TYPE_DEPTH_OS_DEVICE	Virtual depth for software device object level.

19.7.3 Function Documentation

19.7.3.1 hwloc_get_depth_type()

Returns the type of objects at depth depth.

depth should between 0 and hwloc_topology_get_depth()-1.

Returns

-1 if depth depth does not exist.

19.7.3.2 hwloc_get_nbobjs_by_depth()

Returns the width of level at depth depth.

19.7.3.3 hwloc_get_nbobjs_by_type()

Returns the width of level type type.

If no object for that type exists, 0 is returned. If there are several levels with objects of that type, -1 is returned.

19.7.3.4 hwloc_get_next_obj_by_depth()

Returns the next object at depth depth.

If ${\tt prev}$ is ${\tt NULL},$ return the first object at depth ${\tt depth}$

19.7.3.5 hwloc_get_next_obj_by_type()

Returns the next object of type type.

If prev is NULL, return the first object at type type. If there are multiple or no depth for given type, return NULL and let the caller fallback to hwloc_get_next_obj_by_depth().

19.7.3.6 hwloc_get_obj_by_depth()

```
hwloc_obj_t hwloc_get_obj_by_depth (
          hwloc_topology_t topology,
          unsigned depth,
          unsigned idx )
```

Returns the topology object at logical index idx from depth depth.

19.7.3.7 hwloc_get_obj_by_type()

Returns the topology object at logical index idx with type type.

If no object for that type exists, NULL is returned. If there are several levels with objects of that type, NULL is returned and ther caller may fallback to $\texttt{hwloc_get_obj_by_depth}()$.

19.7.3.8 hwloc_get_root_obj()

Returns the top-object of the topology-tree.

Its type is typically HWLOC_OBJ_MACHINE but it could be different for complex topologies.

19.7.3.9 hwloc_get_type_depth()

Returns the depth of objects of type type.

If no object of this type is present on the underlying architecture, or if the OS doesn't provide this kind of information, the function returns HWLOC TYPE DEPTH UNKNOWN.

If type is absent but a similar type is acceptable, see also hwloc_get_type_or_below_depth() and hwloc_get_type or above depth().

If some objects of the given type exist in different levels, for instance L1 and L2 caches, or L1i and L1d caches, the function returns HWLOC_TYPE_DEPTH_MULTIPLE. See hwloc_get_cache_type_depth() in hwloc/helper.h to better handle this case.

If an I/O object type is given, the function returns a virtual value because I/O objects are stored in special levels that are not CPU-related. This virtual depth may be passed to other hwloc functions such as hwloc_get_obj_by_depth() but it should not be considered as an actual depth by the application. In particular, it should not be compared with any other object depth or with the entire topology depth.

If HWLOC OBJ MISC is given, the function returns HWLOC TYPE DEPTH UNKNOWN.

19.7.3.10 hwloc_get_type_or_above_depth()

Returns the depth of objects of type type or above.

If no object of this type is present on the underlying architecture, the function returns the depth of the first "present" object typically containing type.

If some objects of the given type exist in different levels, for instance L1 and L2 caches, the function returns HWL← OC TYPE DEPTH MULTIPLE.

19.7.3.11 hwloc_get_type_or_below_depth()

Returns the depth of objects of type type or below.

If no object of this type is present on the underlying architecture, the function returns the depth of the first "present" object typically found inside type.

If some objects of the given type exist in different levels, for instance L1 and L2 caches, the function returns HWL← OC_TYPE_DEPTH_MULTIPLE.

19.7.3.12 hwloc_topology_get_depth()

Get the depth of the hierarchical tree of objects.

This is the depth of HWLOC_OBJ_PU objects plus one.

Note

I/O and Misc objects are ignored when computing the depth of the tree (they are placed on special levels, or none).

19.8 Converting between Object Types, Sets and Attributes, and Strings

Functions

- const char * hwloc_obj_type_string (hwloc_obj_type_t type)
- int hwloc_obj_type_snprintf (char *restrict string, size_t size, hwloc_obj_t obj, int verbose)
- int hwloc_obj_attr_snprintf (char *restrict string, size_t size, hwloc_obj_t obj, const char *restrict separator, int verbose)
- int hwloc_obj_cpuset_snprintf (char *restrict str, size_t size, size_t nobj, const hwloc_obj_t *restrict objs)
- int hwloc_obj_type_sscanf (const char *string, hwloc_obj_type_t *typep, int *depthattrp, void *typeattrp, size_t typeattrsize)

19.8.1 Detailed Description

19.8.2 Function Documentation

19.8.2.1 hwloc_obj_attr_snprintf()

Stringify the attributes of a given topology object into a human-readable form.

Attribute values are separated by separator.

Only the major attributes are printed in non-verbose mode.

If size is 0, string may safely be NULL.

Returns

the number of character that were actually written if not truncating, or that would have been written (not including the ending \0).

19.8.2.2 hwloc_obj_cpuset_snprintf()

Stringify the cpuset containing a set of objects.

If size is 0, string may safely be NULL.

Returns

the number of character that were actually written if not truncating, or that would have been written (not including the ending \0).

19.8.2.3 hwloc_obj_type_snprintf()

Stringify the type of a given topology object into a human-readable form.

Contrary to hwloc_obj_type_string(), this function includes object-specific attributes (such as the Group depth, the Bridge type, or OS device type) in the output, and it requires the caller to provide the output buffer.

The output is guaranteed to be the same for all objects of a same topology level.

If size is 0, string may safely be NULL.

Returns

the number of character that were actually written if not truncating, or that would have been written (not including the ending \0).

19.8.2.4 hwloc obj type sscanf()

Return an object type and attributes from a type string.

Convert strings such as "Package" or "Cache" into the corresponding types. Matching is case-insensitive, and only the first letters are actually required to match.

This function is guaranteed to match any string returned by hwloc_obj_type_string() or hwloc_obj_type_snprintf().

Types that have specific attributes, for instance caches and groups, may be returned in depthattrp and typeattrp. They are ignored when these pointers are NULL.

For instance "L2i" or "L2iCache" would return type HWLOC_OBJ_CACHE in typep, 2 in depthattrp, and HWLOC_OBJ_CACHE_TYPE_INSTRUCTION in typeattrp (this last pointer should point to a hwloc_obj_cache_type_t). "Group3" would return type HWLOC_OBJ_GROUP type and 3 in depthattrp. Attributes that are not specified in the string (for instance "Group" without a depth, or "L2Cache" without a cache type) are set to -1.

typeattrp is only filled if the size specified in typeattrsize is large enough. It is currently only used for caches, and the required size is at least the size of hwloc_obj_cache_type_t.

Returns

0 if a type was correctly identified, otherwise -1.

Note

This is an extended version of the now deprecated hwloc obj type of string()

19.8.2.5 hwloc_obj_type_string()

Return a constant stringified object type.

This function is the basic way to convert a generic type into a string.

hwloc_obj_type_snprintf() may return a more precise output for a specific object, but it requires the caller to provide the output buffer.

19.9 Consulting and Adding Key-Value Info Attributes

Functions

- static const char * hwloc_obj_get_info_by_name (hwloc_obj_t obj, const char *name)
- void hwloc_obj_add_info (hwloc_obj_t obj, const char *name, const char *value)

19.9.1 Detailed Description

19.9.2 Function Documentation

19.9.2.1 hwloc_obj_add_info()

Add the given info name and value pair to the given object.

The info is appended to the existing info array even if another key with the same name already exists.

The input strings are copied before being added in the object infos.

Note

This function may be used to enforce object colors in the Istopo graphical output by using "IstopoStyle" as a name and "Background=#rrggbb" as a value. See CUSTOM COLORS in the Istopo(1) manpage for details. If value contains some non-printable characters, they will be dropped when exporting to XML, see hwloc—topology_export_xml().

19.9.2.2 hwloc_obj_get_info_by_name()

Search the given key name in object infos and return the corresponding value.

If multiple keys match the given name, only the first one is returned.

Returns

 \mathtt{NULL} if no such key exists.

19.10 CPU binding

Enumerations

enum hwloc_cpubind_flags_t { HWLOC_CPUBIND_PROCESS, HWLOC_CPUBIND_THREAD, HWLOC_←
 CPUBIND_STRICT, HWLOC_CPUBIND_NOMEMBIND }

Functions

- int hwloc_set_cpubind (hwloc_topology_t topology, hwloc_const_cpuset_t set, int flags)
- int hwloc_get_cpubind (hwloc_topology_t topology, hwloc_cpuset_t set, int flags)
- int hwloc_set_proc_cpubind (hwloc_topology_t topology, hwloc_pid_t pid, hwloc_const_cpuset_t set, int flags)
- int hwloc get proc cpubind (hwloc topology t topology, hwloc pid t pid, hwloc cpuset t set, int flags)
- int hwloc_set_thread_cpubind (hwloc_topology_t topology, hwloc_thread_t thread, hwloc_const_cpuset_← t set, int flags)
- int hwloc_get_thread_cpubind (hwloc_topology_t topology, hwloc_thread_t thread, hwloc_cpuset_t set, int flags)
- int hwloc_get_last_cpu_location (hwloc_topology_t topology, hwloc_cpuset_t set, int flags)
- int hwloc_get_proc_last_cpu_location (hwloc_topology_t topology, hwloc_pid_t pid, hwloc_cpuset_t set, int flags)

19.10.1 Detailed Description

It is often useful to call https://hww.ncbitmap_singlify() first so that a single CPU remains in the set. This way, the process will not even migrate between different CPUs inside the given set. Some operating systems also only support that kind of binding.

Some operating systems do not provide all hwloc-supported mechanisms to bind processes, threads, etc. hwloc_ctopology_get_support() may be used to query about the actual CPU binding support in the currently used operating system.

When the requested binding operation is not available and the HWLOC_CPUBIND_STRICT flag was passed, the function returns -1. errno is set to ENOSYS when it is not possible to bind the requested kind of object processes/threads. errno is set to EXDEV when the requested cpuset can not be enforced (e.g. some systems only allow one CPU, and some other systems only allow one NUMA node).

If HWLOC_CPUBIND_STRICT was not passed, the function may fail as well, or the operating system may use a slightly different operation (with side-effects, smaller binding set, etc.) when the requested operation is not exactly supported.

The most portable version that should be preferred over the others, whenever possible, is the following one which just binds the current program, assuming it is single-threaded:

```
hwloc_set_cpubind(topology, set, 0),
```

If the program may be multithreaded, the following one should be preferred to only bind the current thread:

```
hwloc_set_cpubind(topology, set, HWLOC_CPUBIND_THREAD),
```

See also

Some example codes are available under doc/examples/ in the source tree.

Note

To unbind, just call the binding function with either a full cpuset or a cpuset equal to the system cpuset.

On some operating systems, CPU binding may have effects on memory binding, see HWLOC_CPUBIND_

NOMEMBIND

Running Istopo --top or hwloc-ps can be a very convenient tool to check how binding actually happened.

19.10 CPU binding

19.10.2 Enumeration Type Documentation

19.10.2.1 hwloc_cpubind_flags_t

enum hwloc_cpubind_flags_t

Process/Thread binding flags.

These bit flags can be used to refine the binding policy.

The default (0) is to bind the current process, assumed to be single-threaded, in a non-strict way. This is the most portable way to bind as all operating systems usually provide it.

Note

Not all systems support all kinds of binding. See the "Detailed Description" section of CPU binding for a description of errors that can occur.

Enumerator

HWLOC_CPUBIND_PROCESS	Bind all threads of the current (possibly) multithreaded process.
HWLOC_CPUBIND_THREAD	Bind current thread of current process.
HWLOC_CPUBIND_STRICT	Request for strict binding from the OS. By default, when the designated CPUs are all busy while other CPUs are idle, operating systems may execute the thread/process on those other CPUs instead of the designated CPUs, to let them progress anyway. Strict binding means that the thread/process will _never_ execute on other cpus than the designated CPUs, even when those are busy with other tasks and other CPUs are idle.
	Note
	Depending on the operating system, strict binding may not be possible (e.g., the OS does not implement it) or not allowed (e.g., for an administrative reasons), and the function will fail in that case.
	When retrieving the binding of a process, this flag checks whether all its threads actually have the same binding. If the flag is not given, the binding of each thread will be accumulated.
	Note
	This flag is meaningless when retrieving the binding of a thread.
HWLOC_CPUBIND_NOMEMBIND	Avoid any effect on memory binding. On some operating systems, some CPU binding function would also bind the memory on the corresponding NUMA node. It is often not a problem for the application, but if it is, setting this flag will make hwloc avoid using OS functions that would also bind memory. This will however reduce the support of CPU bindings, i.e. potentially return -1 with errno set to ENOSYS in some cases. This flag is only meaningful when used with functions that set the CPU binding. It is ignored when used with functions that get CPU binding information.

19.10.3 Function Documentation

19.10.3.1 hwloc_get_cpubind()

Get current process or thread binding.

Writes into set the physical cpuset which the process or thread (according to flags) was last bound to.

19.10.3.2 hwloc_get_last_cpu_location()

Get the last physical CPU where the current process or thread ran.

The operating system may move some tasks from one processor to another at any time according to their binding, so this function may return something that is already outdated.

flags can include either HWLOC_CPUBIND_PROCESS or HWLOC_CPUBIND_THREAD to specify whether the query should be for the whole process (union of all CPUs on which all threads are running), or only the current thread. If the process is single-threaded, flags can be set to zero to let hwloc use whichever method is available on the underlying OS.

19.10.3.3 hwloc_get_proc_cpubind()

Get the current physical binding of process pid.

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms. As a special case on Linux, if a tid (thread ID) is supplied instead of a pid (process ID) and HWLOC_CPUB IND_THREAD is passed in flags, the binding for that specific thread is returned. On non-Linux systems, HWLOC_CPUBIND_THREAD can not be used in flags.

19.10 CPU binding 105

19.10.3.4 hwloc_get_proc_last_cpu_location()

Get the last physical CPU where a process ran.

The operating system may move some tasks from one processor to another at any time according to their binding, so this function may return something that is already outdated.

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.

As a special case on Linux, if a tid (thread ID) is supplied instead of a pid (process ID) and HWLOC_CPUB IND_THREAD is passed in flags, the last CPU location of that specific thread is returned.

On non-Linux systems, HWLOC_CPUBIND_THREAD can not be used in flags.

19.10.3.5 hwloc_get_thread_cpubind()

Get the current physical binding of thread tid.

Note

hwloc_thread_t is pthread_t on Unix platforms, and HANDLE on native Windows platforms. HWLOC_CPUBIND_PROCESS can not be used in flags.

19.10.3.6 hwloc_set_cpubind()

```
int hwloc_set_cpubind (
          hwloc_topology_t topology,
          hwloc_const_cpuset_t set,
          int flags )
```

Bind current process or thread on cpus given in physical bitmap set.

Returns

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

19.10.3.7 hwloc_set_proc_cpubind()

Bind a process pid on cpus given in physical bitmap set.

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.

As a special case on Linux, if a tid (thread ID) is supplied instead of a pid (process ID) and HWLOC_CPUB \(\text{IND_THREAD} \) is passed in flags, the binding is applied to that specific thread.

On non-Linux systems, HWLOC_CPUBIND_THREAD can not be used in flags.

19.10.3.8 hwloc_set_thread_cpubind()

Bind a thread thread on cpus given in physical bitmap set.

Note

hwloc_thread_t is pthread_t on Unix platforms, and HANDLE on native Windows platforms. HWLOC_CPUBIND_PROCESS can not be used in flags.

19.11 Memory binding 107

19.11 Memory binding

Enumerations

enum hwloc_membind_policy_t {
 HWLOC_MEMBIND_DEFAULT, HWLOC_MEMBIND_FIRSTTOUCH, HWLOC_MEMBIND_BIND, HWLO←
 C_MEMBIND_INTERLEAVE,
 HWLOC_MEMBIND_REPLICATE, HWLOC_MEMBIND_NEXTTOUCH, HWLOC_MEMBIND_MIXED }

 enum hwloc_membind_flags_t {
 HWLOC_MEMBIND_PROCESS, HWLOC_MEMBIND_THREAD, HWLOC_MEMBIND_STRICT, HWLOC←
 _MEMBIND_MIGRATE,
 HWLOC_MEMBIND_NOCPUBIND, HWLOC_MEMBIND_BYNODESET = (1<<5) }

Functions

- int hwloc_set_membind_nodeset (hwloc_topology_t topology, hwloc_const_nodeset_t nodeset, hwloc_
 membind policy t policy, int flags)
- int hwloc_set_membind (hwloc_topology_t topology, hwloc_const_bitmap_t set, hwloc_membind_policy_

 t policy, int flags)
- int hwloc_get_membind_nodeset (hwloc_topology_t topology, hwloc_nodeset_t nodeset, hwloc_membind
 —policy_t *policy, int flags)
- int hwloc_get_membind (hwloc_topology_t topology, hwloc_bitmap_t set, hwloc_membind_policy_t *policy, int flags)
- int hwloc_set_proc_membind_nodeset (hwloc_topology_t topology, hwloc_pid_t pid, hwloc_const_nodeset
 — t nodeset, hwloc_membind_policy_t policy, int flags)
- int hwloc_set_proc_membind (hwloc_topology_t topology, hwloc_pid_t pid, hwloc_const_bitmap_t set, hwloc_membind_policy_t policy, int flags)
- int hwloc_get_proc_membind_nodeset (hwloc_topology_t topology, hwloc_pid_t pid, hwloc_nodeset_t nodeset, hwloc_membind_policy_t *policy, int flags)
- int hwloc_get_proc_membind (hwloc_topology_t topology, hwloc_pid_t pid, hwloc_bitmap_t set, hwloc_
 membind_policy_t *policy, int flags)
- int hwloc_set_area_membind_nodeset (hwloc_topology_t topology, const void *addr, size_t len, hwloc_
 const nodeset t nodeset, hwloc membind policy t policy, int flags)
- int hwloc_set_area_membind (hwloc_topology_t topology, const void *addr, size_t len, hwloc_const_
 bitmap_t set, hwloc_membind_policy_t policy, int flags)
- int hwloc_get_area_membind_nodeset (hwloc_topology_t topology, const void *addr, size_t len, hwloc_
 nodeset_t nodeset, hwloc_membind_policy_t *policy, int flags)
- int hwloc_get_area_membind (hwloc_topology_t topology, const void *addr, size_t len, hwloc_bitmap_t set, hwloc_membind_policy_t *policy, int flags)
- int hwloc_get_area_memlocation (hwloc_topology_t topology, const void *addr, size_t len, hwloc_bitmap_t set, int flags)
- void * hwloc_alloc (hwloc_topology_t topology, size_t len)
- void * hwloc_alloc_membind_nodeset (hwloc_topology_t topology, size_t len, hwloc_const_nodeset_t nodeset, hwloc_membind_policy_t policy, int flags)
- void * hwloc_alloc_membind (hwloc_topology_t topology, size_t len, hwloc_const_bitmap_t set, hwloc_
 membind_policy_t policy, int flags)
- static void * hwloc_alloc_membind_policy_nodeset (hwloc_topology_t topology, size_t len, hwloc_const_
 —
 nodeset_t nodeset, hwloc_membind_policy_t policy, int flags)
- static void * hwloc_alloc_membind_policy (hwloc_topology_t topology, size_t len, hwloc_const_bitmap_t set, hwloc_membind_policy_t policy, int flags)
- int hwloc_free (hwloc_topology_t topology, void *addr, size_t len)

19.11.1 Detailed Description

Memory binding can be done three ways:

• explicit memory allocation thanks to <a href="https://www.number.numbe

- implicit memory binding through binding policy: hwloc_set_membind() and friends only define the current policy of the process, which will be applied to the subsequent calls to malloc() and friends.
- migration of existing memory ranges, thanks to <a href="https://hww.nemory.ne

Not all operating systems support all three ways. https://hwloc_topology_get_support() may be used to query about the actual memory binding support in the currently used operating system.

When the requested binding operation is not available and the HWLOC_MEMBIND_STRICT flag was passed, the function returns -1. errno will be set to ENOSYS when the system does support the specified action or policy (e.g., some systems only allow binding memory on a per-thread basis, whereas other systems only allow binding memory for all threads in a process). errno will be set to EXDEV when the requested set can not be enforced (e.g., some systems only allow binding memory to a single NUMA node).

If HWLOC_MEMBIND_STRICT was not passed, the function may fail as well, or the operating system may use a slightly different operation (with side-effects, smaller binding set, etc.) when the requested operation is not exactly supported.

The most portable form that should be preferred over the others whenever possible is as follows. It allocates some memory hopefully bound to the specified set. To do so, hwloc will possibly have to change the current memory binding policy in order to actually get the memory bound, if the OS does not provide any other way to simply allocate bound memory without changing the policy for all allocations. That is the difference with hwloc_alloc_membind(), which will never change the current memory binding policy.

Each hwloc memory binding function is available in two forms: one that takes a bitmap argument (a CPU set by default, or a NUMA memory node set if the flag HWLOC_MEMBIND_BYNODESET is specified), and another one (whose name ends with _nodeset) that always takes a NUMA memory node set. See Object Sets (hwloc_cpuset_t and hwloc_nodeset_t) and The bitmap API for a discussion of CPU sets and NUMA memory node sets. It is also possible to convert between CPU set and node set using hwloc_cpuset_to_nodeset() or hwloc_cpuset_from_chodeset().

Memory binding by CPU set cannot work for CPU-less NUMA memory nodes. Binding by nodeset should therefore be preferred whenever possible.

See also

Some example codes are available under doc/examples/ in the source tree.

Note

On some operating systems, memory binding affects the CPU binding; see HWLOC_MEMBIND_NOCPUB← IND

19.11 Memory binding

19.11.2 Enumeration Type Documentation

19.11.2.1 hwloc_membind_flags_t

enum hwloc_membind_flags_t

Memory binding flags.

These flags can be used to refine the binding policy. All flags can be logically OR'ed together with the exception of HWLOC_MEMBIND_PROCESS and HWLOC_MEMBIND_THREAD; these two flags are mutually exclusive.

Not all systems support all kinds of binding. hwloc_topology_get_support() may be used to query about the actual memory binding support in the currently used operating system. See the "Detailed Description" section of Memory binding for a description of errors that can occur.

Enumerator

HWLOC_MEMBIND_PROCESS	Set policy for all threads of the specified (possibly multithreaded) process. This flag is mutually exclusive with HWLOC_MEMBIND_THREAD.
HWLOC_MEMBIND_THREAD	Set policy for a specific thread of the current process. This flag is mutually exclusive with HWLOC_MEMBIND_PROCESS.
HWLOC_MEMBIND_STRICT	Request strict binding from the OS. The function will fail if the binding can not be guaranteed / completely enforced. This flag has slightly different meanings depending on which function it is used with.
HWLOC_MEMBIND_MIGRATE	Migrate existing allocated memory. If the memory cannot be migrated and the HWLOC_MEMBIND_STRICT flag is passed, an error will be returned.
HWLOC_MEMBIND_NOCPUBIND	Avoid any effect on CPU binding. On some operating systems, some underlying memory binding functions also bind the application to the corresponding CPU(s). Using this flag will cause hwloc to avoid using OS functions that could potentially affect CPU bindings. Note, however, that using NOCPUBIND may reduce hwloc's overall memory binding support. Specifically: some of hwloc's memory binding functions may fail with errno set to ENOSYS when used with NOCPUBIND.
HWLOC_MEMBIND_BYNODESET	Consider the bitmap argument as a nodeset. Functions whose name ends with _nodeset() take a nodeset argument. Other functions take a bitmap argument that is considered a nodeset if this flag is given, or a cpuset otherwise. Memory binding by CPU set cannot work for CPU-less NUMA memory nodes. Binding by nodeset should therefore be preferred whenever possible.

19.11.2.2 hwloc_membind_policy_t

 $\verb"enum hwloc_membind_policy_t"$

Memory binding policy.

These constants can be used to choose the binding policy. Only one policy can be used at a time (i.e., the values cannot be OR'ed together).

Not all systems support all kinds of binding. hwloc_topology_get_support() may be used to query about the actual memory binding policy support in the currently used operating system. See the "Detailed Description" section of Memory binding for a description of errors that can occur.

Enumerator

HWLOC_MEMBIND_DEFAULT	Reset the memory allocation policy to the system default. Depending on the operating system, this may correspond to HWLOC_MEMBIND_FIRSTTOUCH (Linux), or HWLOC_MEMBIND_BIND (AIX, HP-UX, OSF, Solaris, Windows). This policy is never returned by get membind functions when running on normal machines. It is only returned when binding hooks are empty because the topology was loaded from XML, or HWLOC_THISSYSTEM=0, etc.
HWLOC_MEMBIND_FIRSTTOUCH	Allocate memory but do not immediately bind it to a specific locality. Instead, each page in the allocation is bound only when it is first touched. Pages are individually bound to the local NUMA node of the first thread that touches it. If there is not enough memory on the node, allocation may be done in the specified nodes before allocating on other nodes.
HWLOC_MEMBIND_BIND	Allocate memory on the specified nodes.
HWLOC_MEMBIND_INTERLEAVE	Allocate memory on the given nodes in an interleaved / round-robin manner. The precise layout of the memory across multiple NUMA nodes is OS/system specific. Interleaving can be useful when threads distributed across the specified NUMA nodes will all be accessing the whole memory range concurrently, since the interleave will then balance the memory references.
HWLOC_MEMBIND_REPLICATE	Replicate memory on the given nodes; reads from this memory will attempt to be serviced from the NUMA node local to the reading thread. Replicating can be useful when multiple threads from the specified NUMA nodes will be sharing the same read-only data. This policy can only be used with existing memory allocations (i.e., the hwloc_set_*membind*() functions); it cannot be used with functions that allocate new memory (i.e., the hwloc_alloc*() functions).
HWLOC_MEMBIND_NEXTTOUCH	For each page bound with this policy, by next time it is touched (and next time only), it is moved from its current location to the local NUMA node of the thread where the memory reference occurred (if it needs to be moved at all).
HWLOC_MEMBIND_MIXED	Returned by get_membind() functions when multiple threads or parts of a memory area have differing memory binding policies.

19.11.3 Function Documentation

19.11.3.1 hwloc_alloc()

19.11 Memory binding 111

Allocate some memory.

This is equivalent to malloc(), except that it tries to allocate page-aligned memory from the OS.

Note

The allocated memory should be freed with <a href="https://hww.need.nih.gov/hww.need.nih.go

19.11.3.2 hwloc alloc membind()

Allocate some memory on NUMA memory nodes specified by set.

Returns

NULL with errno set to ENOSYS if the action is not supported and HWLOC_MEMBIND_STRICT is given NULL with errno set to EXDEV if the binding cannot be enforced and HWLOC_MEMBIND_STRICT is given NULL with errno set to ENOMEM if the memory allocation failed even before trying to bind.

If HWLOC_MEMBIND_BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

Note

19.11.3.3 hwloc_alloc_membind_nodeset()

Allocate some memory on NUMA memory nodes specified by nodeset.

Returns

NULL with errno set to ENOSYS if the action is not supported and HWLOC_MEMBIND_STRICT is given NULL with errno set to EXDEV if the binding cannot be enforced and HWLOC_MEMBIND_STRICT is given NULL with errno set to ENOMEM if the memory allocation failed even before trying to bind.

Note

The allocated memory should be freed with hwloc_free().

19.11.3.4 hwloc_alloc_membind_policy()

```
static void* hwloc_alloc_membind_policy (
    hwloc_topology_t topology,
    size_t len,
    hwloc_const_bitmap_t set,
    hwloc_membind_policy_t policy,
    int flags ) [inline], [static]
```

Allocate some memory on NUMA memory nodes specified by set.

This is similar to hwloc_alloc_membind_nodeset() except that it is allowed to change the current memory binding policy, thus providing more binding support, at the expense of changing the current state.

If HWLOC_MEMBIND_BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

19.11.3.5 hwloc_alloc_membind_policy_nodeset()

```
static void* hwloc_alloc_membind_policy_nodeset (
    hwloc_topology_t topology,
    size_t len,
    hwloc_const_nodeset_t nodeset,
    hwloc_membind_policy_t policy,
    int flags ) [inline], [static]
```

Allocate some memory on NUMA memory nodes specified by nodeset.

19.11.3.6 hwloc_free()

```
int hwloc_free (
          hwloc_topology_t topology,
          void * addr,
          size_t len )
```

Free memory that was previously allocated by hwloc_alloc() or hwloc_alloc_membind().

19.11 Memory binding 113

19.11.3.7 hwloc_get_area_membind()

```
int hwloc_get_area_membind (
    hwloc_topology_t topology,
    const void * addr,
    size_t len,
    hwloc_bitmap_t set,
    hwloc_membind_policy_t * policy,
    int flags )
```

Query the CPUs near the physical NUMA node(s) and binding policy of the memory identified by (addr, len).

This function has two output parameters: set and policy. The values returned in these parameters depend on both the flags passed in and the memory binding policies and nodesets of the pages in the address range.

If HWLOC_MEMBIND_STRICT is specified, the target pages are first checked to see if they all have the same memory binding policy and nodeset. If they do not, -1 is returned and errno is set to EXDEV. If they are identical across all pages, the set and policy are returned in set and policy, respectively.

If HWLOC_MEMBIND_STRICT is not specified, the union of all NUMA node(s) containing pages in the address range is calculated. If all pages in the target have the same policy, it is returned in policy. Otherwise, policy is set to HWLOC MEMBIND MIXED.

If HWLOC_MEMBIND_BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

If len is 0, -1 is returned and errno is set to EINVAL.

If any other flags are specified, -1 is returned and errno is set to EINVAL.

19.11.3.8 hwloc_get_area_membind_nodeset()

```
int hwloc_get_area_membind_nodeset (
    hwloc_topology_t topology,
    const void * addr,
    size_t len,
    hwloc_nodeset_t nodeset,
    hwloc_membind_policy_t * policy,
    int flags )
```

Query the physical NUMA node(s) and binding policy of the memory identified by (addr, len).

This function has two output parameters: nodeset and policy. The values returned in these parameters depend on both the flags passed in and the memory binding policies and nodesets of the pages in the address range.

If HWLOC_MEMBIND_STRICT is specified, the target pages are first checked to see if they all have the same memory binding policy and nodeset. If they do not, -1 is returned and errno is set to EXDEV. If they are identical across all pages, the nodeset and policy are returned in nodeset and policy, respectively.

If HWLOC_MEMBIND_STRICT is not specified, nodeset is set to the union of all NUMA node(s) containing pages in the address range. If all pages in the target have the same policy, it is returned in policy. Otherwise, policy is set to HWLOC_MEMBIND_MIXED.

If len is 0, -1 is returned and errno is set to EINVAL.

If any other flags are specified, -1 is returned and errno is set to EINVAL.

19.11.3.9 hwloc_get_area_memlocation()

```
int hwloc_get_area_memlocation (
    hwloc_topology_t topology,
    const void * addr,
    size_t len,
    hwloc_bitmap_t set,
    int flags )
```

Get the NUMA nodes where memory identified by (addr, len) is physically allocated.

Fills set according to the NUMA nodes where the memory area pages are physically allocated. If no page is actually allocated yet, set may be empty.

If pages spread to multiple nodes, it is not specified whether they spread equitably, or whether most of them are on a single node, etc.

The operating system may move memory pages from one processor to another at any time according to their binding, so this function may return something that is already outdated.

If HWLOC MEMBIND BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

If len is 0, set is emptied.

Flags are currently unused.

19.11.3.10 hwloc_get_membind()

Query the default memory binding policy and physical locality of the current process or thread.

This function has two output parameters: set and policy. The values returned in these parameters depend on both the flags passed in and the current memory binding policies and nodesets in the gueried target.

Passing the HWLOC_MEMBIND_PROCESS flag specifies that the query target is the current policies and nodesets for all the threads in the current process. Passing HWLOC_MEMBIND_THREAD specifies that the query target is the current policy and nodeset for only the thread invoking this function.

If neither of these flags are passed (which is the most portable method), the process is assumed to be single threaded. This allows hwloc to use either process-based OS functions or thread-based OS functions, depending on which are available.

HWLOC_MEMBIND_STRICT is only meaningful when HWLOC_MEMBIND_PROCESS is also specified. In this case, hwloc will check the default memory policies and nodesets for all threads in the process. If they are not identical, -1 is returned and errno is set to EXDEV. If they are identical, the values are returned in set and policy.

Otherwise, if HWLOC_MEMBIND_PROCESS is specified (and HWLOC_MEMBIND_STRICT is *not* specified), the default set from each thread is logically OR'ed together. If all threads' default policies are the same, policy is set to that policy. If they are different, policy is set to HWLOC_MEMBIND_MIXED.

In the HWLOC_MEMBIND_THREAD case (or when neither HWLOC_MEMBIND_PROCESS or HWLOC_MEM BIND_THREAD is specified), there is only one set and policy; they are returned in set and policy, respectively.

If HWLOC MEMBIND BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

If any other flags are specified, -1 is returned and errno is set to EINVAL.

19.11 Memory binding 115

19.11.3.11 hwloc_get_membind_nodeset()

Query the default memory binding policy and physical locality of the current process or thread.

This function has two output parameters: nodeset and policy. The values returned in these parameters depend on both the flags passed in and the current memory binding policies and nodesets in the queried target.

Passing the HWLOC_MEMBIND_PROCESS flag specifies that the query target is the current policies and nodesets for all the threads in the current process. Passing HWLOC_MEMBIND_THREAD specifies that the query target is the current policy and nodeset for only the thread invoking this function.

If neither of these flags are passed (which is the most portable method), the process is assumed to be single threaded. This allows hwloc to use either process-based OS functions or thread-based OS functions, depending on which are available.

HWLOC_MEMBIND_STRICT is only meaningful when HWLOC_MEMBIND_PROCESS is also specified. In this case, hwloc will check the default memory policies and nodesets for all threads in the process. If they are not identical, -1 is returned and errno is set to EXDEV. If they are identical, the values are returned in nodeset and policy.

Otherwise, if HWLOC_MEMBIND_PROCESS is specified (and HWLOC_MEMBIND_STRICT is *not* specified), nodeset is set to the logical OR of all threads' default nodeset. If all threads' default policies are the same, policy is set to that policy. If they are different, policy is set to HWLOC_MEMBIND_MIXED.

In the HWLOC_MEMBIND_THREAD case (or when neither HWLOC_MEMBIND_PROCESS or HWLOC_MEM BIND_THREAD is specified), there is only one nodeset and policy; they are returned in nodeset and policy, respectively.

If any other flags are specified, -1 is returned and errno is set to EINVAL.

19.11.3.12 hwloc_get_proc_membind()

Query the default memory binding policy and physical locality of the specified process.

This function has two output parameters: set and policy. The values returned in these parameters depend on both the flags passed in and the current memory binding policies and nodesets in the queried target.

Passing the HWLOC_MEMBIND_PROCESS flag specifies that the query target is the current policies and nodesets for all the threads in the specified process. If HWLOC_MEMBIND_PROCESS is not specified (which is the most portable method), the process is assumed to be single threaded. This allows hwloc to use either process-based OS functions or thread-based OS functions, depending on which are available.

Note that it does not make sense to pass HWLOC_MEMBIND_THREAD to this function.

If HWLOC_MEMBIND_STRICT is specified, hwloc will check the default memory policies and nodesets for all threads in the specified process. If they are not identical, -1 is returned and errno is set to EXDEV. If they are identical, the values are returned in set and policy.

Otherwise, set is set to the logical OR of all threads' default set. If all threads' default policies are the same, policy is set to that policy. If they are different, policy is set to HWLOC_MEMBIND_MIXED.

If HWLOC_MEMBIND_BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

If any other flags are specified, -1 is returned and errno is set to EINVAL.

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.

19.11.3.13 hwloc_get_proc_membind_nodeset()

Query the default memory binding policy and physical locality of the specified process.

This function has two output parameters: nodeset and policy. The values returned in these parameters depend on both the flags passed in and the current memory binding policies and nodesets in the queried target.

Passing the HWLOC_MEMBIND_PROCESS flag specifies that the query target is the current policies and nodesets for all the threads in the specified process. If HWLOC_MEMBIND_PROCESS is not specified (which is the most portable method), the process is assumed to be single threaded. This allows hwloc to use either process-based OS functions or thread-based OS functions, depending on which are available.

Note that it does not make sense to pass HWLOC_MEMBIND_THREAD to this function.

If HWLOC_MEMBIND_STRICT is specified, hwloc will check the default memory policies and nodesets for all threads in the specified process. If they are not identical, -1 is returned and errno is set to EXDEV. If they are identical, the values are returned in nodeset and policy.

Otherwise, nodeset is set to the logical OR of all threads' default nodeset. If all threads' default policies are the same, policy is set to that policy. If they are different, policy is set to HWLOC MEMBIND MIXED.

If any other flags are specified, -1 is returned and errno is set to EINVAL.

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.

19.11.3.14 hwloc_set_area_membind()

```
int hwloc_set_area_membind (
    hwloc_topology_t topology,
    const void * addr,
    size_t len,
    hwloc_const_bitmap_t set,
    hwloc_membind_policy_t policy,
    int flags )
```

Bind the already-allocated memory identified by (addr, len) to the NUMA node(s) specified by set.

If HWLOC_MEMBIND_BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

Returns

0 if len is 0.

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

19.11 Memory binding 117

19.11.3.15 hwloc_set_area_membind_nodeset()

```
int hwloc_set_area_membind_nodeset (
    hwloc_topology_t topology,
    const void * addr,
    size_t len,
    hwloc_const_nodeset_t nodeset,
    hwloc_membind_policy_t policy,
    int flags )
```

Bind the already-allocated memory identified by (addr, len) to the NUMA node(s) specified by nodeset.

Returns

0 if len is 0.

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

19.11.3.16 hwloc_set_membind()

Set the default memory binding policy of the current process or thread to prefer the NUMA node(s) specified by set.

If neither HWLOC_MEMBIND_PROCESS nor HWLOC_MEMBIND_THREAD is specified, the current process is assumed to be single-threaded. This is the most portable form as it permits hwloc to use either process-based OS functions or thread-based OS functions, depending on which are available.

If HWLOC MEMBIND BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

Returns

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

19.11.3.17 hwloc_set_membind_nodeset()

Set the default memory binding policy of the current process or thread to prefer the NUMA node(s) specified by nodeset.

If neither HWLOC_MEMBIND_PROCESS nor HWLOC_MEMBIND_THREAD is specified, the current process is assumed to be single-threaded. This is the most portable form as it permits hwloc to use either process-based OS functions or thread-based OS functions, depending on which are available.

Returns

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

19.11.3.18 hwloc_set_proc_membind()

Set the default memory binding policy of the specified process to prefer the NUMA node(s) specified by set.

If HWLOC MEMBIND BYNODESET is specified, set is considered a nodeset. Otherwise it's a cpuset.

Returns

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.

19.11.3.19 hwloc_set_proc_membind_nodeset()

```
int hwloc_set_proc_membind_nodeset (
    hwloc_topology_t topology,
    hwloc_pid_t pid,
    hwloc_const_nodeset_t nodeset,
    hwloc_membind_policy_t policy,
    int flags )
```

Set the default memory binding policy of the specified process to prefer the NUMA node(s) specified by nodeset.

Returns

- -1 with errno set to ENOSYS if the action is not supported
- -1 with errno set to EXDEV if the binding cannot be enforced

Note

hwloc_pid_t is pid_t on Unix platforms, and HANDLE on native Windows platforms.

19.12 Modifying a loaded Topology

Enumerations

• enum hwloc_restrict_flags_e { HWLOC_RESTRICT_FLAG_ADAPT_DISTANCES, HWLOC_RESTRICT_← FLAG_ADAPT_MISC, HWLOC_RESTRICT_FLAG_ADAPT_IO }

Functions

- hwloc_obj_t hwloc_topology_insert_misc_object_by_cpuset (hwloc_topology_t topology, hwloc_const_
 cpuset_t cpuset, const char *name)
- hwloc_obj_t hwloc_topology_insert_misc_object_by_parent (hwloc_topology_t topology, hwloc_obj_t parent, const char *name)
- int hwloc_topology_restrict (hwloc_topology_t restrict topology, hwloc_const_cpuset_t cpuset, unsigned long flags)

19.12.1 Detailed Description

19.12.2 Enumeration Type Documentation

19.12.2.1 hwloc_restrict_flags_e

enum hwloc_restrict_flags_e

Flags to be given to hwloc_topology_restrict().

Enumerator

HWLOC_RESTRICT_FLAG_ADAPT_DISTANCES	Adapt distance matrices according to objects being removed during restriction. If this flag is not set, distance matrices are removed.
HWLOC_RESTRICT_FLAG_ADAPT_MISC	Move Misc objects to ancestors if their parents are removed during restriction. If this flag is not set, Misc objects are removed when their parents are removed.
HWLOC_RESTRICT_FLAG_ADAPT_IO	Move I/O objects to ancestors if their parents are removed during restriction. If this flag is not set, I/O devices and bridges are removed when their parents are removed.

19.12.3 Function Documentation

19.12.3.1 hwloc_topology_insert_misc_object_by_cpuset()

Add a MISC object to the topology.

A new MISC object will be created and inserted into the topology at the position given by bitmap cpuset. This offers a way to add new intermediate levels to the topology hierarchy.

cpuset and name will be copied to setup the new object attributes.

Returns

the newly-created object.

NULL if the insertion conflicts with the existing topology tree.

Note

If name contains some non-printable characters, they will be dropped when exporting to XML, see hwloc_
topology_export_xml().

19.12.3.2 hwloc_topology_insert_misc_object_by_parent()

```
hwloc_obj_t hwloc_topology_insert_misc_object_by_parent (
          hwloc_topology_t topology,
          hwloc_obj_t parent,
          const char * name )
```

Add a MISC object as a leaf of the topology.

A new MISC object will be created and inserted into the topology at the position given by parent. It is appended to the list of existing children, without ever adding any intermediate hierarchy level. This is useful for annotating the topology without actually changing the hierarchy.

name will be copied to the setup the new object attributes. However, the new leaf object will not have any cpuset.

Returns

the newly-created object

Note

If name contains some non-printable characters, they will be dropped when exporting to XML, see hwloc_
topology_export_xml().

19.12.3.3 hwloc_topology_restrict()

Restrict the topology to the given CPU set.

Topology topology is modified so as to remove all objects that are not included (or partially included) in the CPU set cpuset. All objects CPU and node sets are restricted accordingly.

flags is a OR'ed set of hwloc_restrict_flags_e.

Note

This call may not be reverted by restricting back to a larger cpuset. Once dropped during restriction, objects may not be brought back, except by loading another topology with hwloc topology load().

Returns

0 on success.

- -1 with errno set to EINVAL if the input cpuset is invalid. The topology is not modified in this case.
- -1 with errno set to ENOMEM on failure to allocate internal data. The topology is reinitialized in this case. It should be either destroyed with hwloc_topology_destroy() or configured and loaded again.

19.13 Building Custom Topologies

Functions

- int hwloc_custom_insert_topology (hwloc_topology_t newtopology, hwloc_obj_t newparent, hwloc_← topology_t oldtopology, hwloc_obj_t oldroot)
- hwloc_obj_t hwloc_custom_insert_group_object_by_parent (hwloc_topology_t topology, hwloc_obj_t parent, int groupdepth)

19.13.1 Detailed Description

A custom topology may be initialized by calling hwloc_topology_set_custom() after hwloc_topology_init(). It may then be modified by inserting objects or entire topologies. Once done assembling, hwloc_topology_load() should be invoked as usual to finalize the topology.

19.13.2 Function Documentation

19.13.2.1 hwloc_custom_insert_group_object_by_parent()

Insert a new group object inside a custom topology.

An object with type HWLOC_OBJ_GROUP is inserted as a new child of object parent.

groupdepth is the depth attribute to be given to the new object. It may for instance be 0 for top-level groups, 1 for their children, and so on.

The custom topology newtopology must have been prepared with $hwloc_topology_set_custom()$ and not loaded with $hwloc_topology_load()$ yet.

parent may be either the root of topology or an object that was added earlier through hwloc_custom_insert _group_object_by_parent().

Note

The cpuset and nodeset of the new group object are NULL because these sets are meaningless when assembling multiple topologies.

The cpuset and nodeset of the parent object are not modified.

19.13.2.2 hwloc_custom_insert_topology()

Insert an existing topology inside a custom topology.

Duplicate the existing topology oldtopology inside a new custom topology newtopology as a leaf of object newparent.

If oldroot is not NULL, duplicate oldroot and all its children instead of the entire oldtopology. Passing the root object of oldtopology in oldroot is equivalent to passing NULL.

The custom topology <code>newtopology</code> must have been prepared with <code>hwloc_topology_set_custom()</code> and not loaded with <code>hwloc_topology_load()</code> yet.

newparent may be either the root of newtopology or an object that was added through hwloc_custom_cinsert_group_object_by_parent().

Note

The cpuset and nodeset of the newparent object are not modified based on the contents of oldtopology.

19.14 Exporting Topologies to XML

Functions

- int hwloc_topology_export_xml (hwloc_topology_t topology, const char *xmlpath)
- int hwloc_topology_export_xmlbuffer (hwloc_topology_t topology, char **xmlbuffer, int *buflen)
- void hwloc_free_xmlbuffer (hwloc_topology_t topology, char *xmlbuffer)
- void hwloc_topology_set_userdata_export_callback (hwloc_topology_t topology, void(*export_cb)(void *reserved, hwloc_topology_t topology, hwloc_obj_t obj))
- int hwloc_export_obj_userdata (void *reserved, hwloc_topology_t topology, hwloc_obj_t obj, const char *name, const void *buffer, size_t length)
- int hwloc_export_obj_userdata_base64 (void *reserved, hwloc_topology_t topology, hwloc_obj_t obj, const char *name, const void *buffer, size_t length)
- void hwloc_topology_set_userdata_import_callback (hwloc_topology_t topology, void(*import_cb)(hwloc_
 topology t topology, hwloc_obj_t obj, const char *name, const void *buffer, size_t length))

19.14.1 Detailed Description

19.14.2 Function Documentation

19.14.2.1 hwloc_export_obj_userdata()

Export some object userdata to XML.

This function may only be called from within the export() callback passed to hwloc_topology_set_userdata_export ← _callback(). It may be invoked one of multiple times to export some userdata to XML. The buffer content of length length is stored with optional name name.

When importing this XML file, the import() callback (if set) will be called exactly as many times as hwloc_export obj_userdata() was called during export(). It will receive the corresponding name, buffer and length arguments.

reserved, topology and obj must be the first three parameters that were given to the export callback.

Only printable characters may be exported to XML string attributes. If a non-printable character is passed in name or buffer, the function returns -1 with errno set to EINVAL.

If exporting binary data, the application should first encode into printable characters only (or use hwloc_export_cobj_userdata_base64()). It should also take care of portability issues if the export may be reimported on a different architecture.

19.14.2.2 hwloc_export_obj_userdata_base64()

Encode and export some object userdata to XML.

This function is similar to hwloc_export_obj_userdata() but it encodes the input buffer into printable characters before exporting. On import, decoding is automatically performed before the data is given to the import() callback if any.

This function may only be called from within the export() callback passed to hwloc_topology_set_userdata_export ← _callback().

The function does not take care of portability issues if the export may be reimported on a different architecture.

19.14.2.3 hwloc_free_xmlbuffer()

Free a buffer allocated by hwloc_topology_export_xmlbuffer()

19.14.2.4 hwloc_topology_export_xml()

Export the topology into an XML file.

This file may be loaded later through hwloc topology set xml().

Returns

-1 if a failure occured.

Note

See also hwloc_topology_set_userdata_export_callback() for exporting application-specific object userdata. The topology-specific userdata pointer is ignored when exporting to XML.

Only printable characters may be exported to XML string attributes. Any other character, especially any non-ASCII character, will be silently dropped.

If name is "-", the XML output is sent to the standard output.

19.14.2.5 hwloc_topology_export_xmlbuffer()

Export the topology into a newly-allocated XML memory buffer.

xmlbuffer is allocated by the callee and should be freed with hwloc free xmlbuffer() later in the caller.

This memory buffer may be loaded later through hwloc topology set xmlbuffer().

Returns

-1 if a failure occured.

Note

See also hwloc_topology_set_userdata_export_callback() for exporting application-specific object userdata. The topology-specific userdata pointer is ignored when exporting to XML.

Only printable characters may be exported to XML string attributes. Any other character, especially any non-ASCII character, will be silently dropped.

19.14.2.6 hwloc_topology_set_userdata_export_callback()

Set the application-specific callback for exporting object userdata.

The object userdata pointer is not exported to XML by default because hwloc does not know what it contains.

This function lets applications set $export_cb$ to a callback function that converts this opaque userdata into an exportable string.

export_cb is invoked during XML export for each object whose userdata pointer is not NULL. The callback should use hwloc_export_obj_userdata() or hwloc_export_obj_userdata_base64() to actually export something to XML (possibly multiple times per object).

export_cb may be set to NULL if userdata should not be exported to XML.

Note

The topology-specific userdata pointer is ignored when exporting to XML.

19.14.2.7 hwloc_topology_set_userdata_import_callback()

Set the application-specific callback for importing userdata.

On XML import, userdata is ignored by default because hwloc does not know how to store it in memory.

This function lets applications set import_cb to a callback function that will get the XML-stored userdata and store it in the object as expected by the application.

import_cb is called during hwloc_topology_load() as many times as hwloc_export_obj_userdata() was called during export. The topology is not entirely setup yet. Object attributes are ready to consult, but links between objects are not.

import_cb may be NULL if userdata should be ignored during import.

Note

buffer contains length characters followed by a null byte ('\0'). This function should be called before hwloc_topology_load().

The topology-specific userdata pointer is ignored when importing from XML.

19.15 Exporting Topologies to Synthetic

Enumerations

enum hwloc_topology_export_synthetic_flags_e { HWLOC_TOPOLOGY_EXPORT_SYNTHETIC_FLAG_←
NO_EXTENDED_TYPES, HWLOC_TOPOLOGY_EXPORT_SYNTHETIC_FLAG_NO_ATTRS }

Functions

 int hwloc_topology_export_synthetic (hwloc_topology_t topology, char *buffer, size_t buflen, unsigned long flags)

19.15.1 Detailed Description

19.15.2 Enumeration Type Documentation

```
19.15.2.1 hwloc_topology_export_synthetic_flags_e
```

```
enum hwloc_topology_export_synthetic_flags_e
```

Flags for exporting synthetic topologies.

Flags to be given as a OR'ed set to hwloc_topology_export_synthetic().

Enumerator

HWLOC_TOPOLOGY_EXPORT_SYNTHETIC_FL←	Export extended types such as L2dcache as basic
AG_NO_EXTENDED_TYPES	types such as Cache. This is required if loading the
	synthetic description with hwloc < 1.9.
HWLOC_TOPOLOGY_EXPORT_SYNTHETIC_FL↔	Do not export level attributes. Ignore level attributes
AG_NO_ATTRS	such as memory/cache sizes or PU indexes. This is
	required if loading the synthetic description with hwloc
	< 1.10.

19.15.3 Function Documentation

19.15.3.1 hwloc_topology_export_synthetic()

```
size_t buflen,
unsigned long flags )
```

Export the topology as a synthetic string.

At most buflen characters will be written in buffer, including the terminating \0.

This exported string may be given back to hwloc_topology_set_synthetic().

 ${\tt flags} \ is \ a \ OR'ed \ set \ of \ hwloc_topology_export_synthetic_flags_e.$

Returns

The number of characters that were written, not including the terminating \0.

-1 if the topology could not be exported, for instance if it is not symmetric.

Note

A 1024-byte buffer should be large enough for exporting topologies in the vast majority of cases.

19.16 Finding Objects inside a CPU set

Functions

- static hwloc_obj_t hwloc_get_first_largest_obj_inside_cpuset (hwloc_topology_t topology, hwloc_const_← cpuset t set)
- int hwloc_get_largest_objs_inside_cpuset (hwloc_topology_t topology, hwloc_const_cpuset_t set, hwloc_
 obj_t *restrict objs, int max)
- static hwloc_obj_t hwloc_get_next_obj_inside_cpuset_by_depth (hwloc_topology_t topology, hwloc_const
 —cpuset_t set, unsigned depth, hwloc_obj_t prev)
- static hwloc_obj_t hwloc_get_next_obj_inside_cpuset_by_type (hwloc_topology_t topology, hwloc_const_
 cpuset_t set, hwloc_obj_type_t type, hwloc_obj_t prev)
- static hwloc_obj_t hwloc_get_obj_inside_cpuset_by_depth (hwloc_topology_t topology, hwloc_const_
 cpuset_t set, unsigned depth, unsigned idx)
- static hwloc_obj_t hwloc_get_obj_inside_cpuset_by_type (hwloc_topology_t topology, hwloc_const_const_t set, hwloc_obj_type_t type, unsigned idx)
- static unsigned hwloc_get_nbobjs_inside_cpuset_by_depth (hwloc_topology_t topology, hwloc_const_
 cpuset_t set, unsigned depth)
- static int hwloc_get_nbobjs_inside_cpuset_by_type (hwloc_topology_t topology, hwloc_const_cpuset_t set, hwloc_obj_type_t type)
- static int hwloc_get_obj_index_inside_cpuset (hwloc_topology_t topology, hwloc_const_cpuset_t set, hwloc obj t obj)

19.16.1 Detailed Description

19.16.2 Function Documentation

19.16.2.1 hwloc_get_first_largest_obj_inside_cpuset()

Get the first largest object included in the given cpuset set.

Returns

the first object that is included in set and whose parent is not.

This is convenient for iterating over all largest objects within a CPU set by doing a loop getting the first largest object and clearing its CPU set from the remaining CPU set.

Note

This function cannot work if the root object does not have a CPU set, e.g. if the topology is made of different machines.

19.16.2.2 hwloc_get_largest_objs_inside_cpuset()

Get the set of largest objects covering exactly a given cpuset set.

Returns

the number of objects returned in objs.

Note

This function cannot work if the root object does not have a CPU set, e.g. if the topology is made of different machines.

19.16.2.3 hwloc_get_nbobjs_inside_cpuset_by_depth()

Return the number of objects at depth depth included in CPU set set.

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set). This function cannot work if objects at the given depth do not have CPU sets or if the topology is made of different machines.

19.16.2.4 hwloc_get_nbobjs_inside_cpuset_by_type()

Return the number of objects of type type included in CPU set set.

If no object for that type exists inside CPU set set, 0 is returned. If there are several levels with objects of that type inside CPU set set, -1 is returned.

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set). This function cannot work if objects of the given type do not have CPU sets or if the topology is made of different machines.

19.16.2.5 hwloc_get_next_obj_inside_cpuset_by_depth()

```
static hwloc_obj_t hwloc_get_next_obj_inside_cpuset_by_depth (
          hwloc_topology_t topology,
          hwloc_const_cpuset_t set,
          unsigned depth,
          hwloc_obj_t prev ) [inline], [static]
```

Return the next object at depth depth included in CPU set set.

If prev is NULL, return the first object at depth depth included in set. The next invokation should pass the previous return value in prev so as to obtain the next object in set.

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set). This function cannot work if objects at the given depth do not have CPU sets or if the topology is made of different machines.

19.16.2.6 hwloc_get_next_obj_inside_cpuset_by_type()

Return the next object of type type included in CPU set set.

If there are multiple or no depth for given type, return NULL and let the caller fallback to $\texttt{hwloc_get_next_obj_} \leftarrow \texttt{inside_cpuset_by_depth()}.$

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set). This function cannot work if objects of the given type do not have CPU sets or if the topology is made of different machines.

19.16.2.7 hwloc_get_obj_index_inside_cpuset()

Return the logical index among the objects included in CPU set set.

Consult all objects in the same level as obj and inside CPU set set in the logical order, and return the index of obj within them. If set covers the entire topology, this is the logical index of obj. Otherwise, this is similar to a logical index within the part of the topology defined by CPU set set.

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set).

19.16.2.8 hwloc_get_obj_inside_cpuset_by_depth()

```
static hwloc_obj_t hwloc_get_obj_inside_cpuset_by_depth (
    hwloc_topology_t topology,
    hwloc_const_cpuset_t set,
    unsigned depth,
    unsigned idx ) [inline], [static]
```

Return the (logically) idx -th object at depth depth included in CPU set set.

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set). This function cannot work if objects at the given depth do not have CPU sets or if the topology is made of different machines.

19.16.2.9 hwloc_get_obj_inside_cpuset_by_type()

Return the idx -th object of type type included in CPU set set.

If there are multiple or no depth for given type, return \mathtt{NULL} and let the caller fallback to $\mathsf{hwloc_get_obj_inside_} \leftarrow \mathsf{cpuset_by_depth}().$

Note

Objects with empty CPU sets are ignored (otherwise they would be considered included in any given set). This function cannot work if objects of the given type do not have CPU sets or if the topology is made of different machines.

19.17 Finding Objects covering at least CPU set

Functions

- static hwloc_obj_t hwloc_get_child_covering_cpuset (hwloc_topology_t topology, hwloc_const_cpuset_t set, hwloc_obj_t parent)
- static hwloc_obj_t hwloc_get_obj_covering_cpuset (hwloc_topology_t topology, hwloc_const_cpuset_t set)
- static hwloc_obj_t hwloc_get_next_obj_covering_cpuset_by_depth (hwloc_topology_t topology, hwloc_
 const_cpuset_t set, unsigned depth, hwloc_obj_t prev)
- static hwloc_obj_t hwloc_get_next_obj_covering_cpuset_by_type (hwloc_topology_t topology, hwloc_
 const_cpuset_t set, hwloc_obj_type_t type, hwloc_obj_t prev)

19.17.1 Detailed Description

19.17.2 Function Documentation

19.17.2.1 hwloc get_child_covering_cpuset()

Get the child covering at least CPU set set.

Returns

NULL if no child matches or if set is empty.

Note

This function cannot work if parent does not have a CPU set.

19.17.2.2 hwloc_get_next_obj_covering_cpuset_by_depth()

Iterate through same-depth objects covering at least CPU set \mathtt{set} .

If object prev is NULL, return the first object at depth depth covering at least part of CPU set set. The next invokation should pass the previous return value in prev so as to obtain the next object covering at least another part of set.

Note

This function cannot work if objects at the given depth do not have CPU sets or if the topology is made of different machines.

19.17.2.3 hwloc_get_next_obj_covering_cpuset_by_type()

Iterate through same-type objects covering at least CPU set set.

If object prev is NULL, return the first object of type type covering at least part of CPU set set. The next invokation should pass the previous return value in prev so as to obtain the next object of type type covering at least another part of set.

If there are no or multiple depths for type type, NULL is returned. The caller may fallback to $hwloc_get_next_{\leftarrow}$ obj_covering_cpuset_by_depth() for each depth.

Note

This function cannot work if objects of the given type do not have CPU sets or if the topology is made of different machines.

19.17.2.4 hwloc_get_obj_covering_cpuset()

Get the lowest object covering at least CPU set set.

Returns

NULL if no object matches or if set is empty.

Note

This function cannot work if the root object does not have a CPU set, e.g. if the topology is made of different machines.

19.18 Looking at Ancestor and Child Objects

Functions

- static hwloc_obj_t hwloc_get_ancestor_obj_by_depth (hwloc_topology_t topology, unsigned depth, hwloc
 _obj_t obj)
- static hwloc_obj_t hwloc_get_ancestor_obj_by_type (hwloc_topology_t topology, hwloc_obj_type_t type, hwloc_obj_t obj)
- static hwloc_obj_t hwloc_get_common_ancestor_obj (hwloc_topology_t topology, hwloc_obj_t obj1, hwloc
 obj_t obj2)
- static int hwloc_obj_is_in_subtree (hwloc_topology_t topology, hwloc_obj_t obj, hwloc_obj_t subtree_root)
- static hwloc_obj_t hwloc_get_next_child (hwloc_topology_t topology, hwloc_obj_t parent, hwloc_obj_t prev)

19.18.1 Detailed Description

Be sure to see the figure in Terms and Definitions that shows a complete topology tree, including depths, child/sibling/cousin relationships, and an example of an asymmetric topology where one package has fewer caches than its peers.

19.18.2 Function Documentation

```
19.18.2.1 hwloc_get_ancestor_obj_by_depth()
```

Returns the ancestor object of obj at depth depth.

```
19.18.2.2 hwloc_get_ancestor_obj_by_type()
```

Returns the ancestor object of obj with type type.

19.18.2.3 hwloc_get_common_ancestor_obj()

Returns the common parent object to objects obj1 and obj2.

19.18.2.4 hwloc_get_next_child()

Return the next child.

If prev is NULL, return the first child.

19.18.2.5 hwloc_obj_is_in_subtree()

Returns true if obj is inside the subtree beginning with ancestor object subtree_root.

Note

This function assumes that both obj and subtree_root have a cpuset.

19.19 Looking at Cache Objects

Functions

- static int hwloc_get_cache_type_depth (hwloc_topology_t topology, unsigned cachelevel, hwloc_obj_
 cache_type_t cachetype)
- static hwloc_obj_t hwloc_get_cache_covering_cpuset (hwloc_topology_t topology, hwloc_const_cpuset_
 t set)
- static hwloc_obj_t hwloc_get_shared_cache_covering_obj (hwloc_topology_t topology, hwloc_obj_t obj)

19.19.1 Detailed Description

19.19.2 Function Documentation

19.19.2.1 hwloc_get_cache_covering_cpuset()

Get the first cache covering a cpuset set.

Returns

NULL if no cache matches.

Note

This function cannot work if the root object does not have a CPU set, e.g. if the topology is made of different machines.

19.19.2.2 hwloc_get_cache_type_depth()

Find the depth of cache objects matching cache depth and type.

Return the depth of the topology level that contains cache objects whose attributes match cachedepth and cachetype. This function intends to disambiguate the case where hwloc_get_type_depth() returns HWLOC_T \leftarrow YPE_DEPTH_MULTIPLE.

If no cache level matches, HWLOC TYPE DEPTH UNKNOWN is returned.

If cachetype is HWLOC_OBJ_CACHE_UNIFIED, the depth of the unique matching unified cache level is returned.

If cachetype is HWLOC_OBJ_CACHE_DATA or HWLOC_OBJ_CACHE_INSTRUCTION, either a matching cache, or a unified cache is returned.

If cachetype is -1, it is ignored and multiple levels may match. The function returns either the depth of a uniquely matching level or HWLOC_TYPE_DEPTH_MULTIPLE.

19.19.2.3 hwloc_get_shared_cache_covering_obj()

Get the first cache shared between an object and somebody else.

Returns

 \mathtt{NULL} if no cache matches or if an invalid object is given.

19.20 Finding objects, miscellaneous helpers

Functions

- static hwloc_obj_t hwloc_get_pu_obj_by_os_index (hwloc_topology_t topology, unsigned os_index)
- static hwloc_obj_t hwloc_get_numanode_obj_by_os_index (hwloc_topology_t topology, unsigned os_index)
- unsigned hwloc_get_closest_objs (hwloc_topology_t topology, hwloc_obj_t src, hwloc_obj_t *restrict objs, unsigned max)
- static hwloc_obj_t hwloc_get_obj_below_by_type (hwloc_topology_t topology, hwloc_obj_type_t type1, unsigned idx1, hwloc_obj_type_t type2, unsigned idx2)
- static hwloc_obj_t hwloc_get_obj_below_array_by_type (hwloc_topology_t topology, int nr, hwloc_obj_type
 _t *typev, unsigned *idxv)

19.20.1 Detailed Description

Be sure to see the figure in Terms and Definitions that shows a complete topology tree, including depths, child/sibling/cousin relationships, and an example of an asymmetric topology where one package has fewer caches than its peers.

19.20.2 Function Documentation

19.20.2.1 hwloc_get_closest_objs()

Do a depth-first traversal of the topology to find and sort.

all objects that are at the same depth than src. Report in objs up to max physically closest ones to src.

Returns

```
the number of objects returned in objs. 0 if src is an I/O object.
```

Note

This function requires the src object to have a CPU set.

19.20.2.2 hwloc_get_numanode_obj_by_os_index()

Returns the object of type HWLOC_OBJ_NUMANODE with os_index.

This function is useful for converting a nodeset into the NUMA node objects it contains. When retrieving the current binding (e.g. with hwloc_get_membind_nodeset()), one may iterate over the bits of the resulting nodeset with hwloc_bitmap_foreach_begin(), and find the corresponding NUMA nodes with this function.

19.20.2.3 hwloc_get_obj_below_array_by_type()

Find an object below a chain of objects specified by types and indexes.

This is a generalized version of hwloc_get_obj_below_by_type().

Arrays typev and idxv must contain nr types and indexes.

Start from the top system object and walk the arrays typev and idxv. For each type and logical index couple in the arrays, look under the previously found object to find the index-th object of the given type. Indexes are specified within the parent, not withing the entire system.

For instance, if nr is 3, typev contains NODE, PACKAGE and CORE, and idxv contains 0, 1 and 2, return the third core object below the second package below the first NUMA node.

Note

This function requires all these objects and the root object to have a CPU set.

19.20.2.4 hwloc_get_obj_below_by_type()

```
static hwloc_obj_t hwloc_get_obj_below_by_type (
    hwloc_topology_t topology,
    hwloc_obj_type_t type1,
    unsigned idx1,
    hwloc_obj_type_t type2,
    unsigned idx2 ) [inline], [static]
```

Find an object below another object, both specified by types and indexes.

Start from the top system object and find object of type type1 and logical index idx1. Then look below this object and find another object of type type2 and logical index idx2. Indexes are specified within the parent, not withing the entire system.

For instance, if type1 is PACKAGE, idx1 is 2, type2 is CORE and idx2 is 3, return the fourth core object below the third package.

Note

This function requires these objects to have a CPU set.

19.20.2.5 hwloc_get_pu_obj_by_os_index()

Returns the object of type HWLOC_OBJ_PU with os_index.

This function is useful for converting a CPU set into the PU objects it contains. When retrieving the current binding (e.g. with hwloc_get_cpubind()), one may iterate over the bits of the resulting CPU set with hwloc_bitmap_foreach← _begin(), and find the corresponding PUs with this function.

19.21 Distributing items over a topology

Enumerations

enum hwloc_distrib_flags_e { HWLOC_DISTRIB_FLAG_REVERSE }

Functions

static int hwloc_distrib (hwloc_topology_t topology, hwloc_obj_t *roots, unsigned n_roots, hwloc_cpuset_t *set, unsigned n, unsigned until, unsigned long flags)

19.21.1 Detailed Description

19.21.2 Enumeration Type Documentation

```
19.21.2.1 hwloc_distrib_flags_e
```

```
enum hwloc_distrib_flags_e
```

Flags to be given to hwloc_distrib().

Enumerator

```
HWLOC_DISTRIB_FLAG_REVERSE  Distrib in reverse order, starting from the last objects.
```

19.21.3 Function Documentation

19.21.3.1 hwloc_distrib()

Distribute n items over the topology under roots.

Array set will be filled with n cpusets recursively distributed linearly over the topology under objects roots, down to depth until (which can be INT_MAX to distribute down to the finest level).

n_roots is usually 1 and roots only contains the topology root object so as to distribute over the entire topology.

This is typically useful when an application wants to distribute n threads over a machine, giving each of them as much private cache as possible and keeping them locally in number order.

The caller may typically want to also call hwloc_bitmap_singlify() before binding a thread so that it does not move at all.

flags should be 0 or a OR'ed set of hwloc_distrib_flags_e.

Note

This function requires the roots objects to have a CPU set.

This function replaces the now deprecated hwloc_distribute() and hwloc_distributev() functions.

19.22 CPU and node sets of entire topologies

Functions

- static hwloc_const_cpuset_t hwloc_topology_get_complete_cpuset (hwloc_topology_t topology)
- static hwloc_const_cpuset_t hwloc_topology_get_topology_cpuset (hwloc_topology_t topology)
- static hwloc_const_cpuset_t hwloc_topology_get_online_cpuset (hwloc_topology_t topology)
- static hwloc_const_cpuset_t hwloc_topology_get_allowed_cpuset (hwloc_topology_t topology)
- static hwloc_const_nodeset_t hwloc_topology_get_complete_nodeset (hwloc_topology_t topology)
- static hwloc_const_nodeset_t hwloc_topology_get_topology_nodeset (hwloc_topology_t topology)
- static hwloc const nodeset thwloc topology get allowed nodeset (hwloc topology)

19.22.1 Detailed Description

19.22.2 Function Documentation

19.22.2.1 hwloc_topology_get_allowed_cpuset()

Get allowed CPU set.

Returns

the CPU set of allowed logical processors of the system. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned cpuset is not newly allocated and should thus not be changed or freed, hwloc_bitmap_dup() must be used to obtain a local copy.

19.22.2.2 hwloc_topology_get_allowed_nodeset()

Get allowed node set.

Returns

the node set of allowed memory of the system. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned nodeset is not newly allocated and should thus not be changed or freed, hwloc_bitmap_dup() must be used to obtain a local copy.

19.22.2.3 hwloc_topology_get_complete_cpuset()

Get complete CPU set.

Returns

the complete CPU set of logical processors of the system. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned cpuset is not newly allocated and should thus not be changed or freed; hwloc_bitmap_dup() must be used to obtain a local copy.

19.22.2.4 hwloc_topology_get_complete_nodeset()

Get complete node set.

Returns

the complete node set of memory of the system. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned nodeset is not newly allocated and should thus not be changed or freed; hwloc_bitmap_dup() must be used to obtain a local copy.

19.22.2.5 hwloc_topology_get_online_cpuset()

Get online CPU set.

Returns

the CPU set of online logical processors of the system. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned cpuset is not newly allocated and should thus not be changed or freed; hwloc_bitmap_dup() must be used to obtain a local copy.

19.22.2.6 hwloc_topology_get_topology_cpuset()

Get topology CPU set.

Returns

the CPU set of logical processors of the system for which hwloc provides topology information. This is equivalent to the cpuset of the system object. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned cpuset is not newly allocated and should thus not be changed or freed; hwloc_bitmap_dup() must be used to obtain a local copy.

19.22.2.7 hwloc_topology_get_topology_nodeset()

Get topology node set.

Returns

the node set of memory of the system for which hwloc provides topology information. This is equivalent to the nodeset of the system object. If the topology is the result of a combination of several systems, NULL is returned.

Note

The returned nodeset is not newly allocated and should thus not be changed or freed; hwloc_bitmap_dup() must be used to obtain a local copy.

19.23 Converting between CPU sets and node sets

Functions

- static void hwloc_cpuset_to_nodeset (hwloc_topology_t topology, hwloc_const_cpuset_t_cpuset, hwloc_
 nodeset t nodeset)
- static void hwloc_cpuset_to_nodeset_strict (struct hwloc_topology *topology, hwloc_const_cpuset_t_cpuset, hwloc_nodeset_t nodeset)
- static void hwloc_cpuset_from_nodeset (hwloc_topology_t topology, hwloc_cpuset_t_cpuset, hwloc_const
 —nodeset_t nodeset)
- static void hwloc_cpuset_from_nodeset_strict (struct hwloc_topology *topology, hwloc_cpuset_t _cpuset, hwloc const nodeset t nodeset)

19.23.1 Detailed Description

There are two semantics for converting cpusets to nodesets depending on how non-NUMA machines are handled.

When manipulating nodesets for memory binding, non-NUMA machines should be considered as having a single NUMA node. The standard conversion routines below should be used so that marking the first bit of the nodeset means that memory should be bound to a non-NUMA whole machine.

When manipulating nodesets as an actual list of NUMA nodes without any need to handle memory binding on non-NUMA machines, the strict conversion routines may be used instead.

19.23.2 Function Documentation

19.23.2.1 hwloc_cpuset_from_nodeset()

Convert a NUMA node set into a CPU set and handle non-NUMA cases.

If the topology contains no NUMA nodes, the machine is considered as a single memory node, and the following behavior is used: If nodeset is empty, cpuset will be emptied as well. Otherwise cpuset will be entirely filled. This is useful for manipulating memory binding sets.

19.23.2.2 hwloc_cpuset_from_nodeset_strict()

Convert a NUMA node set into a CPU set without handling non-NUMA cases.

This is the strict variant of hwloc_cpuset_from_nodeset(). It does not fix non-NUMA cases. If the topology contains some NUMA nodes, behave exactly the same. However, if the topology contains no NUMA nodes, return an empty cpuset.

19.23.2.3 hwloc_cpuset_to_nodeset()

```
static void hwloc_cpuset_to_nodeset (
          hwloc_topology_t topology,
          hwloc_const_cpuset_t _cpuset,
          hwloc_nodeset_t nodeset ) [inline], [static]
```

Convert a CPU set into a NUMA node set and handle non-NUMA cases.

If some NUMA nodes have no CPUs at all, this function never sets their indexes in the output node set, even if a full CPU set is given in input.

If the topology contains no NUMA nodes, the machine is considered as a single memory node, and the following behavior is used: If cpuset is empty, nodeset will be emptied as well. Otherwise nodeset will be entirely filled.

19.23.2.4 hwloc_cpuset_to_nodeset_strict()

Convert a CPU set into a NUMA node set without handling non-NUMA cases.

This is the strict variant of hwloc_cpuset_to_nodeset(). It does not fix non-NUMA cases. If the topology contains some NUMA nodes, behave exactly the same. However, if the topology contains no NUMA nodes, return an empty nodeset.

19.24 Manipulating Distances

Functions

- static const struct hwloc_distances_s * hwloc_get_whole_distance_matrix_by_depth (hwloc_topology_
 t topology, unsigned depth)
- static const struct hwloc_distances_s * hwloc_get_whole_distance_matrix_by_type (hwloc_topology_t topology, hwloc_obj_type t type)
- static const struct hwloc_distances_s * hwloc_get_distance_matrix_covering_obj_by_depth (hwloc_
 topology_t topology, hwloc_obj_t obj, unsigned depth, unsigned *firstp)
- static int hwloc_get_latency (hwloc_topology_t topology, hwloc_obj_t obj1, hwloc_obj_t obj2, float *latency, float *reverse_latency)

19.24.1 Detailed Description

19.24.2 Function Documentation

19.24.2.1 hwloc_get_distance_matrix_covering_obj_by_depth()

Get distances for the given depth and covering some objects.

Return a distance matrix that describes depth depth and covers at least object obj and all its children.

When looking for the distance between some objects, a common ancestor should be passed in obj.

firstp is set to logical index of the first object described by the matrix.

The returned structure belongs to the hwloc library. The caller should not modify or free it.

19.24.2.2 hwloc_get_latency()

```
static int hwloc_get_latency (
          hwloc_topology_t topology,
          hwloc_obj_t obj1,
          hwloc_obj_t obj2,
          float * latency,
          float * reverse_latency ) [inline], [static]
```

Get the latency in both directions between two objects.

Look at ancestor objects from the bottom to the top until one of them contains a distance matrix that matches the objects exactly.

latency gets the value from object obj1 to obj2, while reverse_latency gets the reverse-direction value, which may be different on some architectures.

Returns

-1 if no ancestor contains a matching latency matrix.

19.24.2.3 hwloc_get_whole_distance_matrix_by_depth()

```
static const struct hwloc_distances_s* hwloc_get_whole_distance_matrix_by_depth (
    hwloc_topology_t topology,
    unsigned depth ) [static]
```

Get the distances between all objects at the given depth.

Returns

a distances structure containing a matrix with all distances between all objects at the given depth.

Slot i+nbobjs*j contains the distance from the object of logical index i the object of logical index j.

Note

This function only returns matrices covering the whole topology, without any unknown distance value. Those matrices are available in top-level object of the hierarchy. Matrices of lower objects are not reported here since they cover only part of the machine.

The returned structure belongs to the hwloc library. The caller should not modify or free it.

Returns

NULL if no such distance matrix exists.

19.24.2.4 hwloc_get_whole_distance_matrix_by_type()

Get the distances between all objects of a given type.

Returns

a distances structure containing a matrix with all distances between all objects of the given type.

Slot i+nbobjs*i contains the distance from the object of logical index i the object of logical index j.

Note

This function only returns matrices covering the whole topology, without any unknown distance value. Those matrices are available in top-level object of the hierarchy. Matrices of lower objects are not reported here since they cover only part of the machine.

The returned structure belongs to the hwloc library. The caller should not modify or free it.

Returns

NULL if no such distance matrix exists.

19.25 Finding I/O objects

Functions

- static hwloc_obj_t hwloc_get_non_io_ancestor_obj (hwloc_topology_t topology, hwloc_obj_t ioobj)
- static hwloc_obj_t hwloc_get_next_pcidev (hwloc_topology_t topology, hwloc_obj_t prev)
- static hwloc_obj_t hwloc_get_pcidev_by_busid (hwloc_topology_t topology, unsigned domain, unsigned bus, unsigned dev, unsigned func)
- static hwloc_obj_t hwloc_get_pcidev_by_busidstring (hwloc_topology_t topology, const char *busid)
- static hwloc_obj_t hwloc_get_next_osdev (hwloc_topology_t topology, hwloc_obj_t prev)
- static hwloc obj t hwloc get next bridge (hwloc topology t topology, hwloc obj t prev)
- static int hwloc_bridge_covers_pcibus (hwloc_obj_t bridge, unsigned domain, unsigned bus)
- static hwloc_obj_t hwloc_get_hostbridge_by_pcibus (hwloc_topology_t topology, unsigned domain, unsigned bus)

19.25.1 Detailed Description

19.25.2 Function Documentation

19.25.2.1 hwloc_bridge_covers_pcibus()

```
static int hwloc_bridge_covers_pcibus (
          hwloc_obj_t bridge,
          unsigned domain,
          unsigned bus ) [inline], [static]
```

19.25.2.2 hwloc get_hostbridge_by_pcibus()

Find the hostbridge that covers the given PCI bus.

This is useful for finding the locality of a bus because it is the hostbridge parent cpuset.

19.25.2.3 hwloc_get_next_bridge()

Get the next bridge in the system.

Returns

the first bridge if prev is NULL.

19.25.2.4 hwloc_get_next_osdev()

Get the next OS device in the system.

Returns

the first OS device if ${\tt prev}$ is ${\tt NULL}.$

19.25.2.5 hwloc_get_next_pcidev()

Get the next PCI device in the system.

Returns

the first PCI device if prev is NULL.

19.25.2.6 hwloc_get_non_io_ancestor_obj()

Get the first non-I/O ancestor object.

Given the I/O object ioobj, find the smallest non-I/O ancestor object. This regular object may then be used for binding because its locality is the same as ioobj.

19.25.2.7 hwloc_get_pcidev_by_busid()

```
static hwloc_obj_t hwloc_get_pcidev_by_busid (
    hwloc_topology_t topology,
    unsigned domain,
    unsigned bus,
    unsigned dev,
    unsigned func ) [inline], [static]
```

Find the PCI device object matching the PCI bus id given domain, bus device and function PCI bus id.

19.25.2.8 hwloc_get_pcidev_by_busidstring()

Find the PCI device object matching the PCI bus id given as a string xxxx:yy:zz.t or yy:zz.t.

19.26 The bitmap API

Macros

- #define hwloc bitmap foreach begin(id, bitmap)
- · #define hwloc bitmap foreach end()

Typedefs

- typedef struct hwloc bitmap s * hwloc bitmap t
- typedef const struct hwloc bitmap s * hwloc const bitmap t

Functions

- hwloc_bitmap_t hwloc_bitmap_alloc (void)
- · hwloc bitmap t hwloc bitmap alloc full (void)
- void hwloc_bitmap_free (hwloc_bitmap_t bitmap)
- hwloc_bitmap_t hwloc_bitmap_dup (hwloc_const_bitmap_t bitmap)
- void hwloc_bitmap_copy (hwloc_bitmap_t dst, hwloc_const_bitmap_t src)
- int hwloc bitmap snprintf (char *restrict buf, size t buflen, hwloc const bitmap t bitmap)
- int hwloc_bitmap_asprintf (char **strp, hwloc_const_bitmap_t bitmap)
- int hwloc bitmap sscanf (hwloc bitmap t bitmap, const char *restrict string)
- int hwloc bitmap list snprintf (char *restrict buf, size t buflen, hwloc const bitmap t bitmap)
- int hwloc_bitmap_list_asprintf (char **strp, hwloc_const_bitmap_t bitmap)
- int hwloc bitmap list sscanf (hwloc bitmap t bitmap, const char *restrict string)
- int hwloc bitmap taskset snprintf (char *restrict buf, size t buflen, hwloc const bitmap t bitmap)
- int hwloc bitmap taskset asprintf (char **strp, hwloc const bitmap t bitmap)
- int hwloc bitmap taskset sscanf (hwloc bitmap t bitmap, const char *restrict string)
- void hwloc_bitmap_zero (hwloc_bitmap_t bitmap)
- void hwloc_bitmap_fill (hwloc_bitmap_t bitmap)
- · void hwloc bitmap only (hwloc bitmap t bitmap, unsigned id)
- void hwloc_bitmap_allbut (hwloc_bitmap_t bitmap, unsigned id)
- void hwloc_bitmap_from_ulong (hwloc_bitmap_t bitmap, unsigned long mask)
- void hwloc_bitmap_from_ith_ulong (hwloc_bitmap_t bitmap, unsigned i, unsigned long mask)
- void hwloc_bitmap_set (hwloc_bitmap_t bitmap, unsigned id)
- void hwloc_bitmap_set_range (hwloc_bitmap_t bitmap, unsigned begin, int end)
- · void hwloc bitmap set ith ulong (hwloc bitmap t bitmap, unsigned i, unsigned long mask)
- void hwloc bitmap clr (hwloc bitmap t bitmap, unsigned id)
- void hwloc_bitmap_clr_range (hwloc_bitmap_t bitmap, unsigned begin, int end)
- void hwloc_bitmap_singlify (hwloc_bitmap_t bitmap)
- unsigned long hwloc_bitmap_to_ulong (hwloc_const_bitmap_t bitmap)
- unsigned long hwloc bitmap to ith ulong (hwloc const bitmap t bitmap, unsigned i)
- int hwloc bitmap isset (hwloc const bitmap t bitmap, unsigned id)
- int hwloc bitmap iszero (hwloc const bitmap t bitmap)
- int hwloc bitmap isfull (hwloc const bitmap t bitmap)
- int hwloc_bitmap_first (hwloc_const_bitmap_t bitmap)
- int hwloc bitmap next (hwloc const bitmap t bitmap, int prev)
- int hwloc bitmap last (hwloc const bitmap t bitmap)
- int hwloc bitmap weight (hwloc const bitmap t bitmap)
- void hwloc bitmap or (hwloc bitmap t res, hwloc const bitmap t bitmap1, hwloc const bitmap t bitmap2)
- void hwloc_bitmap_and (hwloc_bitmap_t res, hwloc_const_bitmap_t bitmap1, hwloc_const_bitmap_
 t bitmap2)

19.26 The bitmap API 155

void hwloc_bitmap_andnot (hwloc_bitmap_t res, hwloc_const_bitmap_t bitmap1, hwloc_const_bitmap_
 t bitmap2)

- void hwloc_bitmap_xor (hwloc_bitmap_t res, hwloc_const_bitmap_t bitmap1, hwloc_const_bitmap_
 t bitmap2)
- void hwloc bitmap not (hwloc bitmap t res, hwloc const bitmap t bitmap)
- int hwloc_bitmap_intersects (hwloc_const_bitmap_t bitmap1, hwloc_const_bitmap_t bitmap2)
- int hwloc_bitmap_isincluded (hwloc_const_bitmap_t sub_bitmap, hwloc_const_bitmap_t super_bitmap)
- int hwloc_bitmap_isequal (hwloc_const_bitmap_t bitmap1, hwloc_const_bitmap_t bitmap2)
- int hwloc bitmap compare first (hwloc const bitmap t bitmap1, hwloc const bitmap2)
- int hwloc_bitmap_compare (hwloc_const_bitmap_t bitmap_t bitmap_t bitmap_t bitmap_t)

19.26.1 Detailed Description

The hwloc_bitmap_t type represents a set of objects, typically OS processors – which may actually be hardware threads (represented by hwloc_cpuset_t, which is a typedef for hwloc_bitmap_t) – or memory nodes (represented by hwloc nodeset t, which is also a typedef for hwloc bitmap t).

Both CPU and node sets are always indexed by OS physical number.

Note

CPU sets and nodesets are described in Object Sets (hwloc_cpuset_t and hwloc_nodeset_t).

A bitmap may be of infinite size (all bits are set after some point). A bitmap may even be full if all bits are set.

Note

Several examples of using the bitmap API are available under the doc/examples/ directory in the source tree. Regression tests such as tests/hwloc/hwloc_bitmap*.c also make intensive use of this API.

19.26.2 Macro Definition Documentation

19.26.2.1 hwloc_bitmap_foreach_begin

Loop macro iterating on bitmap bitmap.

The loop must start with hwloc_bitmap_foreach_begin() and end with hwloc_bitmap_foreach_end() followed by a terminating ';'.

index is the loop variable; it should be an unsigned int. The first iteration will set index to the lowest index in the bitmap. Successive iterations will iterate through, in order, all remaining indexes set in the bitmap. To be specific: each iteration will return a value for index such that hwloc bitmap isset(bitmap, index) is true.

The assert prevents the loop from being infinite if the bitmap is infinitely set.

```
19.26.2.2 hwloc_bitmap_foreach_end
#define hwloc_bitmap_foreach_end( )
End of loop macro iterating on a bitmap.
Needs a terminating ';'.
See also
     hwloc_bitmap_foreach_begin()
19.26.3 Typedef Documentation
19.26.3.1 hwloc_bitmap_t
typedef struct hwloc_bitmap_s* hwloc_bitmap_t
Set of bits represented as an opaque pointer to an internal bitmap.
19.26.3.2 hwloc_const_bitmap_t
typedef const struct hwloc_bitmap_s* hwloc_const_bitmap_t
a non-modifiable hwloc_bitmap_t
19.26.4 Function Documentation
19.26.4.1 hwloc_bitmap_allbut()
void hwloc_bitmap_allbut (
             hwloc_bitmap_t bitmap,
              unsigned id )
```

Fill the bitmap and clear the index id.

19.26 The bitmap API 157

```
19.26.4.2 hwloc_bitmap_alloc()
```

Allocate a new empty bitmap.

Returns

A valid bitmap or NULL.

The bitmap should be freed by a corresponding call to hwloc_bitmap_free().

```
19.26.4.3 hwloc_bitmap_alloc_full()
```

Allocate a new full bitmap.

19.26.4.4 hwloc_bitmap_and()

```
void hwloc_bitmap_and (
          hwloc_bitmap_t res,
          hwloc_const_bitmap_t bitmap1,
          hwloc_const_bitmap_t bitmap2 )
```

And bitmaps bitmap1 and bitmap2 and store the result in bitmap res.

res can be the same as bitmap1 or bitmap2

19.26.4.5 hwloc_bitmap_andnot()

And bitmap bitmap1 and the negation of bitmap2 and store the result in bitmap res.

 $\verb"res" \textbf{ can be the same as} \verb"bitmap1" \textbf{ or} \verb"bitmap2"$

19.26.4.6 hwloc_bitmap_asprintf()

Stringify a bitmap into a newly allocated string.

19.26.4.7 hwloc_bitmap_clr()

Remove index id from bitmap bitmap.

19.26.4.8 hwloc_bitmap_clr_range()

Remove indexes from begin to end in bitmap bitmap.

If end is -1, the range is infinite.

19.26.4.9 hwloc_bitmap_compare()

Compare bitmaps bitmap1 and bitmap2 in lexicographic order.

Lexicographic comparison of bitmaps, starting for their highest indexes. Compare last indexes first, then second, etc. The empty bitmap is considered lower than anything.

Note

This is different from the non-existing hwloc_bitmap_compare_last() which would only compare the highest index of each bitmap.

19.26.4.10 hwloc_bitmap_compare_first()

Compare bitmaps bitmap1 and bitmap2 using their lowest index.

Smaller least significant bit is smaller. The empty bitmap is considered higher than anything.

19.26 The bitmap API 159

```
19.26.4.11 hwloc_bitmap_copy()
```

Copy the contents of bitmap src into the already allocated bitmap dst.

```
19.26.4.12 hwloc_bitmap_dup()
```

Duplicate bitmap bitmap by allocating a new bitmap and copying bitmap contents.

If bitmap is NULL, NULL is returned.

```
19.26.4.13 hwloc_bitmap_fill()
```

Fill bitmap bitmap with all possible indexes (even if those objects don't exist or are otherwise unavailable)

```
19.26.4.14 hwloc_bitmap_first()
```

Compute the first index (least significant bit) in bitmap bitmap.

Returns

-1 if no index is set in bitmap.

19.26.4.15 hwloc_bitmap_free()

Free bitmap bitmap.

If \mbox{bitmap} is \mbox{NULL} , no operation is performed.

19.26.4.16 hwloc_bitmap_from_ith_ulong()

```
void hwloc_bitmap_from_ith_ulong (
          hwloc_bitmap_t bitmap,
          unsigned i,
          unsigned long mask )
```

Setup bitmap bitmap from unsigned long mask used as i -th subset.

19.26.4.17 hwloc_bitmap_from_ulong()

Setup bitmap bitmap from unsigned long mask.

19.26.4.18 hwloc_bitmap_intersects()

Test whether bitmaps bitmap1 and bitmap2 intersects.

19.26.4.19 hwloc_bitmap_isequal()

Test whether bitmap bitmap1 is equal to bitmap bitmap2.

19.26.4.20 hwloc_bitmap_isfull()

Test whether bitmap bitmap is completely full.

Note

A full bitmap is always infinitely set.

19.26 The bitmap API 161

19.26.4.21 hwloc_bitmap_isincluded()

Test whether bitmap sub_bitmap is part of bitmap super_bitmap.

Note

The empty bitmap is considered included in any other bitmap.

```
19.26.4.22 hwloc_bitmap_isset()
```

Test whether index id is part of bitmap bitmap.

```
19.26.4.23 hwloc_bitmap_iszero()
```

Test whether bitmap bitmap is empty.

```
19.26.4.24 hwloc_bitmap_last()
```

Compute the last index (most significant bit) in bitmap bitmap.

Returns

-1 if no index is set in bitmap, or if bitmap is infinitely set.

19.26.4.25 hwloc_bitmap_list_asprintf()

Stringify a bitmap into a newly allocated list string.

19.26.4.26 hwloc bitmap list snprintf()

Stringify a bitmap in the list format.

Lists are comma-separated indexes or ranges. Ranges are dash separated indexes. The last range may not have an ending indexes if the bitmap is infinitely set.

Up to buflen characters may be written in buffer buf.

If buflen is 0, buf may safely be NULL.

Returns

the number of character that were actually written if not truncating, or that would have been written (not including the ending \0).

19.26.4.27 hwloc_bitmap_list_sscanf()

```
int hwloc_bitmap_list_sscanf (
          hwloc_bitmap_t bitmap,
          const char *restrict string )
```

Parse a list string and stores it in bitmap bitmap.

19.26.4.28 hwloc_bitmap_next()

Compute the next index in bitmap bitmap which is after index prev.

If prev is -1, the first index is returned.

Returns

-1 if no index with higher index is set in bitmap.

19.26 The bitmap API 163

19.26.4.29 hwloc_bitmap_not() void hwloc_bitmap_not (hwloc_bitmap_t res, hwloc_const_bitmap_t bitmap) Negate bitmap bitmap and store the result in bitmap res. res can be the same as bitmap 19.26.4.30 hwloc_bitmap_only() void hwloc_bitmap_only (hwloc_bitmap_t bitmap, unsigned id) Empty the bitmap bitmap and add bit id. 19.26.4.31 hwloc_bitmap_or() void hwloc_bitmap_or (hwloc_bitmap_t res, hwloc_const_bitmap_t bitmap1, hwloc_const_bitmap_t bitmap2) Or bitmaps bitmap1 and bitmap2 and store the result in bitmap res. res can be the same as bitmap1 or bitmap2 19.26.4.32 hwloc_bitmap_set() void hwloc_bitmap_set (hwloc_bitmap_t bitmap, unsigned id) Add index id in bitmap bitmap. 19.26.4.33 hwloc_bitmap_set_ith_ulong() void hwloc_bitmap_set_ith_ulong (

Replace i -th subset of bitmap bitmap with unsigned long mask.

hwloc_bitmap_t bitmap,

unsigned long mask)

unsigned i,

19.26.4.34 hwloc_bitmap_set_range()

```
void hwloc_bitmap_set_range (
                hwloc_bitmap_t bitmap,
                unsigned begin,
                int end )
```

Add indexes from begin to end in bitmap bitmap.

If end is -1, the range is infinite.

19.26.4.35 hwloc_bitmap_singlify()

Keep a single index among those set in bitmap bitmap.

May be useful before binding so that the process does not have a chance of migrating between multiple logical CPUs in the original mask.

19.26.4.36 hwloc_bitmap_snprintf()

Stringify a bitmap.

Up to buflen characters may be written in buffer buf.

If buflen is 0, buf may safely be NULL.

Returns

the number of character that were actually written if not truncating, or that would have been written (not including the ending \0).

19.26.4.37 hwloc_bitmap_sscanf()

Parse a bitmap string and stores it in bitmap bitmap.

19.26 The bitmap API 165

19.26.4.38 hwloc_bitmap_taskset_asprintf()

Stringify a bitmap into a newly allocated taskset-specific string.

19.26.4.39 hwloc bitmap taskset snprintf()

Stringify a bitmap in the taskset-specific format.

The taskset command manipulates bitmap strings that contain a single (possible very long) hexadecimal number starting with 0x.

Up to buflen characters may be written in buffer buf.

If buflen is 0, buf may safely be NULL.

Returns

the number of character that were actually written if not truncating, or that would have been written (not including the ending 0).

19.26.4.40 hwloc_bitmap_taskset_sscanf()

```
int hwloc_bitmap_taskset_sscanf (
          hwloc_bitmap_t bitmap,
          const char *restrict string )
```

Parse a taskset-specific bitmap string and stores it in bitmap bitmap.

19.26.4.41 hwloc_bitmap_to_ith_ulong()

```
unsigned long hwloc_bitmap_to_ith_ulong ( \label{eq:hwloc_const_bitmap_t} \begin{subarray}{l} hwloc_const_bitmap_t \begin{subarray}{l} bitmap, \\ unsigned \begin{subarray}{l} i \end{subarray} \end{subarray}
```

Convert the i -th subset of bitmap bitmap into unsigned long mask.

```
19.26.4.42 hwloc_bitmap_to_ulong()
```

Convert the beginning part of bitmap bitmap into unsigned long mask.

```
19.26.4.43 hwloc_bitmap_weight()
```

Compute the "weight" of bitmap bitmap (i.e., number of indexes that are in the bitmap).

Returns

the number of indexes that are in the bitmap.

-1 if bitmap is infinitely set.

19.26.4.44 hwloc_bitmap_xor()

Xor bitmaps bitmap1 and bitmap2 and store the result in bitmap res.

 $\verb"res" \textbf{ can be the same as} \verb"bitmap" 1 \textbf{ or} \verb"bitmap" 2$

19.26.4.45 hwloc_bitmap_zero()

Empty the bitmap bitmap.

19.27 Linux-specific helpers

Functions

- int hwloc_linux_parse_cpumap_file (FILE *file, hwloc_cpuset_t set)
- int hwloc_linux_set_tid_cpubind (hwloc_topology_t topology, pid_t tid, hwloc_const_cpuset_t set)
- int hwloc_linux_get_tid_cpubind (hwloc_topology_t topology, pid_t tid, hwloc_cpuset_t set)
- int hwloc_linux_get_tid_last_cpu_location (hwloc_topology_t topology, pid_t tid, hwloc_bitmap_t set)

19.27.1 Detailed Description

This includes helpers for manipulating Linux kernel cpumap files, and hwloc equivalents of the Linux sched_← setaffinity and sched_getaffinity system calls.

19.27.2 Function Documentation

19.27.2.1 hwloc_linux_get_tid_cpubind()

Get the current binding of thread tid.

The behavior is exactly the same as the Linux sched_getaffinity system call, but uses a hwloc cpuset.

Note

This is equivalent to calling hwloc_get_proc_cpubind() with HWLOC_CPUBIND_THREAD as flags.

19.27.2.2 hwloc_linux_get_tid_last_cpu_location()

Get the last physical CPU where thread tid ran.

Note

This is equivalent to calling hwloc_get_proc_last_cpu_location() with HWLOC_CPUBIND_THREAD as flags.

19.27.2.3 hwloc_linux_parse_cpumap_file()

```
int hwloc_linux_parse_cpumap_file (
    FILE * file,
    hwloc_cpuset_t set )
```

Convert a linux kernel cpumap file file into hwloc CPU set.

Might be used when reading CPU set from sysfs attributes such as topology and caches for processors, or local ← _cpus for devices.

19.27.2.4 hwloc_linux_set_tid_cpubind()

Bind a thread tid on cpus given in cpuset set.

The behavior is exactly the same as the Linux sched_setaffinity system call, but uses a hwloc cpuset.

Note

This is equivalent to calling hwloc_set_proc_cpubind() with HWLOC_CPUBIND_THREAD as flags.

19.28 Interoperability with Linux libnuma unsigned long masks

Functions

- static int hwloc_cpuset_to_linux_libnuma_ulongs (hwloc_topology_t topology, hwloc_const_cpuset_t cpuset, unsigned long *mask, unsigned long *maxnode)
- static int hwloc_nodeset_to_linux_libnuma_ulongs (hwloc_topology_t topology, hwloc_const_nodeset_
 t nodeset, unsigned long *mask, unsigned long *maxnode)
- static int hwloc_cpuset_from_linux_libnuma_ulongs (hwloc_topology_t topology, hwloc_cpuset_t cpuset, const unsigned long *mask, unsigned long maxnode)
- static int hwloc_nodeset_from_linux_libnuma_ulongs (hwloc_topology_t topology, hwloc_nodeset_t nodeset, const unsigned long *mask, unsigned long maxnode)

19.28.1 Detailed Description

This interface helps converting between Linux libnuma unsigned long masks and hwloc cpusets and nodesets.

It also offers a consistent behavior on non-NUMA machines or non-NUMA-aware kernels by assuming that the machines have a single NUMA node.

Note

Topology topology must match the current machine.

The behavior of libnuma is undefined if the kernel is not NUMA-aware. (when CONFIG_NUMA is not set in the kernel configuration). This helper and libnuma may thus not be strictly compatible in this case, which may be detected by checking whether numa_available() returns -1.

19.28.2 Function Documentation

19.28.2.1 hwloc_cpuset_from_linux_libnuma_ulongs()

Convert the array of unsigned long mask into hwloc CPU set.

mask is a array of unsigned long that will be read. maxnode contains the maximal node number that may be read in mask.

This function may be used after calling get_mempolicy or any other function that takes an array of unsigned long as output parameter (and possibly a maximal node number as input parameter).

19.28.2.2 hwloc_cpuset_to_linux_libnuma_ulongs()

Convert hwloc CPU set cpuset into the array of unsigned long mask.

mask is the array of unsigned long that will be filled. maxnode contains the maximal node number that may be stored in mask. maxnode will be set to the maximal node number that was found, plus one.

This function may be used before calling set_mempolicy, mbind, migrate_pages or any other function that takes an array of unsigned long and a maximal node number as input parameter.

19.28.2.3 hwloc_nodeset_from_linux_libnuma_ulongs()

Convert the array of unsigned long mask into hwloc NUMA node set.

mask is a array of unsigned long that will be read. maxnode contains the maximal node number that may be read in mask.

This function may be used after calling get_mempolicy or any other function that takes an array of unsigned long as output parameter (and possibly a maximal node number as input parameter).

19.28.2.4 hwloc_nodeset_to_linux_libnuma_ulongs()

Convert hwloc NUMA node set nodeset into the array of unsigned long mask.

mask is the array of unsigned long that will be filled. maxnode contains the maximal node number that may be stored in mask. maxnode will be set to the maximal node number that was found, plus one.

This function may be used before calling set_mempolicy, mbind, migrate_pages or any other function that takes an array of unsigned long and a maximal node number as input parameter.

19.29 Interoperability with Linux libnuma bitmask

Functions

- static struct bitmask * hwloc_nodeset_to_linux_libnuma_bitmask (hwloc_topology_t topology, hwloc_const
 __nodeset_t nodeset)
- static int hwloc_cpuset_from_linux_libnuma_bitmask (hwloc_topology_t topology, hwloc_cpuset_t cpuset, const struct bitmask *bitmask)
- static int hwloc_nodeset_from_linux_libnuma_bitmask (hwloc_topology_t topology, hwloc_nodeset_t nodeset, const struct bitmask *bitmask)

19.29.1 Detailed Description

This interface helps converting between Linux libnuma bitmasks and hwloc cpusets and nodesets.

It also offers a consistent behavior on non-NUMA machines or non-NUMA-aware kernels by assuming that the machines have a single NUMA node.

Note

Topology topology must match the current machine.

The behavior of libnuma is undefined if the kernel is not NUMA-aware. (when CONFIG_NUMA is not set in the kernel configuration). This helper and libnuma may thus not be strictly compatible in this case, which may be detected by checking whether numa_available() returns -1.

19.29.2 Function Documentation

19.29.2.1 hwloc_cpuset_from_linux_libnuma_bitmask()

Convert libnuma bitmask bitmask into hwloc CPU set cpuset.

This function may be used after calling many numa_functions that use a struct bitmask as an output parameter.

19.29.2.2 hwloc_cpuset_to_linux_libnuma_bitmask()

Convert hwloc CPU set cpuset into the returned libnuma bitmask.

The returned bitmask should later be freed with numa bitmask free.

This function may be used before calling many numa_functions that use a struct bitmask as an input parameter.

Returns

newly allocated struct bitmask.

19.29.2.3 hwloc_nodeset_from_linux_libnuma_bitmask()

Convert libnuma bitmask bitmask into hwloc NUMA node set nodeset.

This function may be used after calling many numa_functions that use a struct bitmask as an output parameter.

19.29.2.4 hwloc_nodeset_to_linux_libnuma_bitmask()

Convert hwloc NUMA node set nodeset into the returned libnuma bitmask.

The returned bitmask should later be freed with numa_bitmask_free.

This function may be used before calling many numa_functions that use a struct bitmask as an input parameter.

Returns

newly allocated struct bitmask.

19.30 Interoperability with glibc sched affinity

Functions

- static int hwloc_cpuset_to_glibc_sched_affinity (hwloc_topology_t topology, hwloc_const_cpuset_t hwlocset, cpu set t*schedset, size t schedsetsize)
- static int hwloc_cpuset_from_glibc_sched_affinity (hwloc_topology_t topology, hwloc_cpuset_t hwlocset, const cpu_set_t *schedset, size_t schedsetsize)

19.30.1 Detailed Description

This interface offers ways to convert between hwloc cpusets and glibc cpusets such as those manipulated by sched_getaffinity() or pthread_attr_setaffinity_np().

Note

Topology topology must match the current machine.

19.30.2 Function Documentation

19.30.2.1 hwloc_cpuset_from_glibc_sched_affinity()

Convert glibc sched affinity CPU set schedset into hwloc CPU set.

This function may be used before calling sched_setaffinity or any other function that takes a cpu_set_t as input parameter.

schedsetsize should be sizeof(cpu_set_t) unless schedset was dynamically allocated with CPU_ALLOC

19.30.2.2 hwloc_cpuset_to_glibc_sched_affinity()

Convert hwloc CPU set toposet into glibc sched affinity CPU set schedset.

This function may be used before calling sched_setaffinity or any other function that takes a cpu_set_t as input parameter.

schedsetsize should be sizeof(cpu_set_t) unless schedset was dynamically allocated with CPU_ALLOC

19.31 Interoperability with OpenCL

Functions

- static int hwloc_opencl_get_device_cpuset (hwloc_topology_t topology, cl_device_id device, hwloc_cpuset
 t set)
- static hwloc_obj_t hwloc_opencl_get_device_osdev_by_index (hwloc_topology_t topology, unsigned platform_index, unsigned device_index)
- static hwloc_obj_t hwloc_opencl_get_device_osdev (hwloc_topology_t topology, cl_device_id device)

19.31.1 Detailed Description

This interface offers ways to retrieve topology information about OpenCL devices.

Only the AMD OpenCL interface currently offers useful locality information about its devices.

19.31.2 Function Documentation

19.31.2.1 hwloc_opencl_get_device_cpuset()

Get the CPU set of logical processors that are physically close to OpenCL device device.

Return the CPU set describing the locality of the OpenCL device device.

Topology topology and device device must match the local machine. I/O devices detection and the OpenCL component are not needed in the topology.

The function only returns the locality of the device. If more information about the device is needed, OS objects should be used instead, see hwloc opencl get device osdev() and hwloc opencl get device osdev by index().

This function is currently only implemented in a meaningful way for Linux with the AMD OpenCL implementation; other systems will simply get a full cpuset.

19.31.2.2 hwloc_opencl_get_device_osdev()

Get the hwloc OS device object corresponding to OpenCL device device.

Return the hwloc OS device object that describes the given OpenCL device device. Return NULL if there is none.

Topology topology and device device must match the local machine. I/O devices detection and the OpenCL component must be enabled in the topology. If not, the locality of the object may still be found using hwloc_opencl—get_device_cpuset().

Note

The corresponding hwloc PCI device may be found by looking at the result parent pointer.

19.31.2.3 hwloc_opencl_get_device_osdev_by_index()

Get the hwloc OS device object corresponding to the OpenCL device for the given indexes.

Return the OS device object describing the OpenCL device whose platform index is platform_index, and whose device index within this platform if device_index. Return NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the OpenCL component must be enabled in the topology.

Note

19.32 Interoperability with the CUDA Driver API

Functions

- static int hwloc_cuda_get_device_pci_ids (hwloc_topology_t topology, CUdevice cudevice, int *domain, int *bus, int *dev)
- static int hwloc_cuda_get_device_cpuset (hwloc_topology_t topology, CUdevice cudevice, hwloc_cpuset_t set)
- static hwloc obj t hwloc cuda get device pcidev (hwloc topology t topology, CUdevice cudevice)
- static hwloc_obj_t hwloc_cuda_get_device_osdev (hwloc_topology_t topology, CUdevice cudevice)
- static hwloc_obj_t hwloc_cuda_get_device_osdev_by_index (hwloc_topology_t topology, unsigned idx)

19.32.1 Detailed Description

This interface offers ways to retrieve topology information about CUDA devices when using the CUDA Driver API.

19.32.2 Function Documentation

19.32.2.1 hwloc_cuda_get_device_cpuset()

Get the CPU set of logical processors that are physically close to device cudevice.

Return the CPU set describing the locality of the CUDA device cudevice.

Topology topology and device cudevice must match the local machine. I/O devices detection and the CUDA component are not needed in the topology.

The function only returns the locality of the device. If more information about the device is needed, OS objects should be used instead, see hwloc cuda get device osdev() and hwloc cuda get device osdev by index().

This function is currently only implemented in a meaningful way for Linux; other systems will simply get a full cpuset.

19.32.2.2 hwloc_cuda_get_device_osdev()

Get the hwloc OS device object corresponding to CUDA device cudevice.

Return the hwloc OS device object that describes the given CUDA device <code>cudevice</code>. Return NULL if there is none.

Topology topology and device cudevice must match the local machine. I/O devices detection and the NVML component must be enabled in the topology. If not, the locality of the object may still be found using hwloc_cudac_get_device_cpuset().

Note

The corresponding hwloc PCI device may be found by looking at the result parent pointer.

19.32.2.3 hwloc_cuda_get_device_osdev_by_index()

Get the hwloc OS device object corresponding to the CUDA device whose index is idx.

Return the OS device object describing the CUDA device whose index is idx. Return NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the CUDA component must be enabled in the topology.

Note

The corresponding PCI device object can be obtained by looking at the OS device parent object. This function is identical to hwloc cudart get device osdev by index().

19.32.2.4 hwloc_cuda_get_device_pci_ids()

Return the domain, bus and device IDs of the CUDA device ${\tt cudevice}$.

Device cudevice must match the local machine.

19.32.2.5 hwloc_cuda_get_device_pcidev()

Get the hwloc PCI device object corresponding to the CUDA device cudevice.

Return the PCI device object describing the CUDA device cudevice. Return NULL if there is none.

Topology topology and device cudevice must match the local machine. I/O devices detection must be enabled in topology topology. The CUDA component is not needed in the topology.

19.33 Interoperability with the CUDA Runtime API

Functions

- static int hwloc_cudart_get_device_pci_ids (hwloc_topology_t topology, int idx, int *domain, int *bus, int *dev)
- static int hwloc_cudart_get_device_cpuset (hwloc_topology_t topology, int idx, hwloc_cpuset_t set)
- static hwloc obj t hwloc cudart get device pcidev (hwloc topology t topology, int idx)
- static hwloc_obj_t hwloc_cudart_get_device_osdev_by_index (hwloc_topology_t topology, unsigned idx)

19.33.1 Detailed Description

This interface offers ways to retrieve topology information about CUDA devices when using the CUDA Runtime API.

19.33.2 Function Documentation

19.33.2.1 hwloc_cudart_get_device_cpuset()

Get the CPU set of logical processors that are physically close to device idx.

Return the CPU set describing the locality of the CUDA device whose index is idx.

Topology topology and device idx must match the local machine. I/O devices detection and the CUDA component are not needed in the topology.

This function is currently only implemented in a meaningful way for Linux; other systems will simply get a full cpuset.

19.33.2.2 hwloc_cudart_get_device_osdev_by_index()

Get the hwloc OS device object corresponding to the CUDA device whose index is idx.

Return the OS device object describing the CUDA device whose index is idx. Return NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the CUDA component must be enabled in the topology. If not, the locality of the object may still be found using hwloc_cudart_get_device_cpuset().

Note

The corresponding PCI device object can be obtained by looking at the OS device parent object. This function is identical to hwloc_cuda_get_device_osdev_by_index().

19.33.2.3 hwloc_cudart_get_device_pci_ids()

```
static int hwloc_cudart_get_device_pci_ids (
    hwloc_topology_t topology,
    int idx,
    int * domain,
    int * bus,
    int * dev ) [inline], [static]
```

Return the domain, bus and device IDs of the CUDA device whose index is idx.

Device index idx must match the local machine.

19.33.2.4 hwloc_cudart_get_device_pcidev()

Get the hwloc PCI device object corresponding to the CUDA device whose index is idx.

Return the PCI device object describing the CUDA device whose index is idx. Return NULL if there is none.

Topology topology and device idx must match the local machine. I/O devices detection must be enabled in topology topology. The CUDA component is not needed in the topology.

19.34 Interoperability with the NVIDIA Management Library

Functions

- static int hwloc_nvml_get_device_cpuset (hwloc_topology_t topology, nvmlDevice_t device, hwloc_cpuset_t set)
- static hwloc_obj_t hwloc_nvml_get_device_osdev_by_index (hwloc_topology_t topology, unsigned idx)
- static hwloc_obj_t hwloc_nvml_get_device_osdev (hwloc_topology_t topology, nvmlDevice_t device)

19.34.1 Detailed Description

This interface offers ways to retrieve topology information about devices managed by the NVIDIA Management Library (NVML).

19.34.2 Function Documentation

19.34.2.1 hwloc_nvml_get_device_cpuset()

Get the CPU set of logical processors that are physically close to NVML device device.

Return the CPU set describing the locality of the NVML device ${\tt device}.$

Topology topology and device device must match the local machine. I/O devices detection and the NVML component are not needed in the topology.

The function only returns the locality of the device. If more information about the device is needed, OS objects should be used instead, see hwloc_nvml_get_device_osdev, and hwloc_nvml_get_device_osdev.

This function is currently only implemented in a meaningful way for Linux; other systems will simply get a full cpuset.

19.34.2.2 hwloc_nvml_get_device_osdev()

Get the hwloc OS device object corresponding to NVML device device.

Return the hwloc OS device object that describes the given NVML device device. Return NULL if there is none.

Topology topology and device device must match the local machine. I/O devices detection and the NVML component must be enabled in the topology. If not, the locality of the object may still be found using hwloc_nvml—get_device_cpuset().

Note

The corresponding hwloc PCI device may be found by looking at the result parent pointer.

19.34.2.3 hwloc_nvml_get_device_osdev_by_index()

Get the hwloc OS device object corresponding to the NVML device whose index is idx.

Return the OS device object describing the NVML device whose index is idx. Returns NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the NVML component must be enabled in the topology.

Note

19.35 Interoperability with OpenGL displays

Functions

- static hwloc_obj_t hwloc_gl_get_display_osdev_by_port_device (hwloc_topology_t topology, unsigned port, unsigned device)
- static hwloc obj t hwloc gl get display osdev by name (hwloc topology, t topology, const char *name)
- static int hwloc_gl_get_display_by_osdev (hwloc_topology_t topology, hwloc_obj_t osdev, unsigned *port, unsigned *device)

19.35.1 Detailed Description

This interface offers ways to retrieve topology information about OpenGL displays.

Only the NVIDIA display locality information is currently available, using the NV-CONTROL X11 extension and the NVCtrl library.

19.35.2 Function Documentation

19.35.2.1 hwloc_gl_get_display_by_osdev()

```
static int hwloc_gl_get_display_by_osdev (
          hwloc_topology_t topology,
          hwloc_obj_t osdev,
          unsigned * port,
          unsigned * device ) [inline], [static]
```

Get the OpenGL display port and device corresponding to the given hwloc OS object.

Return the OpenGL display port (server) in port and device (screen) in screen that correspond to the given hwloc OS device object. Return -1 if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the GL component must be enabled in the topology.

19.35.2.2 hwloc_gl_get_display_osdev_by_name()

Get the hwloc OS device object corresponding to the OpenGL display given by name.

Return the OS device object describing the OpenGL display whose name is name, built as ":port.device" such as ":0.0". Return NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the GL component must be enabled in the topology.

Note

19.35.2.3 hwloc_gl_get_display_osdev_by_port_device()

```
static hwloc_obj_t hwloc_gl_get_display_osdev_by_port_device (
          hwloc_topology_t topology,
          unsigned port,
          unsigned device ) [inline], [static]
```

Get the hwloc OS device object corresponding to the OpenGL display given by port and device index.

Return the OS device object describing the OpenGL display whose port (server) is port and device (screen) is device. Return NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection and the GL component must be enabled in the topology.

Note

19.36 Interoperability with Intel Xeon Phi (MIC)

Functions

- static int hwloc_intel_mic_get_device_cpuset (hwloc_topology_t topology, int idx, hwloc_cpuset_t set)
- static hwloc_obj_t hwloc_intel_mic_get_device_osdev_by_index (hwloc_topology_t topology, unsigned idx)

19.36.1 Detailed Description

This interface offers ways to retrieve topology information about Intel Xeon Phi (MIC) devices.

19.36.2 Function Documentation

19.36.2.1 hwloc_intel_mic_get_device_cpuset()

Get the CPU set of logical processors that are physically close to MIC device whose index is idx.

Return the CPU set describing the locality of the MIC device whose index is idx.

Topology topology and device index idx must match the local machine. I/O devices detection is not needed in the topology.

The function only returns the locality of the device. If more information about the device is needed, OS objects should be used instead, see hwloc_intel_mic_get_device_osdev_by_index().

This function is currently only implemented in a meaningful way for Linux; other systems will simply get a full cpuset.

19.36.2.2 hwloc_intel_mic_get_device_osdev_by_index()

Get the hwloc OS device object corresponding to the MIC device for the given index.

Return the OS device object describing the MIC device whose index is idx. Return NULL if there is none.

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection must be enabled in the topology.

Note

19.37 Interoperability with OpenFabrics

Functions

- static int hwloc_ibv_get_device_cpuset (hwloc_topology_t topology, struct ibv_device *ibdev, hwloc_cpuset ← t set)
- static hwloc_obj_t hwloc_ibv_get_device_osdev_by_name (hwloc_topology_t topology, const char *ibname)
- static hwloc_obj_t hwloc_ibv_get_device_osdev (hwloc_topology_t topology, struct ibv_device *ibdev)

19.37.1 Detailed Description

This interface offers ways to retrieve topology information about OpenFabrics devices (InfiniBand, Omni-Path, us⇔ NIC, etc).

19.37.2 Function Documentation

19.37.2.1 hwloc_ibv_get_device_cpuset()

Get the CPU set of logical processors that are physically close to device ibdev.

Return the CPU set describing the locality of the OpenFabrics device ibdev (InfiniBand, etc).

Topology topology and device ibdev must match the local machine. I/O devices detection is not needed in the topology.

The function only returns the locality of the device. If more information about the device is needed, OS objects should be used instead, see hwloc_ibv_get_device_osdev, and hwloc_ibv_get_device_osdev, by _name().

This function is currently only implemented in a meaningful way for Linux; other systems will simply get a full cpuset.

19.37.2.2 hwloc ibv get device osdev()

Get the hwloc OS device object corresponding to the OpenFabrics device ibdev.

Return the OS device object describing the OpenFabrics device ibdev (InfiniBand, etc). Returns NULL if there is none.

Topology topology and device ibdev must match the local machine. I/O devices detection must be enabled in the topology. If not, the locality of the object may still be found using $hwloc_ibv_get_device_cpuset()$.

Note

19.37.2.3 hwloc_ibv_get_device_osdev_by_name()

Get the hwloc OS device object corresponding to the OpenFabrics device named ibname.

Return the OS device object describing the OpenFabrics device (InfiniBand, Omni-Path, usNIC, etc) whose name is ibname (mlx5_0, hfi1_0, usnic_0, qib0, etc). Returns NULL if there is none. The name ibname is usually obtained from ibv_get_device_name().

The topology topology does not necessarily have to match the current machine. For instance the topology may be an XML import of a remote host. I/O devices detection must be enabled in the topology.

Note

19.38 Interoperability with Myrinet Express

Functions

- static int hwloc mx board get device cpuset (hwloc topology t topology, unsigned id, hwloc cpuset t set)
- static int hwloc_mx_endpoint_get_device_cpuset (hwloc_topology_t topology, mx_endpoint_t endpoint, hwloc cpuset t set)

19.38.1 Detailed Description

This interface offers ways to retrieve topology information about Myrinet Express hardware.

19.38.2 Function Documentation

19.38.2.1 hwloc_mx_board_get_device_cpuset()

Get the CPU set of logical processors that are physically close the MX board id.

Return the CPU set describing the locality of the Myrinet Express board whose index is id.

Topology topology and device id must match the local machine. I/O devices detection is not needed in the topology.

The function only returns the locality of the device. No additional information about the device is available.

19.38.2.2 hwloc_mx_endpoint_get_device_cpuset()

Get the CPU set of logical processors that are physically close the MX endpoint endpoint.

Return the CPU set describing the locality of the Myrinet Express board that runs the MX endpoint endpoint.

Topology topology and device id must match the local machine. I/O devices detection is not needed in the topology.

The function only returns the locality of the endpoint. No additional information about the endpoint or device is available.

19.39 Topology differences

Data Structures

- · union hwloc topology diff obj attr u
- union hwloc_topology_diff_u

Typedefs

- typedef enum hwloc_topology_diff_obj_attr_type_e hwloc_topology_diff_obj_attr_type_t
- typedef enum hwloc_topology_diff_type_e hwloc_topology_diff_type_t
- typedef union hwloc_topology_diff_u * hwloc_topology_diff_t

Enumerations

- enum hwloc_topology_diff_obj_attr_type_e { HWLOC_TOPOLOGY_DIFF_OBJ_ATTR_SIZE, HWLOC_T
 OPOLOGY_DIFF_OBJ_ATTR_NAME, HWLOC_TOPOLOGY_DIFF_OBJ_ATTR_INFO }
- enum hwloc_topology_diff_type_e { HWLOC_TOPOLOGY_DIFF_OBJ_ATTR, HWLOC_TOPOLOGY_DIF←
 F TOO COMPLEX }
- enum hwloc_topology_diff_apply_flags_e { HWLOC_TOPOLOGY_DIFF_APPLY_REVERSE }

Functions

- int hwloc_topology_diff_build (hwloc_topology_t topology, hwloc_topology_t newtopology, unsigned long flags, hwloc_topology_diff_t *diff)
- int hwloc_topology_diff_apply (hwloc_topology_t topology, hwloc_topology_diff_t diff, unsigned long flags)
- int hwloc topology diff destroy (hwloc topology t topology, hwloc topology diff t diff)
- int hwloc_topology_diff_load_xml (hwloc_topology_t topology, const char *xmlpath, hwloc_topology_diff_

 t *diff, char **refname)
- int hwloc_topology_diff_export_xml (hwloc_topology_t topology, hwloc_topology_diff_t diff, const char *refname, const char *xmlpath)
- int hwloc_topology_diff_load_xmlbuffer (hwloc_topology_t topology, const char *xmlbuffer, int buflen, hwloc
 _topology_diff_t *diff, char **refname)
- int hwloc_topology_diff_export_xmlbuffer (hwloc_topology_t topology, hwloc_topology_diff_t diff, const char *refname, char **xmlbuffer, int *buflen)

19.39.1 Detailed Description

Applications that manipulate many similar topologies, for instance one for each node of a homogeneous cluster, may want to compress topologies to reduce the memory footprint.

This file offers a way to manipulate the difference between topologies and export/import it to/from XML. Compression may therefore be achieved by storing one topology entirely while the others are only described by their differences with the former. The actual topology can be reconstructed when actually needed by applying the precomputed difference to the reference topology.

This interface targets very similar nodes. Only very simple differences between topologies are actually supported, for instance a change in the memory size, the name of the object, or some info attribute. More complex differences such as adding or removing objects cannot be represented in the difference structures and therefore return errors.

It means that there is no need to apply the difference when looking at the tree organization (how many levels, how many objects per level, what kind of objects, CPU and node sets, etc) and when binding to objects. However the difference must be applied when looking at object attributes such as the name, the memory size or info attributes.

19.39.2 Typedef Documentation

```
19.39.2.1 hwloc_topology_diff_obj_attr_type_t
```

```
typedef enum hwloc_topology_diff_obj_attr_type_e hwloc_topology_diff_obj_attr_type_t
```

Type of one object attribute difference.

19.39.2.2 hwloc_topology_diff_t

```
{\tt typedef union \ hwloc\_topology\_diff\_u * hwloc\_topology\_diff\_t}
```

One element of a difference list between two topologies.

19.39.2.3 hwloc_topology_diff_type_t

```
typedef enum hwloc_topology_diff_type_e hwloc_topology_diff_type_t
```

Type of one element of a difference list.

19.39.3 Enumeration Type Documentation

```
19.39.3.1 hwloc_topology_diff_apply_flags_e
```

```
enum hwloc_topology_diff_apply_flags_e
```

Flags to be given to hwloc_topology_diff_apply().

Enumerator

```
HWLOC_TOPOLOGY_DIFF_APPLY_REVERSE | Apply topology diff in reverse direction.
```

19.39.3.2 hwloc_topology_diff_obj_attr_type_e

enum hwloc_topology_diff_obj_attr_type_e

190 Module Documentation

Type of one object attribute difference.

Enumerator

HWLOC_TOPOLOGY_DIFF_OBJ_ATTR_SIZE	The object local memory is modified. The union is a hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_cobj_attr_uint64_s (and the index field is ignored).
HWLOC_TOPOLOGY_DIFF_OBJ_ATTR_NAME	The object name is modified. The union is a hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_cobj_attr_string_s (and the name field is ignored).
HWLOC_TOPOLOGY_DIFF_OBJ_ATTR_INFO	the value of an info attribute is modified. The union is a hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_cobj_attr_string_s.

19.39.3.3 hwloc_topology_diff_type_e

```
\verb"enum hwloc_topology_diff_type_e"
```

Type of one element of a difference list.

Enumerator

HWLOC_TOPOLOGY_DIFF_OBJ_ATTR	An object attribute was changed. The union is a hwloc_← topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_s.
HWLOC_TOPOLOGY_DIFF_TOO_COMPLEX	The difference is too complex, it cannot be represented. The difference below this object has not been checked. hwloc_topology_diff_build() will return 1. The union is a hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_too_complex_s.

19.39.4 Function Documentation

19.39.4.1 hwloc_topology_diff_apply()

```
int hwloc_topology_diff_apply (
          hwloc_topology_t topology,
          hwloc_topology_diff_t diff,
          unsigned long flags )
```

Apply a topology diff to an existing topology.

flags is an OR'ed set of hwloc_topology_diff_apply_flags_e.

The new topology is modified in place. hwloc_topology_dup() may be used to duplicate it before patching.

If the difference cannot be applied entirely, all previous applied elements are unapplied before returning.

192 Module Documentation

Returns

0 on success.

-N if applying the difference failed while trying to apply the N-th part of the difference. For instance -1 is returned if the very first difference element could not be applied.

19.39.4.2 hwloc_topology_diff_build()

Compute the difference between 2 topologies.

The difference is stored as a list of hwloc_topology_diff_t entries starting at diff. It is computed by doing a depth-first traversal of both topology trees simultaneously.

If the difference between 2 objects is too complex to be represented (for instance if some objects have different types, or different numbers of children), a special diff entry of type HWLOC_TOPOLOGY_DIFF_TOO_COMPLEX is queued. The computation of the diff does not continue below these objects. So each such diff entry means that the difference between two subtrees could not be computed.

Returns

0 if the difference can be represented properly.

0 with diff pointing to NULL if there is no difference between the topologies.

- 1 if the difference is too complex (see above). Some entries in the list will be of type HWLOC_TOPOLOGY ← DIFF TOO COMPLEX.
- -1 on any other error.

Note

flags is currently not used. It should be 0.

The output diff has to be freed with hwloc_topology_diff_destroy().

The output diff can only be exported to XML or passed to hwloc_topology_diff_apply() if 0 was returned, i.e. if no entry of type HWLOC_TOPOLOGY_DIFF_TOO_COMPLEX is listed.

The output diff may be modified by removing some entries from the list. The removed entries should be freed by passing them to to hwloc_topology_diff_destroy() (possible as another list).

19.39.4.3 hwloc_topology_diff_destroy()

Destroy a list of topology differences.

Note

The topology parameter must be a valid topology but it is not required that it is related to diff.

19.39.4.4 hwloc_topology_diff_export_xml()

```
int hwloc_topology_diff_export_xml (
          hwloc_topology_t topology,
          hwloc_topology_diff_t diff,
          const char * refname,
          const char * xmlpath )
```

Export a list of topology differences to a XML file.

If not NULL, refname defines an identifier string for the reference topology which was used as a base when computing this difference. This identifier is usually the name of the other XML file that contains the reference topology. This attribute is given back when reading the diff from XML.

Note

The topology parameter must be a valid topology but it is not required that it is related to diff.

19.39.4.5 hwloc_topology_diff_export_xmlbuffer()

```
int hwloc_topology_diff_export_xmlbuffer (
    hwloc_topology_t topology,
    hwloc_topology_diff_t diff,
    const char * refname,
    char ** xmlbuffer,
    int * buflen )
```

Export a list of topology differences to a XML buffer.

If not NULL, refname defines an identifier string for the reference topology which was used as a base when computing this difference. This identifier is usually the name of the other XML file that contains the reference topology. This attribute is given back when reading the diff from XML.

Note

The XML buffer should later be freed with hwloc free xmlbuffer().

The topology parameter must be a valid topology but it is not required that it is related to diff.

19.39.4.6 hwloc_topology_diff_load_xml()

```
int hwloc_topology_diff_load_xml (
          hwloc_topology_t topology,
          const char * xmlpath,
          hwloc_topology_diff_t * diff,
          char ** refname )
```

Load a list of topology differences from a XML file.

If not NULL, refname will be filled with the identifier string of the reference topology for the difference file, if any was specified in the XML file. This identifier is usually the name of the other XML file that contains the reference topology.

Note

The topology parameter must be a valid topology but it is not required that it is related to diff. the pointer returned in refname should later be freed by the caller.

194 Module Documentation

19.39.4.7 hwloc_topology_diff_load_xmlbuffer()

```
int hwloc_topology_diff_load_xmlbuffer (
    hwloc_topology_t topology,
    const char * xmlbuffer,
    int buflen,
    hwloc_topology_diff_t * diff,
    char ** refname )
```

Load a list of topology differences from a XML buffer.

If not <code>NULL</code>, <code>refname</code> will be filled with the identifier string of the reference topology for the difference file, if any was specified in the XML file. This identifier is usually the name of the other XML file that contains the reference topology.

Note

The topology parameter must be a valid topology but it is not required that it is related to diff. the pointer returned in refname should later be freed by the caller.

19.40 Components and Plugins: Discovery components

Data Structures

• struct hwloc_disc_component

Typedefs

• typedef enum hwloc_disc_component_type_e hwloc_disc_component_type_t

Enumerations

enum hwloc_disc_component_type_e { HWLOC_DISC_COMPONENT_TYPE_CPU, HWLOC_DISC_CO
 MPONENT_TYPE_GLOBAL, HWLOC_DISC_COMPONENT_TYPE_MISC }

19.40.1 Detailed Description

19.40.2 Typedef Documentation

19.40.2.1 hwloc_disc_component_type_t

typedef enum hwloc_disc_component_type_e hwloc_disc_component_type_t

Discovery component type.

19.40.3 Enumeration Type Documentation

19.40.3.1 hwloc_disc_component_type_e

enum hwloc_disc_component_type_e

Discovery component type.

Enumerator

HWLOC_DISC_COMPONENT_TYPE_CPU	CPU-only discovery through the OS, or generic no-OS support.
HWLOC_DISC_COMPONENT_TYPE_GLOBAL	xml, synthetic or custom, platform-specific components such as bgq. Anything the discovers CPU and everything else. No misc backend is expected to complement a global component.
HWLOC_DISC_COMPONENT_TYPE_MISC	OpenCL, Cuda, etc.

196 Module Documentation

19.41 Components and Plugins: Discovery backends

Data Structures

· struct hwloc backend

Enumerations

enum hwloc_backend_flag_e { HWLOC_BACKEND_FLAG_NEED_LEVELS }

Functions

- struct hwloc_backend * hwloc_backend_alloc (struct hwloc_disc_component *component)
- int hwloc_backend_enable (struct hwloc_topology *topology, struct hwloc_backend *backend)
- int hwloc_backends_get_obj_cpuset (struct hwloc_backend *caller, struct hwloc_obj *obj, hwloc_bitmap_t cpuset)
- int hwloc backends notify new object (struct hwloc backend *caller, struct hwloc obj *obj)

19.41.1 Detailed Description

19.41.2 Enumeration Type Documentation

```
19.41.2.1 hwloc_backend_flag_e
```

```
enum hwloc_backend_flag_e
```

Backend flags.

Enumerator

HWLOC_BACKEND_FLAG_NEED_LEVELS	Levels should be reconnected before this backend discover()
	is used.

19.41.3 Function Documentation

```
19.41.3.1 hwloc_backend_alloc()
```

Allocate a backend structure, set good default values, initialize backend->component and topology, etc. The caller will then modify whatever needed, and call hwloc_backend_enable().

19.41.3.2 hwloc_backend_enable()

Enable a previously allocated and setup backend.

19.41.3.3 hwloc_backends_get_obj_cpuset()

Used by backends discovery callbacks to request locality information from others.

Traverse the list of enabled backends until one has a get_obj_cpuset() method, and call it.

19.41.3.4 hwloc_backends_notify_new_object()

Used by backends discovery callbacks to notify other backends of new objects.

Traverse the list of enabled backends (all but caller) and invoke their notify_new_object() method to notify them that a new object just got added to the topology.

Currently only used for notifying of new PCI device objects.

198 Module Documentation

19.42 Components and Plugins: Generic components

Data Structures

· struct hwloc_component

Typedefs

• typedef enum hwloc_component_type_e hwloc_component_type_t

Enumerations

 enum hwloc_component_type_e { HWLOC_COMPONENT_TYPE_DISC, HWLOC_COMPONENT_TYPE XML }

19.42.1 Detailed Description

19.42.2 Typedef Documentation

```
19.42.2.1 hwloc_component_type_t
```

typedef enum hwloc_component_type_e hwloc_component_type_t

Generic component type.

19.42.3 Enumeration Type Documentation

19.42.3.1 hwloc_component_type_e

enum hwloc_component_type_e

Generic component type.

Enumerator

HWLOC_COMPONENT_TYPE_DISC	The data field must point to a struct hwloc_disc_component.
HWLOC_COMPONENT_TYPE_XML	The data field must point to a struct hwloc_xml_component.

19.43 Components and Plugins: Core functions to be used by components

Typedefs

typedef void(* hwloc_report_error_t) (const char *msg, int line)

Functions

- struct hwloc_obj * hwloc_insert_object_by_cpuset (struct hwloc_topology *topology, hwloc_obj_t obj)
- void hwloc report os error (const char *msg, int line)
- int hwloc hide errors (void)
- struct hwloc_obj * hwloc_insert_object_by_cpuset (struct hwloc_topology *topology, hwloc_obj_t obj, hwloc_report_error_t report_error)
- void hwloc_insert_object_by_parent (struct hwloc_topology *topology, hwloc_obj_t parent, hwloc_obj_t obj)
- static struct hwloc_obj * hwloc_alloc_setup_object (hwloc_obj_type_t type, signed os_index)
- int hwloc fill object sets (hwloc obj t obj)
- static int hwloc_plugin_check_namespace (const char *pluginname, const char *symbol)

19.43.1 Detailed Description

19.43.2 Typedef Documentation

```
19.43.2.1 hwloc_report_error_t
```

```
typedef void(* hwloc_report_error_t) (const char *msg, int line)
```

Type of error callbacks during object insertion.

19.43.3 Function Documentation

19.43.3.1 hwloc__insert_object_by_cpuset()

Add an object to the topology and specify which error callback to use.

Aside from the error callback selection, this function is identical to hwloc_insert_object_by_cpuset()

200 Module Documentation

19.43.3.2 hwloc_alloc_setup_object()

Allocate and initialize an object of the given type and physical index.

```
19.43.3.3 hwloc_fill_object_sets()
```

Setup object cpusets/nodesets by OR'ing its children.

Used when adding an object late in the topology, after propagating sets up and down. The caller should use this after inserting by cpuset (which means the cpusets is already OK). Typical case: PCI backend adding a hostbridge parent.

19.43.3.4 hwloc_hide_errors()

Check whether insertion errors are hidden.

19.43.3.5 hwloc_insert_object_by_cpuset()

Add an object to the topology.

It is sorted along the tree of other objects according to the inclusion of cpusets, to eventually be added as a child of the smallest object including this object.

If the cpuset is empty, the type of the object (and maybe some attributes) must be enough to find where to insert the object. This is especially true for NUMA nodes with memory and no CPUs.

The given object should not have children.

This shall only be called before levels are built.

In case of error, hwloc_report_os_error() is called.

Returns the object on success. Returns NULL and frees obj on error. Returns another object and frees obj if it was merged with an identical pre-existing object.

19.43.3.6 hwloc_insert_object_by_parent()

Insert an object somewhere in the topology.

It is added as the last child of the given parent. The cpuset is completely ignored, so strange objects such as I/O devices should preferably be inserted with this.

When used for "normal" children with cpusets (when importing from XML when duplicating a topology), the caller should make sure children are inserted in order.

The given object may have children.

Remember to call topology_connect() afterwards to fix handy pointers.

19.43.3.7 hwloc_plugin_check_namespace()

Make sure that plugins can lookup core symbols.

This is a sanity check to avoid lazy-lookup failures when libhwloc is loaded within a plugin, and later tries to load its own plugins. This may fail (and abort the program) if libhwloc symbols are in a private namespace.

Returns

0 on success.

-1 if the plugin cannot be successfully loaded. The caller plugin init() callback should return a negative error code as well.

Plugins should call this function in their init() callback to avoid later crashes if lazy symbol resolution is used by the upper layer that loaded hwloc (e.g. OpenCL implementations using dlopen with RTLD_LAZY).

Note

The build system must define HWLOC_INSIDE_PLUGIN if and only if building the caller as a plugin. This function should remain inline so plugins can call it even when they cannot find libhwloc symbols.

19.43.3.8 hwloc_report_os_error()

Report an insertion error from a backend.

202 Module Documentation

19.44 Components and Plugins: PCI functions to be used by components

Functions

- int hwloc insert pci device list (struct hwloc backend *backend, struct hwloc obj *first obj)
- unsigned hwloc pci find cap (const unsigned char *config, unsigned cap)
- int hwloc pci find linkspeed (const unsigned char *config, unsigned offset, float *linkspeed)
- int hwloc pci prepare bridge (hwloc obj t obj, const unsigned char *config)

19.44.1 Detailed Description

19.44.2 Function Documentation

```
19.44.2.1 hwloc_insert_pci_device_list()
```

Insert a list of PCI devices and bridges in the backend topology.

Insert a list of objects (either PCI device or bridges) starting at first_obj (linked by next_sibling in the topology, and ending with NULL). Objects are placed under the right bridges, and the remaining upstream bridges are then inserted in the topology by calling the get_obj_cpuset() callback to find their locality.

19.44.2.2 hwloc_pci_find_cap()

Return the offset of the given capability in the PCI config space buffer.

This function requires a 256-bytes config space. Unknown/unavailable bytes should be set to 0xff.

19.44.2.3 hwloc_pci_find_linkspeed()

Fill linkspeed by reading the PCI config space where PCI_CAP_ID_EXP is at position offset.

Needs 20 bytes of EXP capability block starting at offset in the config space for registers up to link status.

19.44.2.4 hwloc_pci_prepare_bridge()

Modify the PCI device object into a bridge and fill its attribute if a bridge is found in the PCI config space.

This function requires 64 bytes of common configuration header at the beginning of config.

Returns -1 and destroys /p obj if bridge fields are invalid.

Chapter 20

Data Structure Documentation

20.1 hwloc_backend Struct Reference

```
#include <plugins.h>
```

Data Fields

- · unsigned long flags
- int is_custom
- int is_thissystem
- void * private data
- void(* disable)(struct hwloc backend *backend)
- int(* discover)(struct hwloc_backend *backend)
- int(* get_obj_cpuset)(struct hwloc_backend *backend, struct hwloc_backend *caller, struct hwloc_obj *obj, hwloc_bitmap_t cpuset)
- int(* notify_new_object)(struct hwloc_backend *backend, struct hwloc_backend *caller, struct hwloc_obj *obj)

20.1.1 Detailed Description

Discovery backend structure.

A backend is the instantiation of a discovery component. When a component gets enabled for a topology, its instantiate() callback creates a backend.

hwloc_backend_alloc() initializes all fields to default values that the component may change (except "component" and "next") before enabling the backend with hwloc_backend_enable().

20.1.2 Field Documentation

20.1.2.1 disable

```
void(* hwloc_backend::disable) (struct hwloc_backend *backend)
```

Callback for freeing the private_data. May be NULL.

20.1.2.2 discover

```
int(* hwloc_backend::discover) (struct hwloc_backend *backend)
```

Main discovery callback. returns > 0 if it modified the topology tree, -1 on error, 0 otherwise. May be NULL if type is HWLOC_DISC_COMPONENT_TYPE_MISC.

20.1.2.3 flags

```
unsigned long hwloc_backend::flags
```

Backend flags, as an OR'ed set of hwloc_backend_flag_e.

20.1.2.4 get_obj_cpuset

```
int(* hwloc_backend::get_obj_cpuset) (struct hwloc_backend *backend, struct hwloc_backend
*caller, struct hwloc_obj *obj, hwloc_bitmap_t cpuset)
```

Callback used by the PCI backend to retrieve the locality of a PCI object from the OS/cpu backend. May be NULL.

20.1.2.5 is_custom

```
int hwloc_backend::is_custom
```

Backend-specific 'is_custom' property. Shortcut on !strcmp(..->component->name, "custom"). Only the custom component should touch this.

20.1.2.6 is_thissystem

```
int hwloc_backend::is_thissystem
```

Backend-specific 'is_thissystem' property. Set to 0 or 1 if the backend should enforce the thissystem flag when it gets enabled. Set to -1 if the backend doesn't care (default).

20.1.2.7 notify_new_object

```
int(* hwloc_backend::notify_new_object) (struct hwloc_backend *backend, struct hwloc_backend
*caller, struct hwloc_obj *obj)
```

Callback called by backends to notify this backend that a new object was added. returns > 0 if it modified the topology tree, 0 otherwise. May be NULL.

20.1.2.8 private_data

```
void* hwloc_backend::private_data
```

Backend private data, or NULL if none.

The documentation for this struct was generated from the following file:

· plugins.h

20.2 hwloc_obj_attr_u::hwloc_bridge_attr_s Struct Reference

```
#include <hwloc.h>
```

Data Fields

```
union {
    struct hwloc_pcidev_attr_s pci
} upstream
```

```
• hwloc_obj_bridge_type_t upstream_type
```

```
    union {
        struct {
            unsigned short domain
            unsigned char secondary_bus
            unsigned char subordinate_bus
        } pci
    } downstream
```

- hwloc_obj_bridge_type_t downstream_type
- · unsigned depth

20.2.1 Detailed Description

Bridge specific Object Attribues.

20.2.2 Field Documentation

20.2.2.1 depth

unsigned hwloc_obj_attr_u::hwloc_bridge_attr_s::depth

20.2.2.2 domain

 $\verb"unsigned" short hwloc_obj_attr_u::hwloc_bridge_attr_s::domain$

20.2.2.3 downstream

union { ... } hwloc_obj_attr_u::hwloc_bridge_attr_s::downstream

20.2.2.4 downstream_type

 $\verb|hwloc_obj_bridge_type_t| \verb|hwloc_obj_attr_u::hwloc_bridge_attr_s::downstream_type| \\$

20.2.2.5 pci [1/2]

struct { ... } hwloc_obj_attr_u::hwloc_bridge_attr_s::pci

20.2.2.6 pci [2/2]

struct hwloc_pcidev_attr_s hwloc_obj_attr_u::hwloc_bridge_attr_s::pci

20.2.2.7 secondary_bus

unsigned char hwloc_obj_attr_u::hwloc_bridge_attr_s::secondary_bus

20.2.2.8 subordinate_bus

unsigned char hwloc_obj_attr_u::hwloc_bridge_attr_s::subordinate_bus

20.2.2.9 upstream

union { ... } hwloc_obj_attr_u::hwloc_bridge_attr_s::upstream

20.2.2.10 upstream_type

hwloc_obj_bridge_type_t hwloc_obj_attr_u::hwloc_bridge_attr_s::upstream_type

The documentation for this struct was generated from the following file:

· hwloc.h

20.3 hwloc_obj_attr_u::hwloc_cache_attr_s Struct Reference

#include <hwloc.h>

Data Fields

- hwloc_uint64_t size
- unsigned depth
- · unsigned linesize
- · int associativity
- hwloc_obj_cache_type_t type

20.3.1 Detailed Description

Cache-specific Object Attributes.

20.3.2 Field Documentation

20.3.2.1 associativity

int hwloc_obj_attr_u::hwloc_cache_attr_s::associativity

Ways of associativity, -1 if fully associative, 0 if unknown.

20.3.2.2 depth

```
unsigned hwloc_obj_attr_u::hwloc_cache_attr_s::depth
```

Depth of cache (e.g., L1, L2, ...etc.)

20.3.2.3 linesize

unsigned hwloc_obj_attr_u::hwloc_cache_attr_s::linesize

Cache-line size in bytes. 0 if unknown.

20.3.2.4 size

```
\verb|hwloc_uint64_t hwloc_obj_attr_u::hwloc_cache_attr_s::size|\\
```

Size of cache in bytes.

20.3.2.5 type

```
hwloc_obj_cache_type_t hwloc_obj_attr_u::hwloc_cache_attr_s::type
```

Cache type.

The documentation for this struct was generated from the following file:

• hwloc.h

20.4 hwloc_component Struct Reference

```
#include <plugins.h>
```

Data Fields

- unsigned abi
- int(* init)(unsigned long flags)
- void(* finalize)(unsigned long flags)
- hwloc_component_type_t type
- · unsigned long flags
- void * data

20.4.1 Detailed Description

Generic component structure.

Generic components structure, either statically listed by configure in static-components.h or dynamically loaded as a plugin.

20.4.2 Field Documentation

20.4.2.1 abi

unsigned hwloc_component::abi

Component ABI version, set to HWLOC_COMPONENT_ABI.

20.4.2.2 data

void* hwloc_component::data

Component data, pointing to a struct hwloc_disc_component or struct hwloc_xml_component.

20.4.2.3 finalize

```
void(* hwloc_component::finalize) (unsigned long flags)
```

Process-wide component termination callback.

This optional callback is called after unregistering the component from the hwloc core (before unloading the plugin).

flags is always 0 for now.

Note

If the component uses Itdl for loading its own plugins, it should load/unload them only in init() and finalize(), to avoid race conditions with hwloc's use of Itdl.

20.4.2.4 flags

unsigned long hwloc_component::flags

Component flags, unused for now.

20.4.2.5 init

```
int(* hwloc_component::init) (unsigned long flags)
```

Process-wide component initialization callback.

This optional callback is called when the component is registered to the hwloc core (after loading the plugin).

When the component is built as a plugin, this callback should call hwloc_check_plugin_namespace() and return an negative error code on error.

flags is always 0 for now.

Returns

0 on success, or a negative code on error.

Note

If the component uses ltdl for loading its own plugins, it should load/unload them only in init() and finalize(), to avoid race conditions with hwloc's use of ltdl.

20.4.2.6 type

```
hwloc_component_type_t hwloc_component::type
```

Component type.

The documentation for this struct was generated from the following file:

· plugins.h

20.5 hwloc_disc_component Struct Reference

```
#include <plugins.h>
```

Data Fields

- hwloc_disc_component_type_t type
- const char * name
- · unsigned excludes
- struct hwloc_backend *(* instantiate)(struct hwloc_disc_component *component, const void *data1, const void *data2, const void *data3)
- · unsigned priority

20.5.1 Detailed Description

Discovery component structure.

This is the major kind of components, taking care of the discovery. They are registered by generic components, either statically-built or as plugins.

20.5.2 Field Documentation

20.5.2.1 excludes

unsigned hwloc_disc_component::excludes

Component types to exclude, as an OR'ed set of hwloc disc component type e.

For a GLOBAL component, this usually includes all other types (\sim 0).

Other components only exclude types that may bring conflicting topology information. MISC components should likely not be excluded since they usually bring non-primary additional information.

20.5.2.2 instantiate

struct hwloc_backend*(* hwloc_disc_component::instantiate) (struct hwloc_disc_component *component,
const void *data1, const void *data2, const void *data3)

20.5.2.3 name

const char* hwloc_disc_component::name

Name. If this component is built as a plugin, this name does not have to match the plugin filename.

20.5.2.4 priority

unsigned hwloc_disc_component::priority

Component priority. Used to sort topology->components, higher priority first. Also used to decide between two components with the same name.

Usual values are 50 for native OS (or platform) components, 45 for x86, 40 for no-OS fallback, 30 for global components (xml/synthetic/custom), 20 for pci, 10 for other misc components (opencl etc.).

20.5.2.5 type

```
hwloc_disc_component_type_t hwloc_disc_component::type
```

Discovery component type.

The documentation for this struct was generated from the following file:

plugins.h

20.6 hwloc_distances_s Struct Reference

```
#include <hwloc.h>
```

Data Fields

- · unsigned relative depth
- unsigned nbobjs
- float * latency
- float latency_max
- · float latency_base

20.6.1 Detailed Description

Distances between objects.

One object may contain a distance structure describing distances between all its descendants at a given relative depth. If the containing object is the root object of the topology, then the distances are available for all objects in the machine.

If the latency pointer is not NULL, the pointed array contains memory latencies (non-zero values), see below.

In the future, some other types of distances may be considered. In these cases, latency may be NULL.

20.6.2 Field Documentation

20.6.2.1 latency

```
float* hwloc_distances_s::latency
```

Matrix of latencies between objects, stored as a one-dimension array. May be \mathtt{NULL} if the distances considered here are not latencies.

Unless defined by the user, this currently contains latencies between NUMA nodes (as reported in the System Locality Distance Information Table (SLIT) in the ACPI specification), which may or may not be accurate. It corresponds to the latency for accessing the memory of one node from a core in another node.

Values are normalized to get 1.0 as the minimal value in the matrix. Latency from i-th to j-th object is stored in slot i*nbobjs+j.

20.6.2.2 latency_base

```
float hwloc_distances_s::latency_base
```

The multiplier that should be applied to latency matrix to retrieve the original OS-provided latencies. Usually 10 on Linux since ACPI SLIT uses 10 for local latency.

20.6.2.3 latency_max

```
float hwloc_distances_s::latency_max
```

The maximal value in the latency matrix.

20.6.2.4 nbobjs

```
unsigned hwloc_distances_s::nbobjs
```

Number of objects considered in the matrix. It is the number of descendant objects at relative_depth below the containing object. It corresponds to the result of hwloc_get_nbobjs_inside_cpuset_by_depth().

20.6.2.5 relative_depth

```
unsigned hwloc_distances_s::relative_depth
```

Relative depth of the considered objects below the object containing this distance information.

The documentation for this struct was generated from the following file:

• hwloc.h

20.7 hwloc_obj_attr_u::hwloc_group_attr_s Struct Reference

#include <hwloc.h>

Data Fields

· unsigned depth

20.7.1 Detailed Description

Group-specific Object Attributes.

20.7.2 Field Documentation

20.7.2.1 depth

unsigned hwloc_obj_attr_u::hwloc_group_attr_s::depth

Depth of group object.

The documentation for this struct was generated from the following file:

· hwloc.h

20.8 hwloc_obj Struct Reference

#include <hwloc.h>

Data Fields

- hwloc_obj_type_t type
- unsigned os_index
- char * name
- struct hwloc_obj_memory_s memory
- union hwloc_obj_attr_u * attr
- · unsigned depth
- unsigned logical_index
- signed os_level
- struct hwloc_obj * next_cousin
- struct hwloc_obj * prev_cousin
- struct hwloc_obj * parent
- unsigned sibling_rank
- struct hwloc_obj * next_sibling
- struct hwloc_obj * prev_sibling
- · unsigned arity
- struct hwloc_obj ** children
- struct hwloc_obj * first_child
- struct hwloc_obj * last_child
- void * userdata
- hwloc_cpuset_t cpuset
- hwloc_cpuset_t complete_cpuset
- · hwloc_cpuset_t online_cpuset
- hwloc_cpuset_t allowed_cpuset
- hwloc_nodeset_t nodeset
- hwloc_nodeset_t complete_nodeset
- hwloc_nodeset_t allowed_nodeset
- struct hwloc_distances_s ** distances
- · unsigned distances count
- struct hwloc_obj_info_s * infos
- unsigned infos count
- int symmetric_subtree

20.8.1 Detailed Description

Structure of a topology object.

Applications must not modify any field except hwloc_obj.userdata.

20.8.2 Field Documentation

20.8.2.1 allowed_cpuset

hwloc_cpuset_t hwloc_obj::allowed_cpuset

The CPU set of allowed logical processors.

This includes the CPUs contained in this object which are allowed for binding, i.e. passing them to the hwloc binding functions should not return permission errors. This is usually restricted by administration rules. Some of them may however be offline so binding to them may still not be possible, see online_cpuset.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

20.8.2.2 allowed_nodeset

hwloc_nodeset_t hwloc_obj::allowed_nodeset

The set of allowed NUMA memory nodes.

This includes the NUMA memory nodes contained in this object which are allowed for memory allocation, i.← e. passing them to NUMA node-directed memory allocation should not return permission errors. This is usually restricted by administration rules.

If there are no NUMA nodes in the machine, all the memory is close to this object, so allowed_nodeset is full.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

20.8.2.3 arity

unsigned hwloc_obj::arity

Number of children.

20.8.2.4 attr

```
union hwloc_obj_attr_u* hwloc_obj::attr
```

Object type-specific Attributes, may be NULL if no attribute value was found.

20.8.2.5 children

```
struct hwloc_obj** hwloc_obj::children
```

Children, children[0 .. arity -1].

20.8.2.6 complete_cpuset

```
hwloc_cpuset_t hwloc_obj::complete_cpuset
```

The complete CPU set of logical processors of this object,.

This includes not only the same as the cpuset field, but also some CPUs for which topology information is unknown or incomplete, and the CPUs that are ignored when the HWLOC_TOPOLOGY_FLAG_WHOLE_SYSTEM flag is not set. Thus no corresponding PU object may be found in the topology, because the precise position is undefined. It is however known that it would be somewhere under this object.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

20.8.2.7 complete_nodeset

```
hwloc_nodeset_t hwloc_obj::complete_nodeset
```

The complete NUMA node set of this object,.

This includes not only the same as the nodeset field, but also some NUMA nodes for which topology information is unknown or incomplete, and the nodes that are ignored when the HWLOC_TOPOLOGY_FLAG_WHOLE_SYST
EM flag is not set. Thus no corresponding NUMA node object may be found in the topology, because the precise position is undefined. It is however known that it would be somewhere under this object.

If there are no NUMA nodes in the machine, all the memory is close to this object, so <code>complete_nodeset</code> is full.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

20.8.2.8 cpuset

hwloc_cpuset_t hwloc_obj::cpuset

CPUs covered by this object.

This is the set of CPUs for which there are PU objects in the topology under this object, i.e. which are known to be physically contained in this object and known how (the children path between this object and the PU objects).

If the HWLOC_TOPOLOGY_FLAG_WHOLE_SYSTEM configuration flag is set, some of these CPUs may be offline, or not allowed for binding, see online_cpuset and allowed_cpuset.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

20.8.2.9 depth

unsigned hwloc_obj::depth

Vertical index in the hierarchy. If the topology is symmetric, this is equal to the parent depth plus one, and also equal to the number of parent/child links from the root object to here.

20.8.2.10 distances

```
struct hwloc_distances_s** hwloc_obj::distances
```

Distances between all objects at same depth below this object.

20.8.2.11 distances_count

unsigned hwloc_obj::distances_count

20.8.2.12 first_child

struct hwloc_obj* hwloc_obj::first_child

First child.

20.8.2.13 infos

```
struct hwloc_obj_info_s* hwloc_obj::infos
```

Array of stringified info type=name.

20.8.2.14 infos_count

unsigned hwloc_obj::infos_count

Size of infos array.

20.8.2.15 last_child

```
struct hwloc_obj* hwloc_obj::last_child
```

Last child.

20.8.2.16 logical_index

```
{\tt unsigned\ hwloc\_obj::logical\_index}
```

Horizontal index in the whole list of similar objects, hence guaranteed unique across the entire machine. Could be a "cousin_rank" since it's the rank within the "cousin" list below.

20.8.2.17 memory

```
struct hwloc_obj_memory_s hwloc_obj::memory
```

Memory attributes.

20.8.2.18 name

```
char* hwloc_obj::name
```

Object-specific name if any. Mostly used for identifying OS devices and Misc objects where a name string is more useful than numerical indexes.

20.8.2.19 next_cousin

```
struct hwloc_obj* hwloc_obj::next_cousin
```

Next object of same type and depth.

20.8.2.20 next_sibling

```
struct hwloc_obj* hwloc_obj::next_sibling
```

Next object below the same parent.

20.8.2.21 nodeset

```
hwloc_nodeset_t hwloc_obj::nodeset
```

NUMA nodes covered by this object or containing this object.

This is the set of NUMA nodes for which there are NUMA node objects in the topology under or above this object, i.e. which are known to be physically contained in this object or containing it and known how (the children path between this object and the NUMA node objects).

In the end, these nodes are those that are close to the current object.

If the HWLOC_TOPOLOGY_FLAG_WHOLE_SYSTEM configuration flag is set, some of these nodes may not be allowed for allocation, see allowed_nodeset.

If there are no NUMA nodes in the machine, all the memory is close to this object, so nodeset is full.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

20.8.2.22 online_cpuset

```
hwloc_cpuset_t hwloc_obj::online_cpuset
```

The CPU set of online logical processors.

This includes the CPUs contained in this object that are online, i.e. draw power and can execute threads. It may however not be allowed to bind to them due to administration rules, see allowed_cpuset.

Note

Its value must not be changed, hwloc_bitmap_dup() must be used instead.

```
20.8.2.23 os_index
```

```
unsigned hwloc_obj::os_index
```

OS-provided physical index number. It is not guaranteed unique across the entire machine, except for PUs and NUMA nodes.

```
20.8.2.24 os_level
```

```
signed hwloc_obj::os_level
```

OS-provided physical level, -1 if unknown or meaningless.

20.8.2.25 parent

```
struct hwloc_obj* hwloc_obj::parent
```

Parent, NULL if root (system object)

20.8.2.26 prev_cousin

```
struct hwloc_obj* hwloc_obj::prev_cousin
```

Previous object of same type and depth.

20.8.2.27 prev_sibling

```
struct hwloc_obj* hwloc_obj::prev_sibling
```

Previous object below the same parent.

20.8.2.28 sibling_rank

unsigned hwloc_obj::sibling_rank

Index in parent's children[] array.

20.8.2.29 symmetric_subtree

```
int hwloc_obj::symmetric_subtree
```

Set if the subtree of objects below this object is symmetric, which means all children and their children have identical subtrees. If set in the topology root object, Istopo may export the topology as a synthetic string.

20.8.2.30 type

```
hwloc_obj_type_t hwloc_obj::type
```

Type of object.

20.8.2.31 userdata

```
void* hwloc_obj::userdata
```

Application-given private data pointer, initialized to NULL, use it as you wish. See <a href="https://hww.nuse.com/hw/hw/com/hw/

The documentation for this struct was generated from the following file:

· hwloc.h

20.9 hwloc_obj_attr_u Union Reference

```
#include <hwloc.h>
```

Data Structures

- struct hwloc_bridge_attr_s
- struct hwloc_cache_attr_s
- struct hwloc_group_attr_s
- struct hwloc_osdev_attr_s
- struct hwloc_pcidev_attr_s

Data Fields

- struct hwloc_obj_attr_u::hwloc_cache_attr_s cache
- struct hwloc_obj_attr_u::hwloc_group_attr_s group
- struct hwloc_obj_attr_u::hwloc_pcidev_attr_s pcidev
- struct hwloc_obj_attr_u::hwloc_bridge_attr_s bridge
- struct hwloc_obj_attr_u::hwloc_osdev_attr_s osdev

20.9.1 Detailed Description

Object type-specific Attributes.

20.9.2 Field Documentation

20.9.2.1 bridge

struct hwloc_obj_attr_u::hwloc_bridge_attr_s hwloc_obj_attr_u::bridge

20.9.2.2 cache

struct hwloc_obj_attr_u::hwloc_cache_attr_s hwloc_obj_attr_u::cache

20.9.2.3 group

struct hwloc_obj_attr_u::hwloc_group_attr_s hwloc_obj_attr_u::group

20.9.2.4 osdev

struct hwloc_obj_attr_u::hwloc_osdev_attr_s hwloc_obj_attr_u::osdev

20.9.2.5 pcidev

struct hwloc_obj_attr_u::hwloc_pcidev_attr_s hwloc_obj_attr_u::pcidev

The documentation for this union was generated from the following file:

• hwloc.h

20.10 hwloc_obj_info_s Struct Reference

#include <hwloc.h>

Data Fields

- char * name
- char * value

20.10.1 Detailed Description

Object info.

See also

Consulting and Adding Key-Value Info Attributes

20.10.2 Field Documentation

```
20.10.2.1 name
```

char* hwloc_obj_info_s::name

Info name.

20.10.2.2 value

```
char* hwloc_obj_info_s::value
```

Info value.

The documentation for this struct was generated from the following file:

· hwloc.h

20.11 hwloc_obj_memory_s::hwloc_obj_memory_page_type_s Struct Reference

```
#include <hwloc.h>
```

Data Fields

- hwloc_uint64_t size
- hwloc_uint64_t count

20.11.1 Detailed Description

Array of local memory page types, \mathtt{NULL} if no local memory and $\mathtt{page_types}$ is 0.

The array is sorted by increasing size fields. It contains page_types_len slots.

20.11.2 Field Documentation

20.11.2.1 count

hwloc_uint64_t hwloc_obj_memory_s::hwloc_obj_memory_page_type_s::count

Number of pages of this size.

20.11.2.2 size

 $\verb|hwloc_uint64_t hwloc_obj_memory_s:: hwloc_obj_memory_page_type_s:: size|\\$

Size of pages.

The documentation for this struct was generated from the following file:

hwloc.h

20.12 hwloc_obj_memory_s Struct Reference

```
#include <hwloc.h>
```

Data Structures

• struct hwloc_obj_memory_page_type_s

Data Fields

- hwloc_uint64_t total_memory
- hwloc_uint64_t local_memory
- unsigned page_types_len
- struct hwloc_obj_memory_s::hwloc_obj_memory_page_type_s * page_types

20.12.1 Detailed Description

Object memory.

20.12.2 Field Documentation

```
20.12.2.1 local_memory
```

hwloc_uint64_t hwloc_obj_memory_s::local_memory

Local memory (in bytes)

20.12.2.2 page_types

struct hwloc_obj_memory_s::hwloc_obj_memory_page_type_s * hwloc_obj_memory_s::page_types

20.12.2.3 page_types_len

unsigned hwloc_obj_memory_s::page_types_len

Size of array page_types.

20.12.2.4 total_memory

hwloc_uint64_t hwloc_obj_memory_s::total_memory

Total memory (in bytes) in this object and its children.

The documentation for this struct was generated from the following file:

• hwloc.h

20.13 hwloc_obj_attr_u::hwloc_osdev_attr_s Struct Reference

#include <hwloc.h>

Data Fields

hwloc_obj_osdev_type_t type

20.13.1 Detailed Description

OS Device specific Object Attributes.

20.13.2 Field Documentation

20.13.2.1 type

```
hwloc_obj_osdev_type_t hwloc_obj_attr_u::hwloc_osdev_attr_s::type
```

The documentation for this struct was generated from the following file:

· hwloc.h

20.14 hwloc_obj_attr_u::hwloc_pcidev_attr_s Struct Reference

#include <hwloc.h>

Data Fields

- unsigned short domain
- unsigned char bus
- unsigned char dev
- unsigned char func
- unsigned short class_id
- unsigned short vendor_id
- unsigned short device_id
- unsigned short subvendor_id
- unsigned short subdevice_id
- unsigned char revision
- · float linkspeed

20.14.1 Detailed Description

PCI Device specific Object Attributes.

20.14.2 Field Documentation

20.14.2.1 bus

unsigned char hwloc_obj_attr_u::hwloc_pcidev_attr_s::bus

20.14.2.2 class_id

 $\verb"unsigned short hwloc_obj_attr_u::hwloc_pcidev_attr_s::class_id$

20.14.2.3 dev

unsigned char hwloc_obj_attr_u::hwloc_pcidev_attr_s::dev

20.14.2.4 device_id

unsigned short hwloc_obj_attr_u::hwloc_pcidev_attr_s::device_id

20.14.2.5 domain

 ${\tt unsigned \ short \ hwloc_obj_attr_u::hwloc_pcidev_attr_s::domain}$

20.14.2.6 func

 ${\tt unsigned\ char\ hwloc_obj_attr_u::hwloc_pcidev_attr_s::func}$

20.14.2.7 linkspeed

float hwloc_obj_attr_u::hwloc_pcidev_attr_s::linkspeed

20.14.2.8 revision

unsigned char hwloc_obj_attr_u::hwloc_pcidev_attr_s::revision

20.14.2.9 subdevice_id

unsigned short hwloc_obj_attr_u::hwloc_pcidev_attr_s::subdevice_id

20.14.2.10 subvendor_id

unsigned short hwloc_obj_attr_u::hwloc_pcidev_attr_s::subvendor_id

20.14.2.11 vendor_id

 $\verb"unsigned" short hwloc_obj_attr_u::hwloc_pcidev_attr_s::vendor_id$

The documentation for this struct was generated from the following file:

· hwloc.h

20.15 hwloc_topology_cpubind_support Struct Reference

#include <hwloc.h>

Data Fields

- · unsigned char set_thisproc_cpubind
- · unsigned char get_thisproc_cpubind
- · unsigned char set_proc_cpubind
- unsigned char get_proc_cpubind
- unsigned char set_thisthread_cpubind
- · unsigned char get thisthread cpubind
- unsigned char set_thread_cpubind
- · unsigned char get_thread_cpubind
- unsigned char get_thisproc_last_cpu_location
- unsigned char get_proc_last_cpu_location
- unsigned char get_thisthread_last_cpu_location

20.15.1 Detailed Description

Flags describing actual PU binding support for this topology.

A flag may be set even if the feature isn't supported in all cases (e.g. binding to random sets of non-contiguous objects).

20.15.2 Field Documentation

20.15.2.1 get_proc_cpubind

unsigned char hwloc_topology_cpubind_support::get_proc_cpubind

Getting the binding of a whole given process is supported.

20.15.2.2 get_proc_last_cpu_location

 $unsigned\ char\ hwloc_topology_cpubind_support::get_proc_last_cpu_location$

Getting the last processors where a whole process ran is supported

20.15.2.3 get_thisproc_cpubind

unsigned char hwloc_topology_cpubind_support::get_thisproc_cpubind

Getting the binding of the whole current process is supported.

20.15.2.4 get_thisproc_last_cpu_location

unsigned char hwloc_topology_cpubind_support::get_thisproc_last_cpu_location

Getting the last processors where the whole current process ran is supported

20.15.2.5 get_thisthread_cpubind

unsigned char hwloc_topology_cpubind_support::get_thisthread_cpubind

Getting the binding of the current thread only is supported.

20.15.2.6 get_thisthread_last_cpu_location

 $\verb"unsigned" char hwloc_topology_cpubind_support::get_thisthread_last_cpu_location$

Getting the last processors where the current thread ran is supported

20.15.2.7 get_thread_cpubind

unsigned char hwloc_topology_cpubind_support::get_thread_cpubind

Getting the binding of a given thread only is supported.

20.15.2.8 set_proc_cpubind

unsigned char hwloc_topology_cpubind_support::set_proc_cpubind

Binding a whole given process is supported.

20.15.2.9 set_thisproc_cpubind

unsigned char hwloc_topology_cpubind_support::set_thisproc_cpubind

Binding the whole current process is supported.

20.15.2.10 set_thisthread_cpubind

unsigned char hwloc_topology_cpubind_support::set_thisthread_cpubind

Binding the current thread only is supported.

20.15.2.11 set_thread_cpubind

unsigned char hwloc_topology_cpubind_support::set_thread_cpubind

Binding a given thread only is supported.

The documentation for this struct was generated from the following file:

· hwloc.h

20.16 hwloc_topology_diff_u::hwloc_topology_diff_generic_s Struct Reference

#include <diff.h>

Data Fields

- hwloc_topology_diff_type_t type
- union hwloc_topology_diff_u * next

20.16.1 Field Documentation

20.16.1.1 next

union hwloc_topology_diff_u* hwloc_topology_diff_u::hwloc_topology_diff_generic_s::next

```
20.16.1.2 type
```

hwloc_topology_diff_type_t hwloc_topology_diff_u::hwloc_topology_diff_generic_s::type

The documentation for this struct was generated from the following file:

· diff.h


```
#include <diff.h>
```

Data Fields

hwloc_topology_diff_obj_attr_type_t type

20.17.1 Field Documentation

20.17.1.1 type

The documentation for this struct was generated from the following file:

· diff.h

20.18 hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s Struct Reference

```
#include <diff.h>
```

Data Fields

- hwloc_topology_diff_type_t type
- union hwloc_topology_diff_u * next
- unsigned obj_depth
- unsigned obj_index
- union hwloc_topology_diff_obj_attr_u diff

20.18.1 Field Documentation

20.18.1.1 diff

20.18.1.2 next

 $union \ \ hwloc_topology_diff_u* \ hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s::next$

20.18.1.3 obj_depth

unsigned hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s::obj_depth

20.18.1.4 obj_index

unsigned hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s::obj_index

20.18.1.5 type

hwloc_topology_diff_type_t hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s::type

The documentation for this struct was generated from the following file:

• diff.h

20.19 hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s Struct Reference

#include <diff.h>

Data Fields

- hwloc_topology_diff_obj_attr_type_t type
- char * name
- char * oldvalue
- char * newvalue

20.19.1 Detailed Description

String attribute modification with an optional name.

20.19.2 Field Documentation

20.19.2.1 name

char* hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s::name

20.19.2.2 newvalue

char* hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s::newvalue

20.19.2.3 oldvalue

 $\verb|char*| hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s::oldvalue | for the content of the content of$

20.19.2.4 type

 $\label{loc_topology_diff_obj_attr_type_t} $$ hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_$$ attr_string_s::type $$ for each of the context of$

The documentation for this struct was generated from the following file:

• diff.h

20.20 hwloc_topology_diff_obj_attr_u Union Reference

#include <diff.h>

Data Structures

- struct hwloc_topology_diff_obj_attr_generic_s
- struct hwloc_topology_diff_obj_attr_string_s
- struct hwloc_topology_diff_obj_attr_uint64_s

Data Fields

- struct hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_generic_s generic
- struct hwloc topology diff obj attr u::hwloc topology diff obj attr uint64 s uint64
- struct hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s string

20.20.1 Detailed Description

One object attribute difference.

20.20.2 Field Documentation

20.20.2.1 generic

 $struct\ hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_generic_s\ hwloc_topology \\ _diff_obj_attr_u::generic$

20.20.2.2 string

 $struct\ hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_string_s\ hwloc_topology_\leftrightarrow diff_obj_attr_u::string$

20.20.2.3 uint64

 $struct\ hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_uint64_s\ hwloc_topology_\leftrightarrow diff_obj_attr_u::uint64$

The documentation for this union was generated from the following file:

diff.h

20.21 hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_uint64_s Struct Reference

#include <diff.h>

Data Fields

- hwloc_topology_diff_obj_attr_type_t type
- hwloc_uint64_t index
- hwloc_uint64_t oldvalue
- hwloc_uint64_t newvalue

20.21.1 Detailed Description

Integer attribute modification with an optional index.

20.21.2 Field Documentation

20.21.2.1 index

hwloc_uint64_t hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_obj_attr_uint64_s::index

20.21.2.2 newvalue

 $\verb|hwloc_uint64_t| hwloc_topology_diff_obj_attr_u:: \verb|hwloc_topology_diff_obj_attr_uint64_s:: new value | hwloc_topology_diff_obj_attr_uint64_s: new value | h$

20.21.2.3 oldvalue

 $\verb|hwloc_uint64_t| hwloc_topology_diff_obj_attr_u:: \verb|hwloc_topology_diff_obj_attr_uint64_s:: oldvalue| left and the left$

20.21.2.4 type

The documentation for this struct was generated from the following file:

· diff.h

20.22 hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s Struct Reference

#include <diff.h>

Data Fields

- hwloc_topology_diff_type_t type
- union hwloc_topology_diff_u * next
- · unsigned obj_depth
- unsigned obj_index

20.22.1 Field Documentation

20.22.1.1 next

union hwloc_topology_diff_u* hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s::next

20.22.1.2 obj_depth

unsigned hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s::obj_depth

20.22.1.3 obj_index

unsigned hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s::obj_index

20.22.1.4 type

hwloc_topology_diff_type_t hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s::type

The documentation for this struct was generated from the following file:

· diff.h

20.23 hwloc_topology_diff_u Union Reference

#include <diff.h>

Data Structures

- struct hwloc_topology_diff_generic_s
- struct hwloc_topology_diff_obj_attr_s
- struct hwloc_topology_diff_too_complex_s

Data Fields

- struct hwloc_topology_diff_u::hwloc_topology_diff_generic_s generic
- struct hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s obj_attr
- struct hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s too_complex

20.23.1 Detailed Description

One element of a difference list between two topologies.

20.23.2 Field Documentation

20.23.2.1 generic

 $\verb|struct hwloc_topology_diff_u::hwloc_topology_diff_generic_s hwloc_topology_diff_u::generic_s hw$

20.23.2.2 obj_attr

 $\verb|struct| hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s| hwloc_topology_diff_u::obj_attr_s| hwloc_topology_diff$

20.23.2.3 too_complex

 $struct\ hwloc_topology_diff_u::hwloc_topology_diff_too_complex_s\ hwloc_topology_diff_u::too_{\hookleftarrow}\ complex$

The documentation for this union was generated from the following file:

• diff.h

20.24 hwloc_topology_discovery_support Struct Reference

#include <hwloc.h>

Data Fields

· unsigned char pu

20.24.1 Detailed Description

Flags describing actual discovery support for this topology.

20.24.2 Field Documentation

20.24.2.1 pu

unsigned char hwloc_topology_discovery_support::pu

Detecting the number of PU objects is supported.

The documentation for this struct was generated from the following file:

· hwloc.h

20.25 hwloc_topology_membind_support Struct Reference

#include <hwloc.h>

Data Fields

- unsigned char set_thisproc_membind
- unsigned char get_thisproc_membind
- unsigned char set_proc_membind
- unsigned char get_proc_membind
- unsigned char set_thisthread_membind
- unsigned char get_thisthread_membind
- unsigned char set_area_membind
- unsigned char get_area_membind
- unsigned char alloc_membind
- unsigned char firsttouch_membind
- unsigned char bind_membind
- unsigned char interleave_membind
- unsigned char replicate_membind
- unsigned char nexttouch_membind
- · unsigned char migrate_membind
- unsigned char get_area_memlocation

20.25.1 Detailed Description

Flags describing actual memory binding support for this topology.

A flag may be set even if the feature isn't supported in all cases (e.g. binding to random sets of non-contiguous objects).

20.25.2 Field Documentation

20.25.2.1 alloc_membind

unsigned char hwloc_topology_membind_support::alloc_membind

Allocating a bound memory area is supported.

20.25.2.2 bind_membind

unsigned char hwloc_topology_membind_support::bind_membind

Bind policy is supported.

20.25.2.3 firsttouch_membind

unsigned char hwloc_topology_membind_support::firsttouch_membind

First-touch policy is supported.

20.25.2.4 get_area_membind

 ${\tt unsigned\ char\ hwloc_topology_membind_support::get_area_membind}$

Getting the binding of a given memory area is supported.

20.25.2.5 get_area_memlocation

 ${\tt unsigned \ char \ hwloc_topology_membind_support::get_area_memlocation}$

Getting the last NUMA nodes where a memory area was allocated is supported

20.25.2.6 get_proc_membind

unsigned char hwloc_topology_membind_support::get_proc_membind

Getting the binding of a whole given process is supported.

20.25.2.7 get_thisproc_membind

unsigned char hwloc_topology_membind_support::get_thisproc_membind

Getting the binding of the whole current process is supported.

20.25.2.8 get_thisthread_membind

 $\verb"unsigned" char hwloc_topology_membind_support::get_this thread_membind$

Getting the binding of the current thread only is supported.

20.25.2.9 interleave_membind

unsigned char hwloc_topology_membind_support::interleave_membind

Interleave policy is supported.

20.25.2.10 migrate_membind

unsigned char hwloc_topology_membind_support::migrate_membind

Migration flags is supported.

20.25.2.11 nexttouch_membind

 ${\tt unsigned\ char\ hwloc_topology_membind_support::nexttouch_membind}$

Next-touch migration policy is supported.

20.25.2.12 replicate_membind

 ${\tt unsigned \ char \ hwloc_topology_membind_support::replicate_membind}$

Replication policy is supported.

20.25.2.13 set_area_membind

unsigned char hwloc_topology_membind_support::set_area_membind

Binding a given memory area is supported.

20.25.2.14 set_proc_membind

 $unsigned \ char \ hwloc_topology_membind_support::set_proc_membind$

Binding a whole given process is supported.

20.25.2.15 set_thisproc_membind

unsigned char hwloc_topology_membind_support::set_thisproc_membind

Binding the whole current process is supported.

20.25.2.16 set_thisthread_membind

unsigned char hwloc_topology_membind_support::set_thisthread_membind

Binding the current thread only is supported.

The documentation for this struct was generated from the following file:

· hwloc.h

20.26 hwloc_topology_support Struct Reference

#include <hwloc.h>

Data Fields

- struct hwloc_topology_discovery_support * discovery
- struct hwloc_topology_cpubind_support * cpubind
- struct hwloc_topology_membind_support * membind

20.26.1 Detailed Description

Set of flags describing actual support for this topology.

This is retrieved with https://hww.nct.noi.org/nct/ and will be valid until the topology object is destroyed. Note: the values are correct only after discovery.

20.26.2 Field Documentation

20.26.2.1 cpubind

 $\verb|struct hwloc_topology_cpubind_support*| hwloc_topology_support::cpubind|$

20.26.2.2 discovery

struct hwloc_topology_discovery_support* hwloc_topology_support::discovery

20.26.2.3 membind

struct hwloc_topology_membind_support* hwloc_topology_support::membind

The documentation for this struct was generated from the following file:

· hwloc.h

Index

API version, 75	children
HWLOC_API_VERSION, 75	hwloc_obj, 216
HWLOC_COMPONENT_ABI, 75	class_id
hwloc_get_api_version, 76	hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227
abi	complete_cpuset
hwloc_component, 209	hwloc_obj, 216
alloc_membind	complete_nodeset
hwloc_topology_membind_support, 239	hwloc_obj, 216
allowed_cpuset	Components and Plugins: Core functions to be used by
hwloc_obj, 215	components, 199
allowed_nodeset	hwlocinsert_object_by_cpuset, 199
hwloc_obj, 215	hwloc_alloc_setup_object, 199
arity	hwloc_fill_object_sets, 200
hwloc_obj, 215	hwloc_hide_errors, 200
associativity	hwloc_insert_object_by_cpuset, 200
hwloc_obj_attr_u::hwloc_cache_attr_s, 207	hwloc_insert_object_by_parent, 200
attr	hwloc_plugin_check_namespace, 201
hwloc_obj, 215	hwloc_report_error_t, 199
	hwloc_report_os_error, 201
bind_membind	Components and Plugins: Discovery backends, 196
hwloc_topology_membind_support, 239	hwloc_backend_alloc, 196
bridge	hwloc_backend_enable, 196
hwloc_obj_attr_u, 222	hwloc_backend_flag_e, 196
Building Custom Topologies, 122	hwloc backends get obj cpuset, 197
hwloc_custom_insert_group_object_by_parent,	hwloc_backends_notify_new_object, 197
122	Components and Plugins: Discovery components, 195
hwloc_custom_insert_topology, 122	hwloc_disc_component_type_e, 195
bus	hwloc_disc_component_type_t, 195
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 226	Components and Plugins: Generic components, 198
CPU and node sets of entire topologies, 145	hwloc_component_type_e, 198
hwloc_topology_get_allowed_cpuset, 145	hwloc_component_type_t, 198
hwloc_topology_get_allowed_nodeset, 145	Components and Plugins: PCI functions to be used by
hwloc_topology_get_complete_cpuset, 145	components, 202
hwloc_topology_get_complete_nodeset, 146	hwloc_insert_pci_device_list, 202
hwloc_topology_get_complete_nodeset, 146	hwloc_pci_find_cap, 202
hwloc_topology_get_topology_cpuset, 146	hwloc_pci_find_linkspeed, 202
hwloc_topology_get_topology_nodeset, 147	hwloc_pci_prepare_bridge, 202
CPU binding, 102	Consulting and Adding Key-Value Info Attributes, 101
hwloc cpubind flags t, 103	hwloc_obj_add_info, 101
hwloc_get_cpubind, 104	hwloc_obj_get_info_by_name, 101
hwloc_get_last_cpu_location, 104	Converting between CPU sets and node sets, 148
hwloc_get_proc_cpubind, 104	hwloc_cpuset_from_nodeset, 148
hwloc_get_proc_last_cpu_location, 104	hwloc_cpuset_from_nodeset_strict, 148
hwloc_get_thread_cpubind, 105	hwloc_cpuset_to_nodeset, 148
hwloc_set_cpubind, 105	hwloc_cpuset_to_nodeset_strict, 149
hwloc set proc cpubind, 105	Converting between Object Types, Sets and Attributes,
hwloc set thread cpubind, 106	and Strings, 98
cache	hwloc_obj_attr_snprintf, 98
hwloc_obj_attr_u, 222	hwloc_obj_cpuset_snprintf, 98
55_55_46,	oooopooot_onpiniti,

hwloc_obj_type_snprintf, 98 hwloc_obj_type_sscanf, 99 hwloc_obj_type_string, 99	hwloc_topology_set_userdata_import_callback, 126
count	finalize
hwloc_obj_memory_s::hwloc_obj_memory_	hwloc_component, 209
page_type_s, 224	Finding I/O objects, 152
cpubind	hwloc_bridge_covers_pcibus, 152
hwloc_topology_support, 241	hwloc_get_hostbridge_by_pcibus, 152
cpuset	hwloc_get_next_bridge, 152
hwloc_obj, 216	hwloc_get_next_osdev, 152
p	hwloc_get_next_pcidev, 153
data	hwloc_get_non_io_ancestor_obj, 153
hwloc_component, 209	hwloc_get_pcidev_by_busid, 153
depth	hwloc_get_pcidev_by_busidstring, 153
hwloc_obj, 217	Finding Objects covering at least CPU set, 134
hwloc_obj_attr_u::hwloc_bridge_attr_s, 206	hwloc_get_child_covering_cpuset, 134
hwloc_obj_attr_u::hwloc_cache_attr_s, 207	hwloc_get_next_obj_covering_cpuset_by_depth,
hwloc obj attr u::hwloc group attr s, 214	134
dev	hwloc_get_next_obj_covering_cpuset_by_type,
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227	134
device_id	hwloc_get_obj_covering_cpuset, 135
hwloc obj attr u::hwloc pcidev attr s, 227	Finding Objects inside a CPU set, 130
diff	hwloc_get_first_largest_obj_inside_cpuset, 130
hwloc_topology_diff_u::hwloc_topology_diff_obj↔	hwloc_get_largest_objs_inside_cpuset, 130
_attr_s, 232	hwloc_get_nbobjs_inside_cpuset_by_depth, 131
disable	hwloc_get_nbobjs_inside_cpuset_by_type, 131
hwloc_backend, 203	hwloc_get_next_obj_inside_cpuset_by_depth, 13
discover	hwloc_get_next_obj_inside_cpuset_by_type, 132
hwloc_backend, 204	hwloc_get_obj_index_inside_cpuset, 132
discovery	hwloc_get_obj_inside_cpuset_by_depth, 132
hwloc_topology_support, 241	hwloc_get_obj_inside_cpuset_by_type, 133
distances	Finding objects, miscellaneous helpers, 140
hwloc_obj, 217	hwloc_get_closest_objs, 140
	hwloc_get_numanode_obj_by_os_index, 140
distances_count hwloc_obj, 217	
Distributing items over a topology, 143	hwloc_get_obj_below_array_by_type, 141
hwloc distrib, 143	hwloc_get_obj_below_by_type, 141
	hwloc_get_pu_obj_by_os_index, 141
hwloc_distrib_flags_e, 143	first_child
domain	hwloc_obj, 217
hwloc_obj_attr_u::hwloc_bridge_attr_s, 206	firsttouch_membind
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227	hwloc_topology_membind_support, 239
downstream	flags
hwloc_obj_attr_u::hwloc_bridge_attr_s, 206	hwloc_backend, 204
downstream_type	hwloc_component, 209
hwloc_obj_attr_u::hwloc_bridge_attr_s, 206	func
eveludes	hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227
excludes	
hwloc_disc_component, 211	generic
Exporting Topologies to Synthetic, 128	hwloc_topology_diff_obj_attr_u, 234
hwloc_topology_export_synthetic, 128	hwloc_topology_diff_u, 237
hwloc_topology_export_synthetic_flags_e, 128	get_area_membind
Exporting Topologies to XML, 124	hwloc_topology_membind_support, 239
hwloc_export_obj_userdata, 124	get_area_memlocation
hwloc_export_obj_userdata_base64, 124	hwloc_topology_membind_support, 239
hwloc_free_xmlbuffer, 125	get_obj_cpuset
hwloc_topology_export_xml, 125	hwloc_backend, 204
hwloc_topology_export_xmlbuffer, 125	get_proc_cpubind
hwloc_topology_set_userdata_export_callback,	hwloc_topology_cpubind_support, 229
126	get_proc_last_cpu_location

hwloc_topology_cpubind_support, 229 get_proc_membind	Components and Plugins: Discovery backends,
hwloc_topology_membind_support, 239	hwloc_backends_notify_new_object
get_thisproc_cpubind	Components and Plugins: Discovery backends,
hwloc_topology_cpubind_support, 229	197
<pre>get_thisproc_last_cpu_location hwloc_topology_cpubind_support, 229</pre>	hwloc_bitmap_allbut The bitmap API, 156
get_thisproc_membind	hwloc_bitmap_alloc
hwloc_topology_membind_support, 239	The bitmap API, 156
get_thisthread_cpubind	hwloc_bitmap_alloc_full
hwloc_topology_cpubind_support, 229	The bitmap API, 157
get_thisthread_last_cpu_location	hwloc_bitmap_and
hwloc_topology_cpubind_support, 229	The bitmap API, 157
get_thisthread_membind	hwloc_bitmap_andnot
hwloc_topology_membind_support, 240 get_thread_cpubind	The bitmap API, 157
hwloc topology cpubind support, 229	hwloc_bitmap_asprintf The bitmap API, 157
group	hwloc bitmap clr
hwloc_obj_attr_u, 222	The bitmap API, 157
_	hwloc_bitmap_clr_range
HWLOC_API_VERSION	The bitmap API, 158
API version, 75	hwloc_bitmap_compare
HWLOC_COMPONENT_ABI API version, 75	The bitmap API, 158
hwlocinsert_object_by_cpuset	hwloc_bitmap_compare_first
Components and Plugins: Core functions to be	The bitmap API, 158
used by components, 199	hwloc_bitmap_copy
hwloc_alloc	The bitmap API, 158
Memory binding, 110	hwloc_bitmap_dup
hwloc_alloc_membind	The bitmap API, 159 hwloc_bitmap_fill
Memory binding, 111	The bitmap API, 159
hwloc_alloc_membind_nodeset	hwloc_bitmap_first
Memory binding, 111	The bitmap API, 159
hwloc_alloc_membind_policy Memory binding, 111	hwloc_bitmap_foreach_begin
hwloc_alloc_membind_policy_nodeset	The bitmap API, 155
Memory binding, 112	hwloc_bitmap_foreach_end
hwloc_alloc_setup_object	The bitmap API, 155
Components and Plugins: Core functions to be	hwloc_bitmap_free
used by components, 199	The bitmap API, 159
hwloc_backend, 203	hwloc_bitmap_from_ith_ulong
disable, 203	The bitmap API, 159 hwloc bitmap from ulong
discover, 204	The bitmap API, 160
flags, 204 get_obj_cpuset, 204	hwloc_bitmap_intersects
is_custom, 204	The bitmap API, 160
is_thissystem, 204	hwloc_bitmap_isequal
notify_new_object, 204	The bitmap API, 160
private_data, 205	hwloc_bitmap_isfull
hwloc_backend_alloc	The bitmap API, 160
Components and Plugins: Discovery backends,	hwloc_bitmap_isincluded
196	The bitmap API, 160
hwloc_backend_enable	hwloc_bitmap_isset
Components and Plugins: Discovery backends,	The bitmap API, 161
196 hwloc_backend_flag_e	hwloc_bitmap_iszero The bitmap API, 161
Components and Plugins: Discovery backends,	hwloc_bitmap_last
196	The bitmap API, 161
hwloc_backends_get_obj_cpuset	hwloc_bitmap_list_asprintf

The bitmap API, 161	Components and Plugins: Generic components
hwloc_bitmap_list_snprintf	198
The bitmap API, 162	hwloc_component_type_t
hwloc_bitmap_list_sscanf	Components and Plugins: Generic components
The bitmap API, 162	198
hwloc_bitmap_next	hwloc_const_bitmap_t
The bitmap API, 162	The bitmap API, 156
hwloc_bitmap_not	hwloc_const_cpuset_t
The bitmap API, 162	Object Sets (hwloc_cpuset_t and hwloc_nodeset
hwloc_bitmap_only	_t), 77
The bitmap API, 163	hwloc_const_nodeset_t
hwloc_bitmap_or	Object Sets (hwloc_cpuset_t and hwloc_nodeset
The bitmap API, 163	_t), 77
hwloc_bitmap_set	hwloc_cpubind_flags_t
The bitmap API, 163	CPU binding, 103
hwloc_bitmap_set_ith_ulong	hwloc_cpuset_from_glibc_sched_affinity
The bitmap API, 163	Interoperability with glibc sched affinity, 173
hwloc_bitmap_set_range	hwloc_cpuset_from_linux_libnuma_bitmask
The bitmap API, 163	Interoperability with Linux libnuma bitmask, 171
hwloc_bitmap_singlify	hwloc_cpuset_from_linux_libnuma_ulongs
The bitmap API, 164	Interoperability with Linux libnuma unsigned long
hwloc_bitmap_snprintf	masks, 169
The bitmap API, 164	hwloc_cpuset_from_nodeset
hwloc_bitmap_sscanf	Converting between CPU sets and node sets, 148
The bitmap API, 164	hwloc_cpuset_from_nodeset_strict
hwloc_bitmap_t	Converting between CPU sets and node sets, 148
The bitmap API, 156	hwloc_cpuset_t
hwloc_bitmap_taskset_asprintf	Object Sets (hwloc_cpuset_t and hwloc_nodeset←
The bitmap API, 164	_t), 77
hwloc_bitmap_taskset_snprintf	hwloc_cpuset_to_glibc_sched_affinity
The bitmap API, 165	Interoperability with glibc sched affinity, 173
hwloc_bitmap_taskset_sscanf	hwloc_cpuset_to_linux_libnuma_bitmask
The bitmap API, 165	Interoperability with Linux libnuma bitmask, 171
hwloc_bitmap_to_ith_ulong	hwloc_cpuset_to_linux_libnuma_ulongs
The bitmap API, 165	Interoperability with Linux libnuma unsigned long
hwloc_bitmap_to_ulong	masks, 169
The bitmap API, 165	hwloc_cpuset_to_nodeset
hwloc_bitmap_weight	Converting between CPU sets and node sets, 148
The bitmap API, 166	hwloc_cpuset_to_nodeset_strict
hwloc_bitmap_xor	Converting between CPU sets and node sets, 149
The bitmap API, 166	hwloc_cuda_get_device_cpuset
hwloc_bitmap_zero	Interoperability with the CUDA Driver API, 176
The bitmap API, 166	hwloc_cuda_get_device_osdev
hwloc_bridge_covers_pcibus	Interoperability with the CUDA Driver API, 176
Finding I/O objects, 152	hwloc_cuda_get_device_osdev_by_index
hwloc_compare_types	Interoperability with the CUDA Driver API, 176
Object Types, 81	hwloc_cuda_get_device_pci_ids
	Interoperability with the CUDA Driver API, 177
hwloc_compare_types_e	hwloc_cuda_get_device_pcidev
Object Types, 79	Interoperability with the CUDA Driver API, 177
hwloc_component, 208	hwloc_cudart_get_device_cpuset
abi, 209	Interoperability with the CUDA Runtime API, 178
data, 209	hwloc_cudart_get_device_osdev_by_index
finalize, 209	Interoperability with the CUDA Runtime API, 178
flags, 209	hwloc_cudart_get_device_pci_ids
init, 209	Interoperability with the CUDA Runtime API, 178
type, 210	hwloc_cudart_get_device_pcidev
hwloc_component_type_e	Interoperability with the CUDA Runtime API, 179

hwloc_custom_insert_group_object_by_parent Building Custom Topologies, 122	Looking at Ancestor and Child Objects, 136 hwloc_get_cpubind
hwloc_custom_insert_topology	CPU binding, 104
Building Custom Topologies, 122	hwloc_get_depth_type
hwloc disc component, 210	Object levels, depths and types, 95
excludes, 211	hwloc_get_distance_matrix_covering_obj_by_depth
instantiate, 211	Manipulating Distances, 150
name, 211	hwloc_get_first_largest_obj_inside_cpuset
priority, 211	Finding Objects inside a CPU set, 130
type, 211	hwloc_get_hostbridge_by_pcibus
hwloc_disc_component_type_e	Finding I/O objects, 152
Components and Plugins: Discovery components,	hwloc get largest objs inside cpuset
195	Finding Objects inside a CPU set, 130
hwloc_disc_component_type_t	hwloc_get_last_cpu_location
Components and Plugins: Discovery components,	CPU binding, 104
195	hwloc_get_latency
hwloc_distances_s, 212	Manipulating Distances, 150
latency, 212	hwloc_get_membind
latency_base, 212	Memory binding, 114
latency_max, 213	hwloc_get_membind_nodeset
nbobjs, 213	Memory binding, 114
relative_depth, 213	hwloc_get_nbobjs_by_depth
hwloc_distrib	Object levels, depths and types, 95
Distributing items over a topology, 143	hwloc_get_nbobjs_by_type
hwloc_distrib_flags_e	Object levels, depths and types, 95
Distributing items over a topology, 143	hwloc_get_nbobjs_inside_cpuset_by_depth
hwloc_export_obj_userdata	Finding Objects inside a CPU set, 131
Exporting Topologies to XML, 124	hwloc_get_nbobjs_inside_cpuset_by_type
hwloc_export_obj_userdata_base64	Finding Objects inside a CPU set, 131
Exporting Topologies to XML, 124	hwloc_get_next_bridge
hwloc_fill_object_sets	Finding I/O objects, 152
Components and Plugins: Core functions to be	hwloc_get_next_child
used by components, 200	Looking at Ancestor and Child Objects, 137
hwloc_free	hwloc_get_next_obj_by_depth
Memory binding, 112	Object levels, depths and types, 95
hwloc_free_xmlbuffer	hwloc_get_next_obj_by_type
Exporting Topologies to XML, 125	Object levels, depths and types, 95
hwloc_get_ancestor_obj_by_depth	hwloc_get_next_obj_covering_cpuset_by_depth
Looking at Ancestor and Child Objects, 136	Finding Objects covering at least CPU set, 134
hwloc_get_ancestor_obj_by_type	hwloc_get_next_obj_covering_cpuset_by_type
Looking at Ancestor and Child Objects, 136	Finding Objects covering at least CPU set, 134
hwloc_get_api_version	hwloc_get_next_obj_inside_cpuset_by_depth
API version, 76	Finding Objects inside a CPU set, 131
hwloc_get_area_membind	hwloc_get_next_obj_inside_cpuset_by_type
Memory binding, 112	Finding Objects inside a CPU set, 132
hwloc_get_area_membind_nodeset	hwloc_get_next_osdev
Memory binding, 113	Finding I/O objects, 152
hwloc_get_area_memlocation	hwloc_get_next_pcidev
Memory binding, 113	Finding I/O objects, 153
hwloc_get_cache_covering_cpuset	hwloc_get_non_io_ancestor_obj
Looking at Cache Objects, 138	Finding I/O objects, 153
hwloc_get_cache_type_depth	hwloc_get_numanode_obj_by_os_index
Looking at Cache Objects, 138	Finding objects, miscellaneous helpers, 140
hwloc_get_child_covering_cpuset	hwloc_get_obj_below_array_by_type
Finding Objects covering at least CPU set, 134	Finding objects, miscellaneous helpers, 141
hwloc_get_closest_objs	hwloc_get_obj_below_by_type
Finding objects, miscellaneous helpers, 140	Finding objects, miscellaneous helpers, 141
hwloc_get_common_ancestor_obj	hwloc_get_obj_by_depth

Object levels, depths and types, 96 hwloc_get_obj_by_type Object levels, depths and types, 96	hwloc_insert_object_by_cpuset Components and Plugins: Core functions to be used by components, 200
hwloc_get_obj_covering_cpuset	hwloc_insert_object_by_parent
Finding Objects covering at least CPU set, 135	Components and Plugins: Core functions to be
hwloc_get_obj_index_inside_cpuset	used by components, 200
Finding Objects inside a CPU set, 132	hwloc_insert_pci_device_list
hwloc_get_obj_inside_cpuset_by_depth	Components and Plugins: PCI functions to be used
Finding Objects inside a CPU set, 132	by components, 202
hwloc_get_obj_inside_cpuset_by_type	hwloc_intel_mic_get_device_cpuset
Finding Objects inside a CPU set, 133	Interoperability with Intel Xeon Phi (MIC), 184
hwloc_get_pcidev_by_busid	hwloc_intel_mic_get_device_osdev_by_index
Finding I/O objects, 153	Interoperability with Intel Xeon Phi (MIC), 184
hwloc_get_pcidev_by_busidstring	hwloc_linux_get_tid_cpubind
Finding I/O objects, 153	Linux-specific helpers, 167
hwloc_get_proc_cpubind	hwloc_linux_get_tid_last_cpu_location
CPU binding, 104	Linux-specific helpers, 167
hwloc_get_proc_last_cpu_location	hwloc_linux_parse_cpumap_file
CPU binding, 104	Linux-specific helpers, 167
hwloc_get_proc_membind	hwloc_linux_set_tid_cpubind
Memory binding, 115	Linux-specific helpers, 168
hwloc_get_proc_membind_nodeset	hwloc_membind_flags_t
Memory binding, 116	Memory binding, 109
hwloc_get_pu_obj_by_os_index	hwloc_membind_policy_t
Finding objects, miscellaneous helpers, 141	Memory binding, 109
hwloc_get_root_obj	hwloc_mx_board_get_device_cpuset
Object levels, depths and types, 96	Interoperability with Myrinet Express, 187
hwloc_get_shared_cache_covering_obj	hwloc_mx_endpoint_get_device_cpuset
Looking at Cache Objects, 138	Interoperability with Myrinet Express, 187
hwloc_get_thread_cpubind	hwloc_nodeset_from_linux_libnuma_bitmask
CPU binding, 105	Interoperability with Linux libnuma bitmask, 171
hwloc_get_type_depth	hwloc_nodeset_from_linux_libnuma_ulongs
Object levels, depths and types, 96	Interoperability with Linux libnuma unsigned long
hwloc_get_type_depth_e	masks, 170
Object levels, depths and types, 94	hwloc_nodeset_t
hwloc_get_type_or_above_depth	Object Sets (hwloc_cpuset_t and hwloc_nodeset ←
Object levels, depths and types, 97	_t), 77
hwloc_get_type_or_below_depth	hwloc_nodeset_to_linux_libnuma_bitmask
Object levels, depths and types, 97	Interoperability with Linux libnuma bitmask, 172
hwloc_get_whole_distance_matrix_by_depth	hwloc_nodeset_to_linux_libnuma_ulongs
Manipulating Distances, 150	Interoperability with Linux libnuma unsigned long
hwloc_get_whole_distance_matrix_by_type	masks, 170
Manipulating Distances, 151	hwloc_nvml_get_device_cpuset
hwloc_gl_get_display_by_osdev	Interoperability with the NVIDIA Management Li-
Interoperability with OpenGL displays, 182	brary, 180
hwloc_gl_get_display_osdev_by_name	hwloc_nvml_get_device_osdev
Interoperability with OpenGL displays, 182	Interoperability with the NVIDIA Management Li-
hwloc_gl_get_display_osdev_by_port_device	brary, 180
Interoperability with OpenGL displays, 182	hwloc_nvml_get_device_osdev_by_index
hwloc_hide_errors	Interoperability with the NVIDIA Management Li-
Components and Plugins: Core functions to be	brary, 180
used by components, 200	hwloc_obj, 214
hwloc_ibv_get_device_cpuset	allowed_cpuset, 215
Interoperability with OpenFabrics, 185	allowed_nodeset, 215
hwloc_ibv_get_device_osdev	arity, 215
Interoperability with OpenFabrics, 185	attr, 215
hwloc_ibv_get_device_osdev_by_name	children, 216
Interoperability with OpenFabrics, 185	complete_cpuset, 216

complete_nodeset, 216	bus, 226
cpuset, 216	class_id, 227
depth, 217	dev, 227
distances, 217	device_id, 227
distances_count, 217	domain, 227
first_child, 217	func, 227
infos, 217	linkspeed, 227
infos_count, 218	revision, 227
last_child, 218	subdevice_id, 227
logical_index, 218	subvendor_id, 228
memory, 218	vendor_id, 228
name, 218	hwloc_obj_bridge_type_e
next_cousin, 218	Object Types, 79
next_sibling, 219	hwloc_obj_bridge_type_t
nodeset, 219	Object Types, 78
online_cpuset, 219	hwloc_obj_cache_type_e
os_index, 219	Object Types, 79
os_level, 220	hwloc_obj_cache_type_t
parent, 220	Object Types, 78
prev_cousin, 220	hwloc_obj_cpuset_snprintf
prev sibling, 220	Converting between Object Types, Sets and At-
sibling_rank, 220	tributes, and Strings, 98
symmetric_subtree, 220	hwloc_obj_get_info_by_name
type, 221	Consulting and Adding Key-Value Info Attributes,
userdata, 221	101
hwloc_obj_add_info	hwloc_obj_info_s, 222
Consulting and Adding Key-Value Info Attributes,	name, 223
101	value, 223
hwloc_obj_attr_snprintf	hwloc_obj_is_in_subtree
Converting between Object Types, Sets and At-	Looking at Ancestor and Child Objects, 137
tributes, and Strings, 98	hwloc_obj_memory_s, 224
hwloc_obj_attr_u, 221	local_memory, 225
bridge, 222	_ •
cache, 222	page_types, 225
	page_types_len, 225
group, 222	total_memory, 225
osdev, 222	hwloc_obj_memory_s::hwloc_obj_memory_page_
poidev, 222	type_s, 223
hwloc_obj_attr_u::hwloc_bridge_attr_s, 205	count, 224
depth, 206	size, 224
domain, 206	hwloc_obj_osdev_type_e
downstream, 206	Object Types, 80
downstream_type, 206	hwloc_obj_osdev_type_t
pci, 206	Object Types, 78
secondary_bus, 206	hwloc_obj_t
subordinate_bus, 206	Object Structure and Attributes, 82
upstream, 207	hwloc_obj_type_snprintf
upstream_type, 207	Converting between Object Types, Sets and At-
hwloc_obj_attr_u::hwloc_cache_attr_s, 207	tributes, and Strings, 98
associativity, 207	hwloc_obj_type_sscanf
depth, 207	Converting between Object Types, Sets and At-
linesize, 208	tributes, and Strings, 99
size, 208	hwloc_obj_type_string
type, 208	Converting between Object Types, Sets and At-
hwloc_obj_attr_u::hwloc_group_attr_s, 213	tributes, and Strings, 99
depth, 214	hwloc_obj_type_t
hwloc_obj_attr_u::hwloc_osdev_attr_s, 225	Object Types, 80
type, 226	hwloc_opencl_get_device_cpuset
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 226	Interoperability with OpenCL, 174

hwloc_opencl_get_device_osdev	hwloc_topology_diff_apply
Interoperability with OpenCL, 174	Topology differences, 191
hwloc_opencl_get_device_osdev_by_index	hwloc_topology_diff_apply_flags_e
Interoperability with OpenCL, 174	Topology differences, 189
hwloc_pci_find_cap	hwloc_topology_diff_build
Components and Plugins: PCI functions to be used	Topology differences, 192
by components, 202	hwloc_topology_diff_destroy
hwloc_pci_find_linkspeed	Topology differences, 192
Components and Plugins: PCI functions to be used	hwloc_topology_diff_export_xml
by components, 202	Topology differences, 192
hwloc_pci_prepare_bridge	hwloc_topology_diff_export_xmlbuffer
Components and Plugins: PCI functions to be used	Topology differences, 193
by components, 202	hwloc_topology_diff_load_xml
hwloc_plugin_check_namespace Components and Plugins: Core functions to be	Topology differences, 193
•	hwloc_topology_diff_load_xmlbuffer
used by components, 201	Topology differences, 193
hwloc_report_error_t	hwloc_topology_diff_obj_attr_type_e
Components and Plugins: Core functions to be	Topology differences, 189
used by components, 199	hwloc_topology_diff_obj_attr_type_t
hwloc_report_os_error Components and Plugins: Core functions to be	Topology differences, 189
	hwloc_topology_diff_obj_attr_u, 233
used by components, 201 hwloc_restrict_flags_e	generic, 234
	string, 234
Modifying a loaded Topology, 119	uint64, 234
hwloc_set_area_membind	hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_
Memory binding, 116	obj_attr_generic_s, 231
hwloc_set_area_membind_nodeset Memory binding, 116	type, 231
	hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_
hwloc_set_cpubind	obj_attr_string_s, 232
CPU binding, 105	name, 233
hwloc_set_membind	newvalue, 233
Memory binding, 117	oldvalue, 233
hwloc_set_membind_nodeset	type, 233
Memory binding, 117	hwloc_topology_diff_obj_attr_u::hwloc_topology_diff_
hwloc_set_proc_cpubind	obj_attr_uint64_s, 235
CPU binding, 105	index, 235
hwloc_set_proc_membind	newvalue, 235
Memory binding, 117	oldvalue, 235
hwloc_set_proc_membind_nodeset	type, 235
Memory binding, 118	hwloc_topology_diff_t
hwloc_set_thread_cpubind	Topology differences, 189
CPU binding, 106	hwloc_topology_diff_type_e
hwloc_topology_check Topology Creation and Destruction, 83	Topology differences, 191 hwloc_topology_diff_type_t
hwloc_topology_cpubind_support, 228	Topology differences, 189
get proc cpubind, 229	
get_proc_last_cpu_location, 229	hwloc_topology_diff_u, 236
get_bioc_last_cpu_location, 229 get_thisproc_cpubind, 229	generic, 237
get_thisproc_last_cpu_location, 229	obj_attr, 237
get_thisthread_cpubind, 229	too_complex, 237
get_thisthread_last_cpu_location, 229	hwloc_topology_diff_u::hwloc_topology_diff_generic_s, 230
get_thread_cpubind, 229 set_proc_cpubind, 229	next, 230
	type, 230
set_thisproc_cpubind, 230	hwloc_topology_diff_u::hwloc_topology_diff_obj_attr_s, 231
set_thisthread_cpubind, 230	diff, 232
set_thread_cpubind, 230 hwloc_topology_destroy	
Topology Creation and Destruction, 84	next, 232
Topology Greation and Destruction, 64	obj_depth, 232

obj_index, 232	hwloc_topology_load
type, 232	Topology Creation and Destruction, 85
$hwloc_topology_diff_u::hwloc_topology_diff_too_{\hookleftarrow}$	hwloc_topology_membind_support, 238
complex_s, 236	alloc_membind, 239
next, 236	bind_membind, 239
obj_depth, 236	firsttouch_membind, 239
obj_index, 236	get_area_membind, 239
type, 236	get_area_memlocation, 239
hwloc_topology_discovery_support, 237	get_proc_membind, 239
pu, 238	get_thisproc_membind, 239
hwloc_topology_dup	get_thisthread_membind, 240
Topology Creation and Destruction, 84	interleave_membind, 240
hwloc_topology_export_synthetic	migrate_membind, 240
Exporting Topologies to Synthetic, 128	nexttouch_membind, 240
hwloc_topology_export_synthetic_flags_e	replicate_membind, 240
Exporting Topologies to Synthetic, 128	set_area_membind, 240
hwloc_topology_export_xml	set_proc_membind, 240
Exporting Topologies to XML, 125	set_thisproc_membind, 240
hwloc_topology_export_xmlbuffer	set_thisthread_membind, 241
Exporting Topologies to XML, 125	hwloc_topology_restrict
hwloc_topology_flags_e	Modifying a loaded Topology, 120
Topology Detection Configuration and Query, 87	hwloc_topology_set_custom
hwloc_topology_get_allowed_cpuset	Topology Detection Configuration and Query, 90
CPU and node sets of entire topologies, 145	hwloc_topology_set_distance_matrix
hwloc_topology_get_allowed_nodeset	Topology Detection Configuration and Query, 90
CPU and node sets of entire topologies, 145	hwloc_topology_set_flags
hwloc_topology_get_complete_cpuset	Topology Detection Configuration and Query, 90
CPU and node sets of entire topologies, 145	hwloc_topology_set_fsroot
hwloc_topology_get_complete_nodeset	Topology Detection Configuration and Query, 91
CPU and node sets of entire topologies, 146	hwloc_topology_set_pid
hwloc_topology_get_depth	Topology Detection Configuration and Query, 91
Object levels, depths and types, 97	hwloc_topology_set_synthetic
hwloc_topology_get_flags	Topology Detection Configuration and Query, 91
Topology Detection Configuration and Query, 88	hwloc_topology_set_userdata
hwloc_topology_get_online_cpuset	Topology Detection Configuration and Query, 92
CPU and node sets of entire topologies, 146	hwloc_topology_set_userdata_export_callback Exporting Topologies to XML, 126
hwloc_topology_get_support	hwloc_topology_set_userdata_import_callback
Topology Detection Configuration and Query, 88	Exporting Topologies to XML, 126
hwloc_topology_get_topology_cpuset	hwloc topology set xml
CPU and node sets of entire topologies, 146	Topology Detection Configuration and Query, 92
hwloc_topology_get_topology_nodeset	hwloc_topology_set_xmlbuffer
CPU and node sets of entire topologies, 147	Topology Detection Configuration and Query, 93
hwloc_topology_get_userdata	hwloc_topology_support, 241
Topology Detection Configuration and Query, 89	cpubind, 241
hwloc_topology_ignore_all_keep_structure	discovery, 241
Topology Detection Configuration and Query, 89	membind, 241
hwloc_topology_ignore_type	hwloc_topology_t
Topology Detection Configuration and Query, 89	Topology Creation and Destruction, 83
hwloc_topology_ignore_type_keep_structure	reportegy enduliers and 2 contaction, co
Topology Detection Configuration and Query, 89	index
hwloc_topology_init	hwloc_topology_diff_obj_attr_u::hwloc_topology-
Topology Creation and Destruction, 84	_diff_obj_attr_uint64_s, 235
hwloc_topology_insert_misc_object_by_cpuset	infos
Modifying a loaded Topology, 119	hwloc_obj, 217
hwloc_topology_insert_misc_object_by_parent	infos_count
Modifying a loaded Topology, 120	hwloc_obj, 218
hwloc_topology_is_thissystem	init
Topology Detection Configuration and Query, 89	hwloc_component, 209

instantiate	latency
hwloc_disc_component, 211	hwloc_distances_s, 212
interleave_membind	latency_base
hwloc_topology_membind_support, 240	hwloc_distances_s, 212
Interoperability with glibc sched affinity, 173	latency_max
hwloc_cpuset_from_glibc_sched_affinity, 173	hwloc_distances_s, 213
hwloc_cpuset_to_glibc_sched_affinity, 173	linesize
Interoperability with Intel Xeon Phi (MIC), 184	hwloc_obj_attr_u::hwloc_cache_attr_s, 208
hwloc_intel_mic_get_device_cpuset, 184	linkspeed
hwloc_intel_mic_get_device_osdev_by_index, 184	hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227
Interoperability with Linux libnuma bitmask, 171	Linux-specific helpers, 167
hwloc_cpuset_from_linux_libnuma_bitmask, 171	hwloc_linux_get_tid_cpubind, 167
hwloc_cpuset_to_linux_libnuma_bitmask, 171	hwloc_linux_get_tid_last_cpu_location, 167
hwloc_nodeset_from_linux_libnuma_bitmask, 171	hwloc_linux_parse_cpumap_file, 167
hwloc_nodeset_to_linux_libnuma_bitmask, 172	hwloc_linux_set_tid_cpubind, 168
Interoperability with Linux libnuma unsigned long masks,	local_memory
169	hwloc_obj_memory_s, 225
hwloc_cpuset_from_linux_libnuma_ulongs, 169	logical_index
hwloc_cpuset_to_linux_libnuma_ulongs, 169	hwloc_obj, 218
hwloc_nodeset_from_linux_libnuma_ulongs, 170	Looking at Ancestor and Child Objects, 136
hwloc_nodeset_to_linux_libnuma_ulongs, 170	hwloc_get_ancestor_obj_by_depth, 136
Interoperability with Myrinet Express, 187	hwloc_get_ancestor_obj_by_type, 136
hwloc_mx_board_get_device_cpuset, 187	hwloc_get_common_ancestor_obj, 136
hwloc_mx_endpoint_get_device_cpuset, 187	hwloc_get_next_child, 137
Interoperability with OpenCL, 174	hwloc_obj_is_in_subtree, 137
hwloc_opencl_get_device_cpuset, 174	Looking at Cache Objects, 138
hwloc_opencl_get_device_osdev, 174	hwloc_get_cache_covering_cpuset, 138
hwloc_opencl_get_device_osdev_by_index, 174	hwloc_get_cache_type_depth, 138
Interoperability with OpenFabrics, 185	hwloc_get_shared_cache_covering_obj, 138
hwloc_ibv_get_device_cpuset, 185 hwloc_ibv_get_device_osdev, 185	Manipulating Distances, 150
hwloc_ibv_get_device_osdev_by_name, 185	hwloc_get_distance_matrix_covering_obj_by_←
Interoperability with OpenGL displays, 182	depth, 150
hwloc_gl_get_display_by_osdev, 182	hwloc get latency, 150
hwloc_gl_get_display_osdev_by_name, 182	hwloc_get_whole_distance_matrix_by_depth, 150
hwloc_gl_get_display_osdev_by_port_device, 182	hwloc_get_whole_distance_matrix_by_type, 151
Interoperability with the CUDA Driver API, 176	membind
hwloc_cuda_get_device_cpuset, 176	hwloc_topology_support, 241
hwloc cuda get device osdev, 176	memory
hwloc_cuda_get_device_osdev_by_index, 176	hwloc_obj, 218
hwloc_cuda_get_device_pci_ids, 177	Memory binding, 107
hwloc_cuda_get_device_pcidev, 177	hwloc_alloc, 110
Interoperability with the CUDA Runtime API, 178	hwloc_alloc_membind, 111
hwloc_cudart_get_device_cpuset, 178	hwloc_alloc_membind_nodeset, 111
hwloc_cudart_get_device_osdev_by_index, 178	hwloc_alloc_membind_policy, 111
hwloc_cudart_get_device_pci_ids, 178	hwloc_alloc_membind_policy_nodeset, 112
hwloc_cudart_get_device_pcidev, 179	hwloc_free, 112
Interoperability with the NVIDIA Management Library,	hwloc_get_area_membind, 112
180	hwloc_get_area_membind_nodeset, 113
hwloc_nvml_get_device_cpuset, 180	hwloc_get_area_memlocation, 113
hwloc_nvml_get_device_osdev, 180	hwloc_get_membind, 114
hwloc_nvml_get_device_osdev_by_index, 180	hwloc_get_membind_nodeset, 114
is_custom	hwloc_get_proc_membind, 115
hwloc_backend, 204	hwloc_get_proc_membind_nodeset, 116
is_thissystem	hwloc_membind_flags_t, 109
hwloc_backend, 204	hwloc_membind_policy_t, 109
lost shild	hwloc_set_area_membind, 116
last_child	hwloc_set_area_membind_nodeset, 116
hwloc_obj, 218	hwloc_set_membind, 117

hwloc_set_membind_nodeset, 117 hwloc_set_proc_membind, 117 hwloc_set_proc_membind_nodeset, 118 migrate_membind hwloc_topology_membind_support, 240	hwloc_get_nbobjs_by_type, 95 hwloc_get_next_obj_by_depth, 95 hwloc_get_next_obj_by_type, 95 hwloc_get_obj_by_depth, 96 hwloc_get_obj_by_type, 96
Modifying a loaded Topology, 119	hwloc_get_root_obj, 96
hwloc_restrict_flags_e, 119	hwloc_get_type_depth, 96
hwloc_topology_insert_misc_object_by_cpuset,	hwloc_get_type_depth_e, 94
119	hwloc_get_type_or_above_depth, 97
	hwloc_get_type_or_below_depth, 97
hwloc_topology_insert_misc_object_by_parent,	
120	hwloc_topology_get_depth, 97
hwloc_topology_restrict, 120	Object Sets (hwloc_cpuset_t and hwloc_nodeset_t), 77
nama	hwloc_const_cpuset_t, 77
name	hwloc_const_nodeset_t, 77
hwloc_disc_component, 211	hwloc_cpuset_t, 77
hwloc_obj, 218	hwloc_nodeset_t, 77
hwloc_obj_info_s, 223	Object Structure and Attributes, 82
hwloc_topology_diff_obj_attr_u::hwloc_topology↔	hwloc_obj_t, 82
_diff_obj_attr_string_s, 233	Object Types, 78
nbobjs	hwloc_compare_types, 81
hwloc_distances_s, 213	hwloc_compare_types_e, 79
newvalue	hwloc_obj_bridge_type_e, 79
hwloc_topology_diff_obj_attr_u::hwloc_topology←	hwloc_obj_bridge_type_t, 78
_diff_obj_attr_string_s, 233	hwloc_obj_cache_type_e, 79
hwloc_topology_diff_obj_attr_u::hwloc_topology ←	hwloc_obj_cache_type_t, 78
_diff_obj_attr_uint64_s, 235	hwloc_obj_osdev_type_e, 80
next	hwloc_obj_osdev_type_t, 78
hwloc_topology_diff_u::hwloc_topology_diff_←	hwloc_obj_type_t, 80
generic_s, 230	oldvalue
hwloc_topology_diff_u::hwloc_topology_diff_obj←	hwloc_topology_diff_obj_attr_u::hwloc_topology↔
_attr_s, 232	_diff_obj_attr_string_s, 233
hwloc_topology_diff_u::hwloc_topology_diff_too↔	hwloc_topology_diff_obj_attr_u::hwloc_topology
_complex_s, 236	diff obj attr uint64 s, 235
next_cousin	online_cpuset
hwloc_obj, 218	
next sibling	hwloc_obj, 219
hwloc_obj, 219	os_index
_ •	hwloc_obj, 219
nexttouch_membind	os_level
hwloc_topology_membind_support, 240	hwloc_obj, 220
nodeset	osdev
hwloc_obj, 219	hwloc_obj_attr_u, 222
notify_new_object	nage types
hwloc_backend, 204	page_types
alat auto	hwloc_obj_memory_s, 225
obj_attr	page_types_len
hwloc_topology_diff_u, 237	hwloc_obj_memory_s, 225
obj_depth	parent
hwloc_topology_diff_u::hwloc_topology_diff_obj ←	hwloc_obj, 220
_attr_s, 232	pci
hwloc_topology_diff_u::hwloc_topology_diff_too ←	hwloc_obj_attr_u::hwloc_bridge_attr_s, 206
_complex_s, 236	pcidev
obj_index	hwloc_obj_attr_u, 222
hwloc_topology_diff_u::hwloc_topology_diff_obj←	prev_cousin
_attr_s, 232	hwloc_obj, 220
hwloc_topology_diff_u::hwloc_topology_diff_too←	prev_sibling
	hwloc_obj, 220
Object levels, depths and types, 94	priority
hwloc_get_depth_type, 95	hwloc_disc_component, 211
hwloc_get_nbobjs_by_depth, 95	private data
	L-:::

hwloc_backend, 205	hwloc_bitmap_fill, 159
pu	hwloc_bitmap_first, 159
hwloc_topology_discovery_support, 238	hwloc_bitmap_foreach_begin, 155
relative_depth	hwloc_bitmap_foreach_end, 155
hwloc_distances_s, 213	hwloc_bitmap_free, 159
replicate_membind	hwloc_bitmap_from_ith_ulong, 159
hwloc_topology_membind_support, 240	hwloc_bitmap_from_ulong, 160
revision	hwloc_bitmap_intersects, 160
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227	hwloc_bitmap_isequal, 160
nwioc_obj_atti_dnwioc_bcidev_atti_3, 227	hwloc_bitmap_isfull, 160
secondary bus	hwloc_bitmap_isincluded, 160
hwloc_obj_attr_u::hwloc_bridge_attr_s, 206	hwloc_bitmap_isset, 161
set_area_membind	hwloc_bitmap_iszero, 161
hwloc_topology_membind_support, 240	hwloc_bitmap_last, 161
set_proc_cpubind	hwloc_bitmap_list_asprintf, 161
hwloc_topology_cpubind_support, 229	hwloc_bitmap_list_snprintf, 162
set_proc_membind	hwloc_bitmap_list_sscanf, 162
hwloc_topology_membind_support, 240	hwloc_bitmap_next, 162
set thisproc cpubind	hwloc_bitmap_not, 162
	hwloc_bitmap_only, 163
hwloc_topology_cpubind_support, 230 set thisproc membind	hwloc_bitmap_or, 163
hwloc_topology_membind_support, 240	hwloc_bitmap_set, 163
set thisthread cpubind	hwloc_bitmap_set_ith_ulong, 163
	hwloc_bitmap_set_range, 163
hwloc_topology_cpubind_support, 230	hwloc_bitmap_singlify, 164
set_thisthread_membind	hwloc_bitmap_snprintf, 164
hwloc_topology_membind_support, 241	hwloc_bitmap_sscanf, 164
set_thread_cpubind	hwloc_bitmap_t, 156
hwloc_topology_cpubind_support, 230	hwloc_bitmap_taskset_asprintf, 164
sibling_rank	hwloc_bitmap_taskset_snprintf, 165
hwloc_obj, 220	hwloc_bitmap_taskset_sscanf, 165
size	hwloc_bitmap_to_ith_ulong, 165
hwloc_obj_attr_u::hwloc_cache_attr_s, 208	hwloc_bitmap_to_ulong, 165
hwloc_obj_memory_s::hwloc_obj_memory_←	hwloc_bitmap_weight, 166
page_type_s, 224	hwloc_bitmap_xor, 166
string	hwloc_bitmap_zero, 166
hwloc_topology_diff_obj_attr_u, 234	hwloc_const_bitmap_t, 156
subdevice_id	too complex
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 227	hwloc_topology_diff_u, 237
subordinate_bus	Topology Creation and Destruction, 83
hwloc_obj_attr_u::hwloc_bridge_attr_s, 206	hwloc topology check, 83
subvendor_id	•,
hwloc_obj_attr_u::hwloc_pcidev_attr_s, 228	hwloc_topology_destroy, 84
symmetric_subtree	hwloc_topology_dup, 84
hwloc_obj, 220	hwloc_topology_init, 84
	hwloc_topology_load, 85
The bitmap API, 154	hwloc_topology_t, 83
hwloc_bitmap_allbut, 156	Topology Detection Configuration and Query, 86
hwloc_bitmap_alloc, 156	hwloc_topology_flags_e, 87
hwloc_bitmap_alloc_full, 157	hwloc_topology_get_flags, 88
hwloc_bitmap_and, 157	hwloc_topology_get_support, 88
hwloc_bitmap_andnot, 157	hwloc_topology_get_userdata, 89
hwloc_bitmap_asprintf, 157	hwloc_topology_ignore_all_keep_structure, 89
hwloc_bitmap_clr, 157	hwloc_topology_ignore_type, 89
hwloc_bitmap_clr_range, 158	hwloc_topology_ignore_type_keep_structure, 89
hwloc_bitmap_compare, 158	hwloc_topology_is_thissystem, 89
hwloc_bitmap_compare_first, 158	hwloc_topology_set_custom, 90
hwloc_bitmap_copy, 158	hwloc_topology_set_distance_matrix, 90
hwloc_bitmap_dup, 159	hwloc_topology_set_flags, 90
	· · · · · · · · · · · · · · · · · · ·

```
hwloc_topology_set_fsroot, 91
     hwloc_topology_set_pid, 91
     hwloc_topology_set_synthetic, 91
     hwloc_topology_set_userdata, 92
    hwloc_topology_set_xml, 92
     hwloc topology set xmlbuffer, 93
Topology differences, 188
     hwloc_topology_diff_apply, 191
     hwloc topology diff apply flags e, 189
     hwloc topology diff build, 192
     hwloc_topology_diff_destroy, 192
     hwloc_topology_diff_export_xml, 192
    hwloc_topology_diff_export_xmlbuffer, 193
     hwloc_topology_diff_load_xml, 193
     hwloc_topology_diff_load_xmlbuffer, 193
     hwloc_topology_diff_obj_attr_type_e, 189
     hwloc topology diff obj attr type t, 189
     hwloc topology diff t, 189
     hwloc_topology_diff_type_e, 191
    hwloc_topology_diff_type_t, 189
total memory
     hwloc_obj_memory_s, 225
type
     hwloc_component, 210
     hwloc disc component, 211
     hwloc_obj, 221
     hwloc_obj_attr_u::hwloc_cache_attr_s, 208
    hwloc obj attr u::hwloc osdev attr s, 226
     hwloc topology diff obj attr u::hwloc topology←
          _diff_obj_attr_generic_s, 231
    hwloc_topology_diff_obj_attr_u::hwloc_topology←
          _diff_obj_attr_string_s, 233
    hwloc_topology_diff_obj_attr_u::hwloc_topology ~
         _diff_obj_attr_uint64_s, 235
    hwloc_topology_diff_u::hwloc_topology_diff_←
         generic_s, 230
    hwloc_topology_diff_u::hwloc_topology_diff_obj←
          _attr_s, 232
    hwloc_topology_diff_u::hwloc_topology_diff_too←
         complex s, 236
uint64
     hwloc topology diff obj attr u, 234
upstream
     hwloc_obj_attr_u::hwloc_bridge_attr_s, 207
upstream_type
     hwloc_obj_attr_u::hwloc_bridge_attr_s, 207
userdata
    hwloc_obj, 221
value
     hwloc_obj_info_s, 223
vendor id
     hwloc obj attr u::hwloc pcidev attr s, 228
```