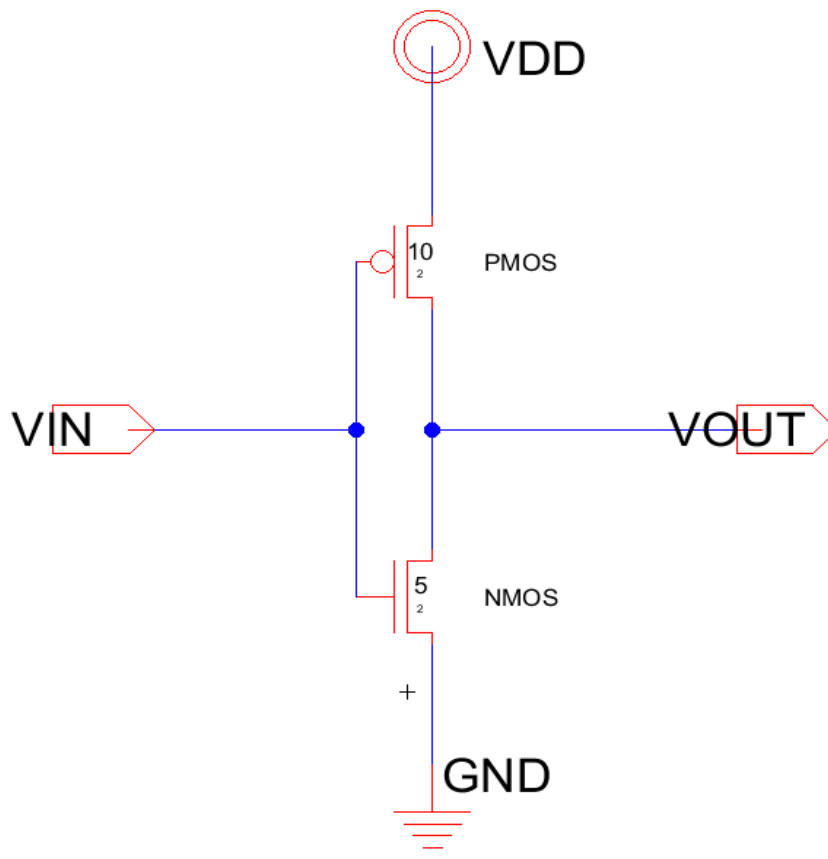


CMOS INVERTER LAYOUT USING ELECTRIC

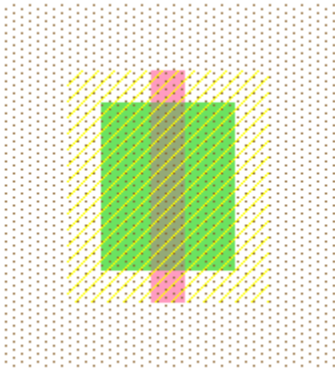
CMOS inverter is a device that is used to generate logic functions is known as CMOS inverter and is the essential component in all integrated circuits. PMOS width is kept twice that of NMOS, as mobility of electrons is twice that of holes and to make rise and fall time equal.

SCHEMATIC:

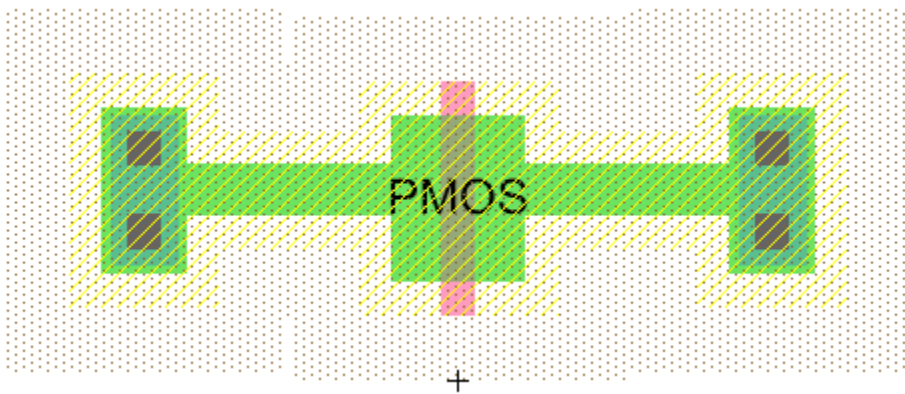


LAYOUT:

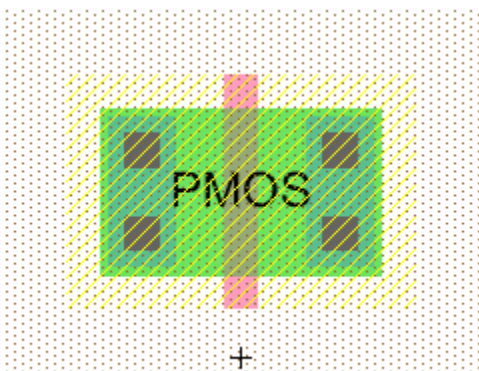
1).PMOS:



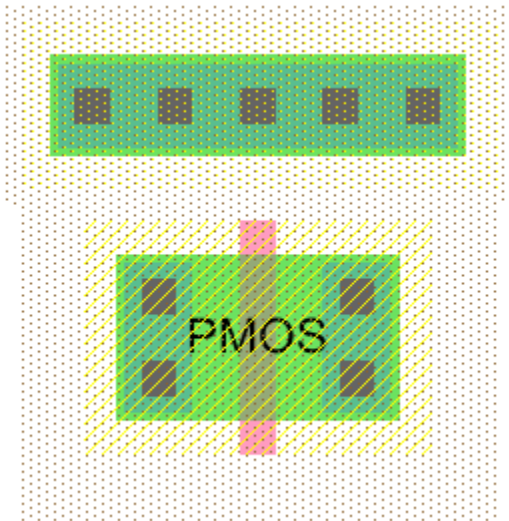
2).Adding p-active



Adjusting p-act as close as possible

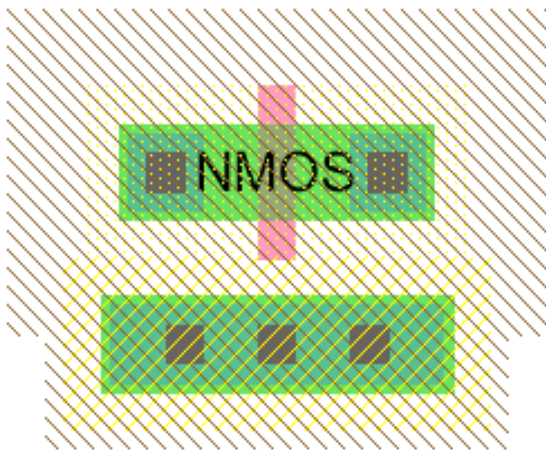


3). Adding p-well and p-act

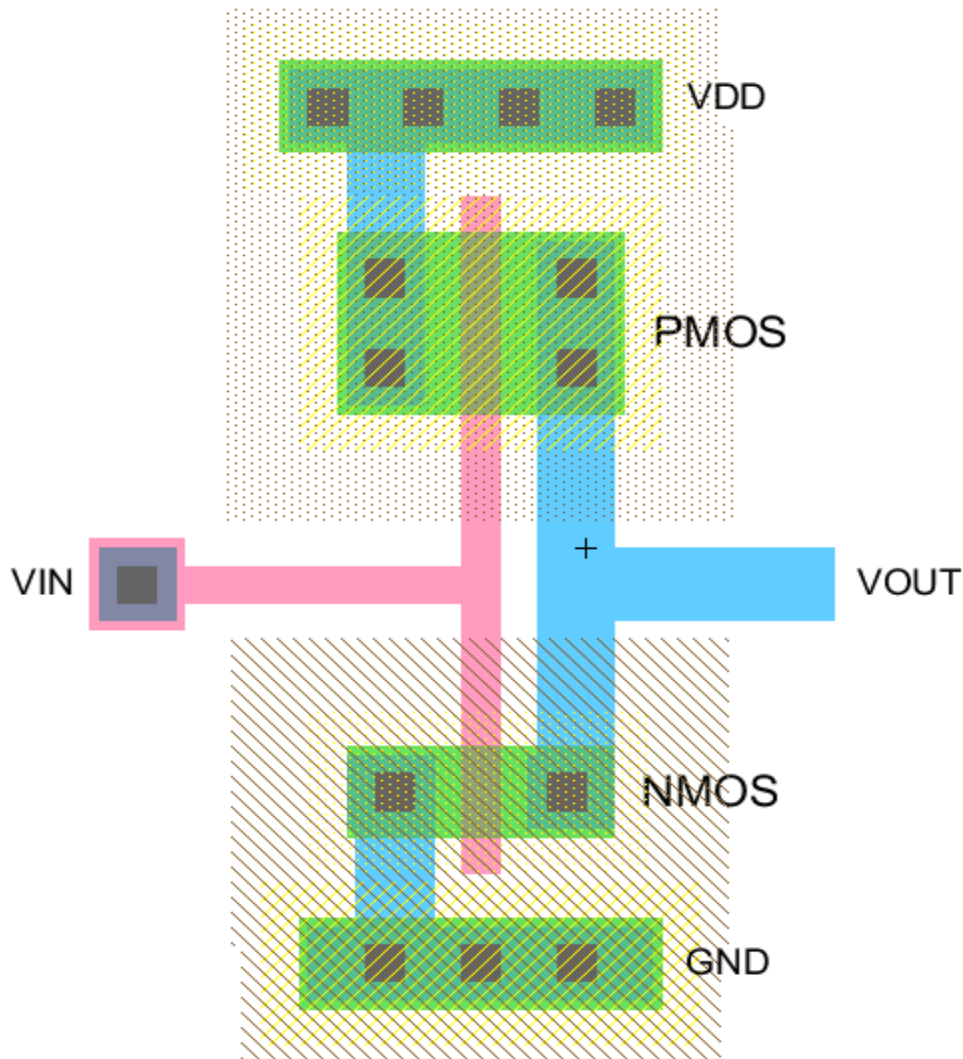


4). NMOS

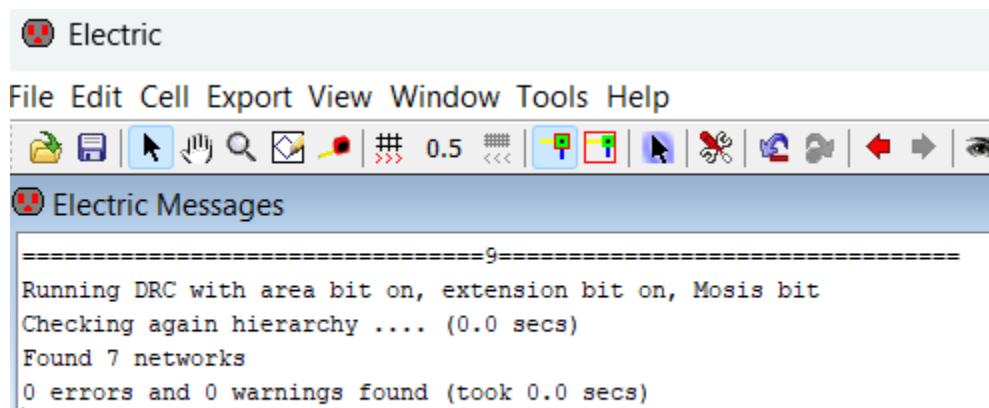
Adding n-active and n-well



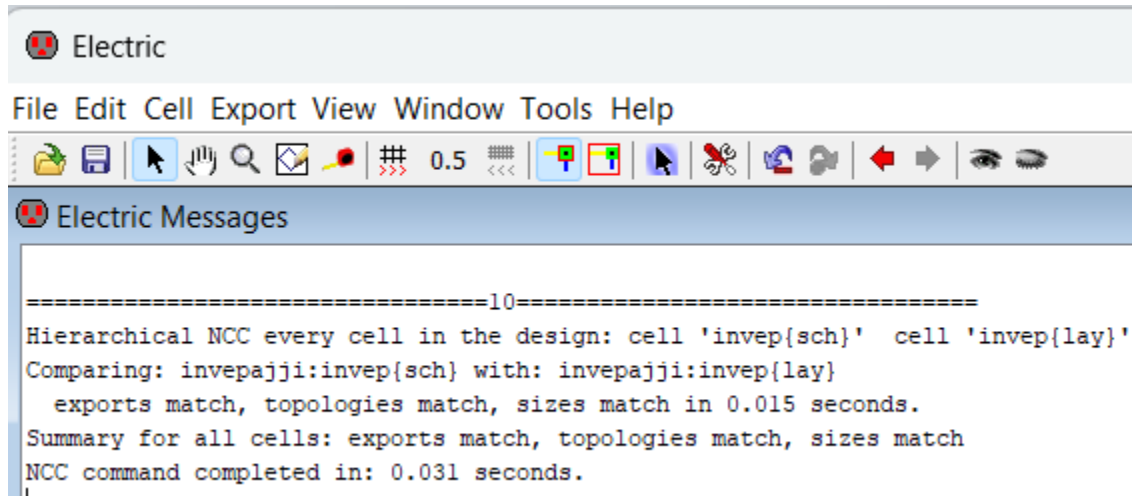
5).Adding Metal contact and Polysilicon



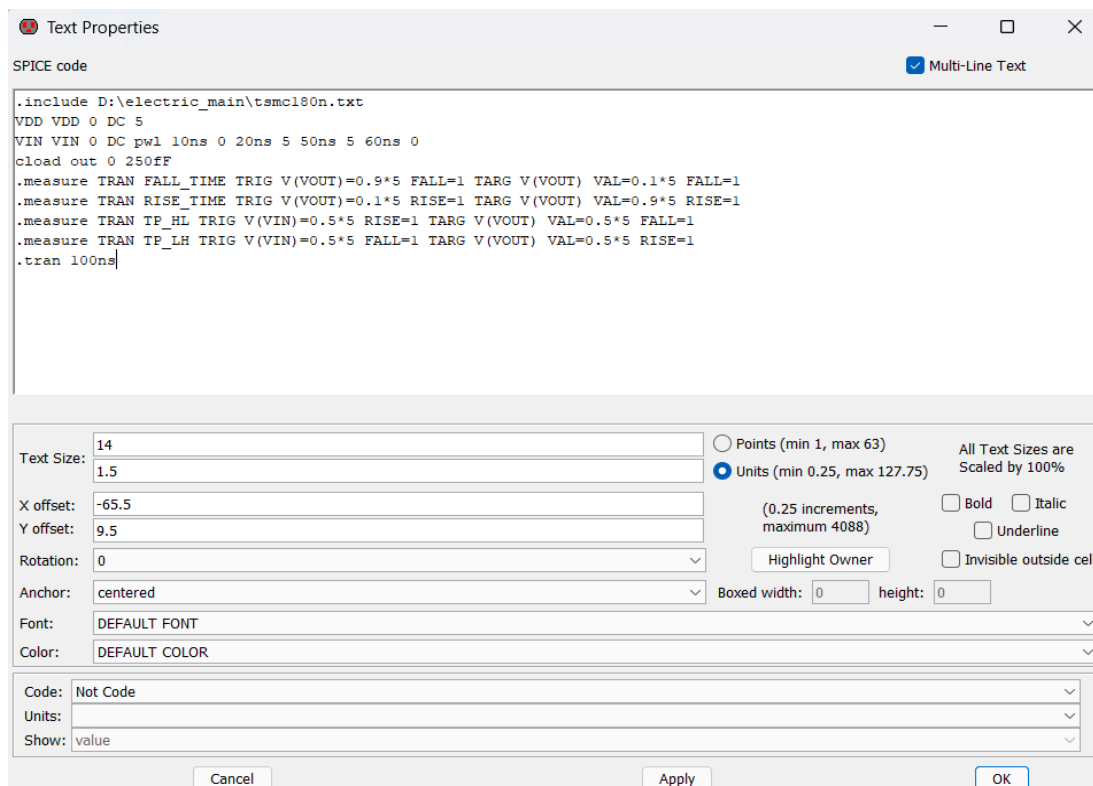
6).Checking DRC



7).Layout V/s Schematic:



8).Adding technology file



9).OUTPUT ON LT-SPICE

